

4MEGA BIT (262,144 WORD×16BIT / 524,288 WORD×8BIT)
CMOS U.V. ERASABLE AND ELECTRICALLY PROGRAMMABLE READ ONLY MEMORY

DESCRIPTION

The TC574200D is a 4,194,304 bit CMOS ultraviolet light erasable and electrically programmable read only memory. It is organized as 256K words of 16 bit or 512K words of 8 bit.

The TC574200D is compatible with 40 pin 4M bit Mask ROM. This product is packed in 40 pin standard cerdip package. The TC574200D is fabricated with the CMOS technology. Advanced circuit techniques provide both high speed and low power features with access time of 100ns/120ns/150ns and a maximum operating current of 70mA/10MHz. The programming time of the TC574200D except overhead times of EPROM programmer is only 28 seconds by using the high speed programming algorithm.

FEATURES

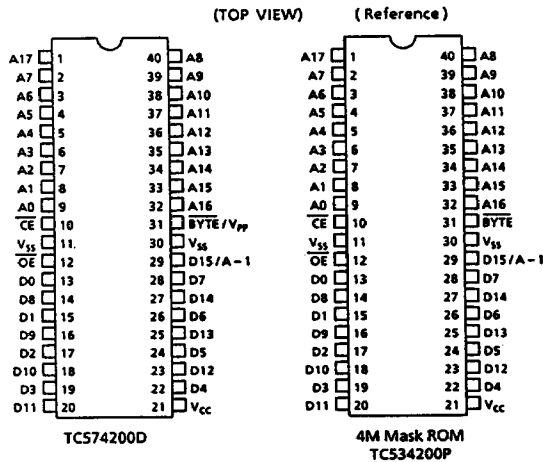
- Peripheral circuit : CMOS
- Memory cell : NMOS
- Fast access time
- Low power
- Standby dissipation : 100 μ A
- Full static operation
- Input and output TTL compatible
- Three state output
- High speed programming operation : tpw 50 μ s
- 4M MROM compatible pinout : TC534200P
- Standard 40 pin DIP cerdip package : WDIP40-P-600B

	- 10	- 120	- 150
V _{CC}	5V \pm 5%	5V \pm 10%	
Temp	0°C ~ 70°C		
T _{acc}	100ns	120ns	150ns

- 10 satisfies characteristics of - 120.

- Single 5V power supply

PIN CONNECTION

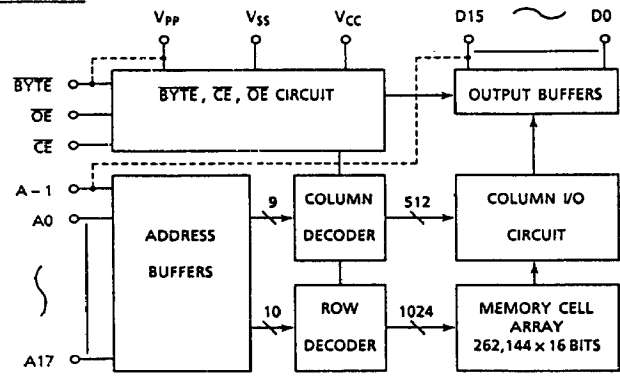


PIN NAMES

A0~A17	Address Input
D0~D14	Output (Input)
CE	Chip Enable Input
OE	Output Enable Input
D15/A-1	Output (Input) / Address Input
BYTE/V _{PP}	Word, Byte select Input / Program Supply Voltage
V _{CC}	V _{CC} Supply Voltage
V _{SS}	Ground

TC574200D-10, TC574200D-120
TC574200D-150

BLOCK DIAGRAM



MODE SELECTION

PINS		CE	OE	BYTE /V _{PP}	V _{CC}	D0~D7	D8~D14	D15 /A-1	Power	
MODE										
Read (16 Bit)	L	L	H	5V	Data Out				Active	
Read (Lower 8 Bit)	L	L	L		Data Out (Lower 8 Bit)	High Impedance	L			
Read (Upper 8 Bit)	L	L	L		Data Out (Upper 8 Bit)	High Impedance	H			
Output Deselect	L	H	H		High Impedance					
			L		High Impedance		*			
Standby	H	*	H	6.25V	High Impedance				Standby	
			L		High Impedance		*			
Program	L	H	12.5V		Data In					Active
Program Inhibit	H	H			High Impedance					
Program Verify	*	L			Data Out					

Note: H = V_{IH}, L = V_{IL}, * = V_{IH} or V_{IL}

MAXIMUM RATINGS

SYMBOL	ITEM	RATING	UNIT
V _{CC}	V _{CC} Power Supply Voltage	-0.6~7.0	V
V _{PP}	Program Supply Voltage	-0.6~14.0	V
V _{IN}	Input Voltage	-0.6~7.0	V
V _{IN (A9)}	Input Voltage (A9)	-0.6~13.5	V
V _{I/O}	Input/Output Voltage	-0.6~V _{CC} +0.5	V
P _D	Power Dissipation	1.5	W
T _{SOLDER}	Soldering Temperature · Time	260·10	°C · sec
T _{STRG}	Storage Temperature	-65~150	°C
T _{OPR}	Operating Temperature	0~70	°C

READ OPERATION

D.C. RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	TC574000D - 10	TC574000D - 120 / 150
V_{IH}	Input High Voltage	$2.2V \sim V_{CC} + 0.3$	$2.2V \sim V_{CC} + 0.3$
V_{IL}	Input Low Voltage	$-0.3V \sim 0.8V$	$-0.3V \sim 0.8V$
V_{CC}	V_{CC} Power Supply Voltage	$5V \pm 5\%$	$5V \pm 10\%$

D.C. AND OPERATING CHARACTERISTICS ($T_a = 0 \sim 70^\circ C$)

SYMBOL	PARAMETER	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
I_{LI}	Input Current	$V_{IN} = 0V \sim V_{CC}$	-	-	± 10	μA
I_{CCO1}	Operating Current	$\overline{CE} = 0V$ $I_{OUT} = 0mA$ $f = 10MHz$	-	-	70	mA
		$\overline{CE} = 0V$ $I_{OUT} = 0mA$ $f = 8.3MHz$	-	-	60	mA
I_{CCO2}		$\overline{CE} = 0V$ $I_{OUT} = 0mA$ $f = 1MHz$	-	-	30	mA
I_{CCS1}	Standby Current	$\overline{CE} = V_{IH}$	-	-	1	mA
I_{CCS2}		$\overline{CE} = V_{CC} - 0.2V$	-	-	100	μA
V_{OH}	Output High Voltage	$I_{OH} = -400\mu A$	2.4	-	-	V
V_{OL}	Output Low Voltage	$I_{OL} = 2.1mA$	-	-	0.4	V
I_{PP1}	V_{PP} Current	$V_{PP} = 0V \sim V_{CC} + 0.6V$	-	-	± 10	μA
I_{LO}	Output Leakage Current	$V_{OUT} = 0.4V \sim V_{CC}$	-	-	± 10	μA

A.C. CHARACTERISTICS ($T_a = 0 \sim 70^\circ C$)

SYMBOL	PARAMETER	- 10		- 120		- 150		UNIT
		MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	
t_{ACC}	Address Access Time	-	100	-	120	-	150	ns
t_{CE}	\overline{CE} to Output Valid	-	100	-	120	-	150	ns
t_{OE}	\overline{OE} to Output Valid	-	50	-	60	-	70	ns
t_{DF1}	\overline{CE} to Output in High Impedance	0	50	0	50	0	60	ns
t_{DF2}	\overline{OE} to Output in High Impedance	0	50	0	50	0	60	ns
t_{OH}	Output Data Hold Time	0	-	0	-	0	-	ns
t_{BT}	BYTE to Output Valid	-	100	-	120	-	150	ns
t_{BTD}	BYTE to Output in High Impedance	-	60	-	60	-	70	ns

TC574200D-10 satisfies characteristics of TC574200D-120.

A.C. TEST CONDITIONS

- Output Load : 1 TTL Gate and $CL = 100PF$
- Input Pulse Rise and Fall Time : 10ns Max
- Input Pulse Levels : 0.45V and 2.4V
- Timing Measurement Reference Level : Input 0.8V and 2.2V, Output 0.8V and 2.0V

TC574200D-10, TC574200D-120 TC574200D-150

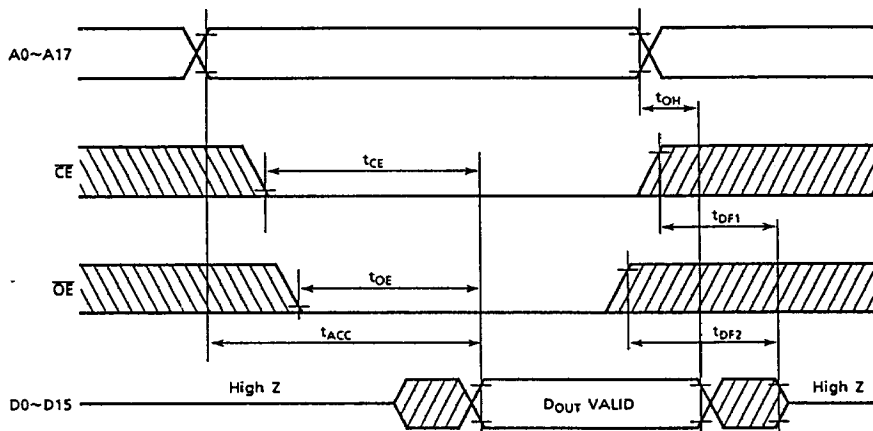
CAPACITANCE * ($T_a = 25^\circ\text{C}$, $f = 1\text{MHz}$)

SYMBOL	PARAMETER	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
C_{IN1}	Input Capacitance	$V_{IN} = 0\text{V}$	-	6	10	pF
C_{IN2}	Input Capacitance (BYTE / V_{PP})	$V_{IN} = 0\text{V}$	-	110	120	pF
C_{OUT}	Output Capacitance	$V_{OUT} = 0\text{V}$	-	10	12	pF

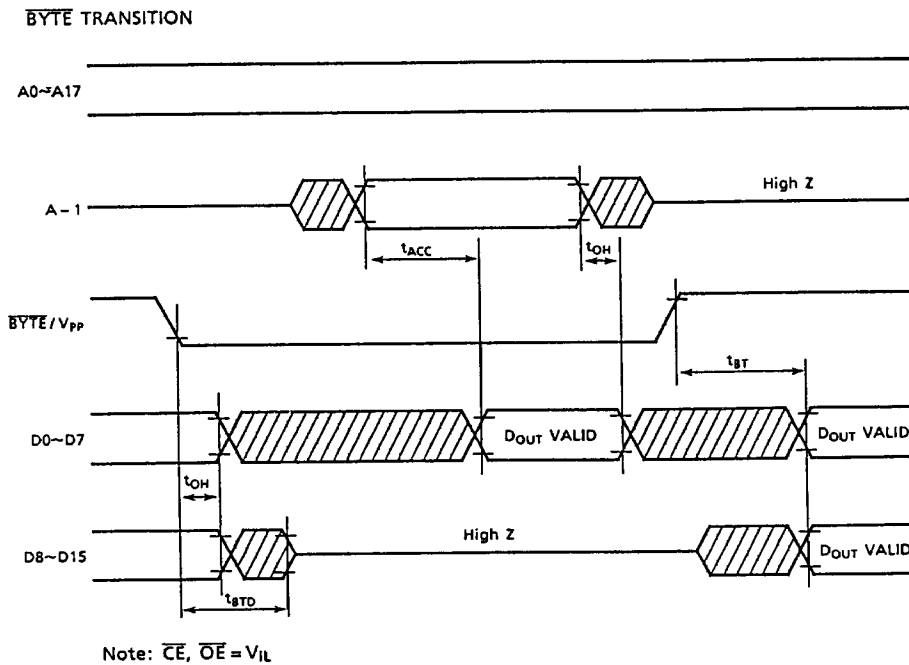
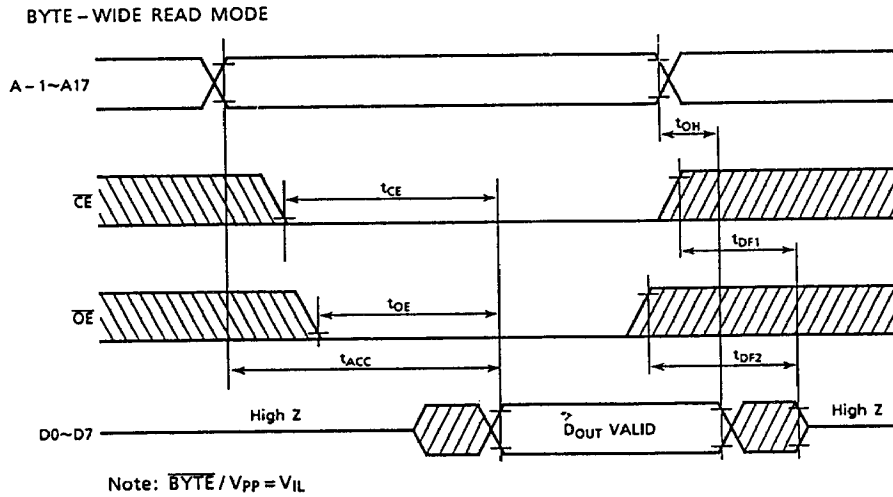
* This parameter is periodically sampled and is not 100% tested.

TIMING WAVEFORMS

WORD-WIDE READ MODE



Note: $\overline{\text{BYTE}} / V_{PP} = V_{IH}$



TC574200D-10, TC574200D-120 TC574200D-150

HIGH SPEED PROGRAM OPERATION

D.C. RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	MIN.	TYP.	MAX.	UNIT
V_{IH}	Input High Voltage	2.2	–	$V_{CC} + 1.0$	V
V_{IL}	Input Low Voltage	– 0.3	–	0.8	V
V_{CC}	V_{CC} Power Supply Voltage	6.00	6.25	6.50	V
V_{PP}	V_{PP} Power Supply Voltage	12.20	12.50	12.80	V

D.C. AND OPERATING CHARACTERISTICS ($T_a = 25 \pm 5^\circ\text{C}$, $V_{CC} = 6.25 \pm 0.25\text{V}$, $V_{PP} = 12.50 \pm 0.30\text{V}$)

SYMBOL	PARAMETER	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
I_{LI}	Input Current	$V_{IN} = 0\text{V} \sim V_{CC}$	–	–	± 10	μA
V_{OH}	Output High Voltage	$I_{OH} = -400\mu\text{A}$	2.4	–	–	V
V_{OL}	Output Low Voltage	$I_{OL} = 2.1\text{mA}$	–	–	0.4	V
I_{CC}	V_{CC} Supply Current	–	–	–	40	mA
I_{PP2}	V_{PP} Supply Current	$V_{PP} = 12.8\text{V}$	–	–	50	mA

A.C. PROGRAMMING CHARACTERISTICS ($T_a = 25 \pm 5^\circ\text{C}$, $V_{CC} = 6.25 \pm 0.25\text{V}$, $V_{PP} = 12.50 \pm 0.30\text{V}$)

SYMBOL	PARAMETER	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
t_{AS}	Address Setup Time	–	2	–	–	μs
t_{AH}	Address Hold Time	–	2	–	–	μs
t_{CES}	\overline{CE} Setup Time	–	0	–	–	μs
t_{CEH}	\overline{CE} Hold Time	–	0	–	–	μs
t_{OES}	\overline{OE} Setup Time	–	2	–	–	μs
t_{DS}	Data Setup Time	–	2	–	–	μs
t_{DH}	Data Hold Time	–	2	–	–	μs
t_{VPS}	V_{PP} Setup Time	–	2	–	–	μs
t_{VCS}	V_{CC} Setup Time	–	2	–	–	μs
t_{PW}	Program Pulse Width	–	45	50	55	μs
t_{OPW}	Overprogram Pulse Width	Note 1	45	50	55	μs
t_{OE}	\overline{OE} to Output Valid	$\overline{CE} = V_{IH}$	–	–	100	ns
t_{DFP}	\overline{OE} to Output in High Impedance	$\overline{CE} = V_{IH}$	–	–	90	ns

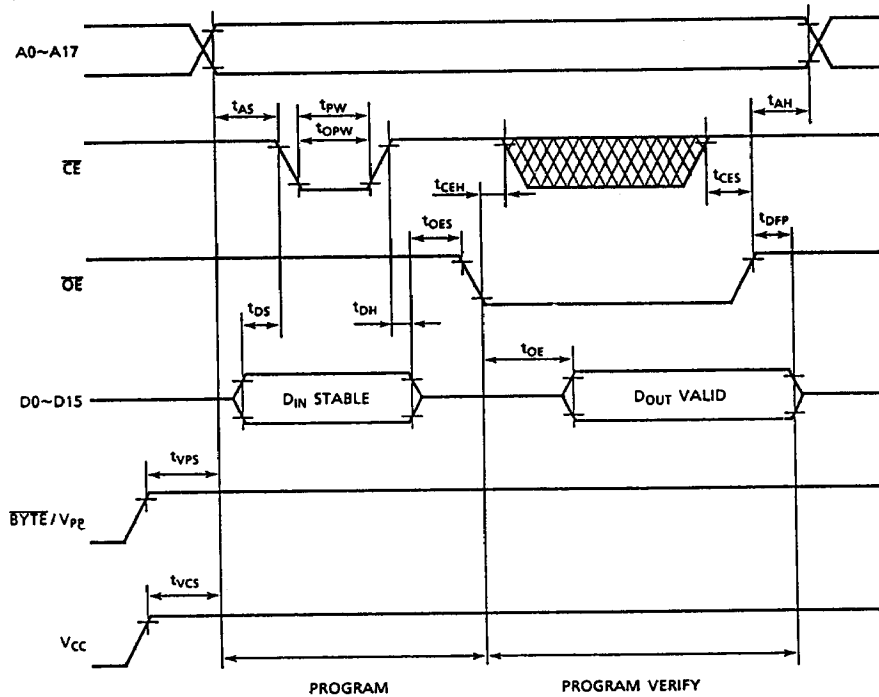
A.C. TEST CONDITIONS

- Output Load : 1 TTL Gate and $CL = 100\text{PF}$
- Input Pulse Rise and Fall Time : 10ns Max
- Input Pulse Levels : 0.45V and 2.4V
- Timing Measurement Reference Level : Input 0.8V and 2.2V, Output 0.8V and 2.0V

Note 1: t_{OPW} depends on the program pulse width which is required in the initial program.

TIMING WAVEFORMS

HIGH SPEED PROGRAM OPERATION



- Note: 1. VCC must be applied simultaneously or before Vpp and cut off simultaneously or after Vpp.
2. Removing the device from socket and setting the device in socket with Vpp=12.50V may cause permanent damage to the device.
3. The Vpp supply voltage is permitted up to 14V for program operation. So the voltage over 14V should not be applied to the Vpp terminal. When the switching pulse voltage is applied to the Vpp terminal, the overshoot voltage of its pulse should not be exceeded 14V.

TC574200D-10, TC574200D-120 TC574200D-150

ERASURE CHARACTERISTICS

The TC574200D's erasure is achieved by applying shortwave ultraviolet light which has a wavelength of 2537Å (Angstroms) to the chip through the transparent window. Then integrated dose (ultraviolet light intensity $[W/cm^2] \times$ exposure time [sec.]) for erasure should be a minimum of 15 $[W \cdot sec/cm^2]$.

When the Toshiba sterilizing lamp GL-15 is used and the device is exposed at a distance of 1cm from the lamp surface, the erasure will be achieved within 60 minutes. And using commercial lamps whose ultraviolet light intensity is a 12000 $[\mu W/cm^2]$ will reduce the exposure time to about 20 minutes. (In this case, the integrated dose is 12000 $[\mu W/cm^2] \times (20 \times 60)$ [sec] = 15 $[W \cdot sec/cm^2]$.)

The TC574200D's erasure begins to occur when exposed to light with wavelength shorter than 4000Å. The sunlight and the fluorescent lamps will include 3000~4000Å wavelength components.

Therefore when used under such lighting for extended periods of time, the opaque seals - Toshiba EPROM Protect Seal AC907 - are available.

OPERATION INFORMATION

The TC574200D's eight operation mode are listed in the following table. Mode selection can be achieved by applying TTL level signal to all inputs.

MODE	PINS	CE	OE	BYTE /V _{PP}	V _{CC}	D0~D7	D8~D14	D15 /A-1	Power
Read (16 Bit)		L	L	H	5V	Data Out			Active
Read (Lower 8 Bit)		L	L	L		Data Out (Lower 8 Bit)	High Impedance	L	
Read (Upper 8 Bit)		L	L	L		Data Out (Upper 8 Bit)	High Impedance	H	
Output Deselect	L	H	H	High Impedance					
			L	High Impedance			*		
Standby	H	*	H	High Impedance			*	Standby	
			L	High Impedance			*		
Program	L	H		12.5V	6.25V	Data In			Active
Program Inhibit	H	H				High Impedance			
Program Verifv	*	L				Data Out			

Note: H = V_{IH}, L = V_{IL}, * = V_{IH} or V_{IL}

READ MODE

The TC574200D has the $\overline{\text{BYTE}}/\text{Vpp}$ terminal that selects word-wide output and byte-wide output. When $\overline{\text{BYTE}}/\text{Vpp}$ is set to V_{IH} , the word-wide output is selected, and D15/A-1 pin is used for D15 data output.

When $\overline{\text{BYTE}}/\text{Vpp}$ is set to V_{IL} , the byte-wide output is selected, and D15/A-1 pin is used for A-1 address input. When A-1 is set to V_{IL} in this condition, the data output is selected lower 8 bits of the 16 bit data which has been programmed. When A-1 is set to V_{IH} , the data output is selected upper 8 bits.

The TC574200D has two control function. The chip enable ($\overline{\text{CE}}$) controls the operation power and should be used for device selection. The output enable ($\overline{\text{OE}}$) controls the output buffers, independent of device selection. Assuming that $\overline{\text{CE}}=\overline{\text{OE}}=V_{IL}$, the output data is valid at the outputs after address access time from stabilizing of all addresses. The $\overline{\text{CE}}$ to output valid (t_{CE}) is equal to the address access time (t_{ACC}). Assuming that $\overline{\text{CE}}=V_{IL}$ and all addresses are valid, the output data is valid at the outputs after t_{OE} from the falling edge of $\overline{\text{OE}}$.

OUTPUT Deselect MODE

Assuming that $\overline{\text{CE}}=V_{IH}$ or $\overline{\text{OE}}=V_{IH}$, the outputs will be in a high impedance state. So two or more TC574200D's can be connected together on a common bus line. When $\overline{\text{CE}}$ is decoded for device selection, all deselected devices are in low power standby mode.

STANDBY MODE

The TC574200D has a low power standby mode controlled by the $\overline{\text{CE}}$ signal. By applying a high level to the $\overline{\text{CE}}$ input, the TC574200D is placed in the standby mode which reduces the operating current to 100 μA by applying MOS-high level (V_{CC}) and then the outputs are in a high impedance state, independent of the $\overline{\text{OE}}$ input.

TC574200D—10, TC574200D—120 TC574200D—150

PROGRAM MODE

Initially, when recieved by customers, all bits of the TC574200D are in the "1" state which is erased state. Therefore the program operation is to introduce "0's" data into the desired bit location by electrically programming. The TC574200D is in the programming mode when the V_{pp} input is at 12.50V and \overline{CE} is at TTL-Low under $\overline{OE}=V_{IH}$. Data to be programmed must be applied 16 bits in parallel to the data pins.

The TC574200D can be programmed any location at anytime either individually, sequentially, or at random.

PROGRAM VERIFY MODE

The verify mode is to check that desired data is correctly programmed on the programmed bits.

The verify is accomplished with \overline{OE} at V_{IL} . The programmed data should be compared with the original word-wide (16 bit) data.

PROGRAM INHIBIT MODE

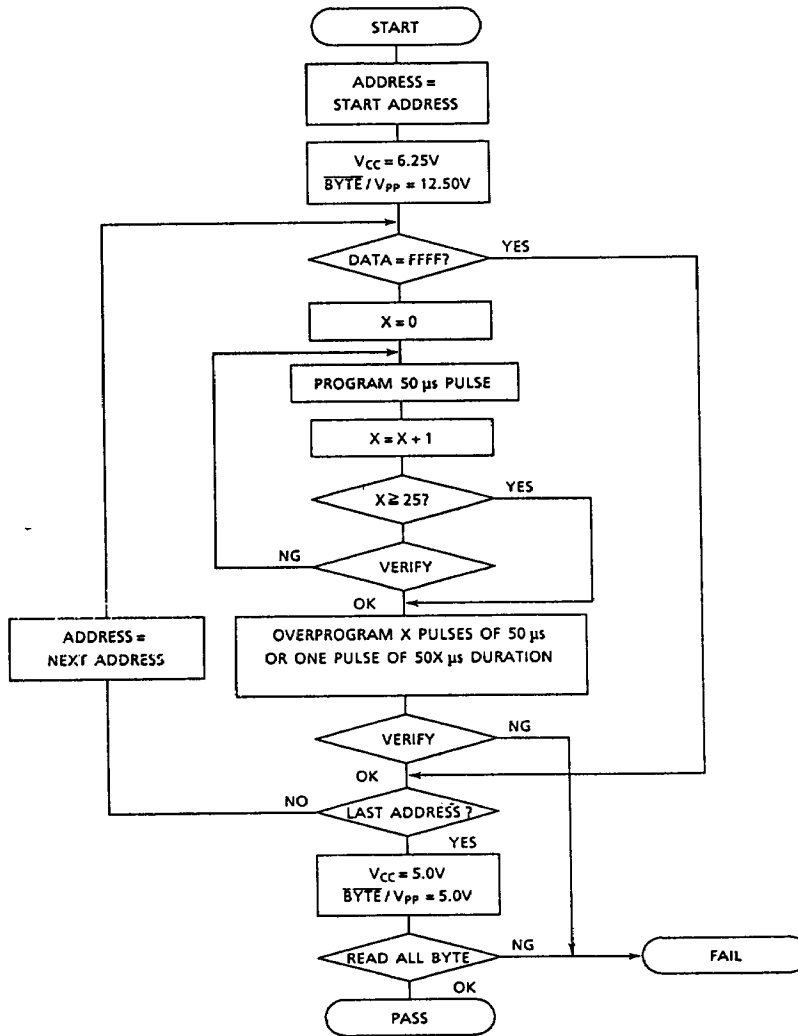
Under the condition that the program voltage (12.50V) is applied to V_{pp} terminal, a high level \overline{CE} input inhibits the TC574200D from being programmed. Programming of two or more EPROMs in parallel with different data is easily accomplished. That is, all inputs except for \overline{CE} and \overline{OE} may be commonly connected, and a TTL low level program pulse is applied to the \overline{CE} of the desired device only and TTL high level signal is applied to the other devices.

HIGH SPEED PROGRAM MODE

The device is set up the high speed programming mode when the programming voltage (12.50V) is applied to the V_{pp} terminal with $V_{CC}=6.25V$. The programming is achieved by applying a single TTL low level 50 μ s pulse to the \overline{CE} input after addresses and data are stable. Then the programmed data is verified by using Program Verify Mode. If the programmed data is not correct, another program pulse of 50 μ s is applied and then the programmed data is verified. This should be repeated until the program operates correctly(max. 25 times).

After correctly programming the selected address, the overprogram pulse of same length that needed for initial programming should be applied. When programming has been completed, the data in all addresses should be verified with $V_{CC}=V_{pp}=5V$.

HIGH SPEED PROGRAM MODE
FLOW CHART



TC574200D—10, TC574200D—120 TC574200D—150

ELECTRIC SIGNATURE MODE

Electric signature mode allows to read out a code from TC574200D which identifies its manufacturer and device type. The programming equipment may read out manufacturer code and device code from TC574200D by using this mode before program operation and automatically set program voltage (V_{pp}) and algorithm.

Electric signature mode is set up when 12V is applied to address line A9 and the rest of address lines is set to V_{IL} in read operation. Data output in this condition is manufacturer code. Device code is identified when address A0 is set to V_{IH} . These two codes possess an odd parity with the parity bit of (D7).

The following table shows electric signature of TC574200D.

SIGNATURE \ PINS	A0	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0	HEX DATA
Manufacturer Code	V_{IL}	*	*	*	*	*	*	*	*	1	0	0	1	1	0	0	0	**98
Device Code	V_{IH}	*	*	*	*	*	*	*	*	1	0	0	0	1	1	1	1	**8F

Note: A1 – A8, A10 – A17, \overline{CE} , $\overline{OE} = V_{IL}$

BYTE / $V_{PP} = V_{IH}$

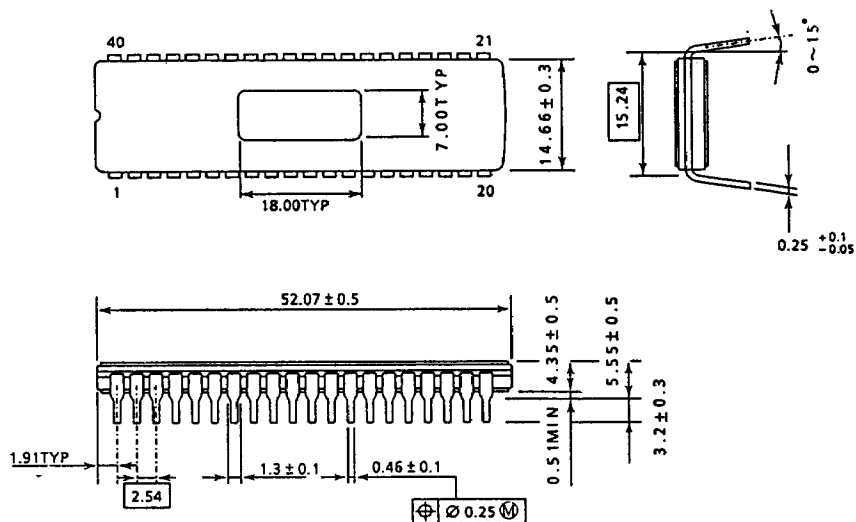
* Don't care

D.C. RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	MIN.	TYP.	MAX.	UNIT
V_{CC}	V_{CC} Power Supply Voltage	4.50	5.00	5.50	V
V_{ID}	A9 Auto Select Voltage	11.50	12.00	12.50	V

OUTLINE DRAWINGS

Unit in mm



Weight : 12.20g (TYP.)