

## New Product

**SURFACE MOUNT  
SHIELDED POWER INDUCTOR  
SERIES SSI703**

### FEATURES

- RoHS Compliant
- High current handling capacity
- Low height

### ELECTRICAL SPECIFICATIONS

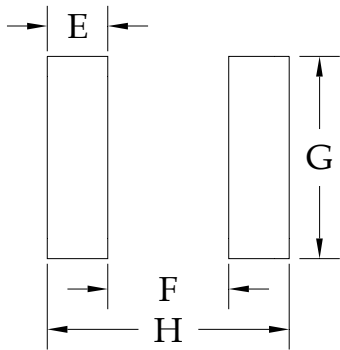
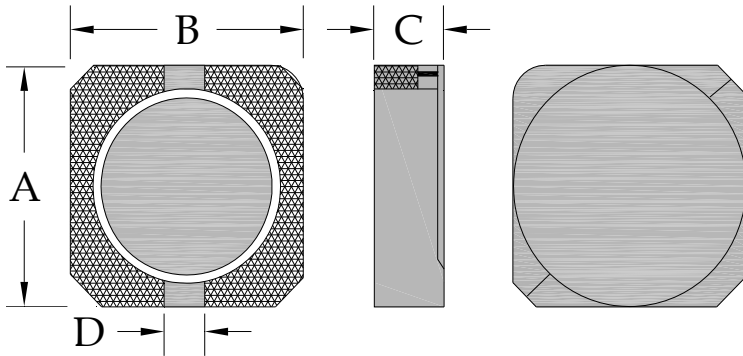
- Inductance range                    3.9uH to 680uH
- Test condition                        100mVrms, 10kHz
- Test equipment                        Quadtech 1750 LCR Meter

### PHYSICAL SPECIFICATIONS

- Operating temp.                       -25°C to +105°C
- Core                                        Ferrite
- Packaging                                 T & R 1500 pieces per reel
- Tape & reel spec.                       Tape 16 mm embossed carrier
- Reel 330 mm

### Dimensions in millimeters

- Length A                                    7.0 max
- Width B                                     7.0 max
- Height C                                    3.0 max
- Terminal pitch D                         2.0 ref



### Suggested PCB land pattern

- E = 2.65
- F = 2.0
- G = 7.3
- H = 7.3

### SPECIFICATIONS

Part Number	L(uH)	Tol ± %	DCR (ohms) max	Rated Current (A) (Note 1)
SSI703-3R9Y	3.9	30	0.045	2.60
SSI703-5R0Y	5.0	30	0.048	2.40
SSI703-6R0Y	6.0	30	0.050	2.25
SSI703-7R3Y	7.3	30	0.065	2.10
SSI703-8R6Y	8.6	30	0.078	1.85
SSI703-100Y	10	30	0.085	1.70
SSI703-120Y	12	30	0.10	1.55
SSI703-150Y	15	30	0.13	1.40
SSI703-180Y	18	30	0.15	1.32
SSI703-220Y	22	30	0.16	1.20
SSI703-270Y	27	30	0.21	1.05
SSI703-330Y	33	30	0.22	0.97
SSI703-390Y	39	30	0.28	0.86
SSI703-470Y	47	30	0.34	0.80
SSI703-560Y	56	30	0.38	0.73
SSI703-680Y	68	30	0.50	0.65
SSI703-820Y	82	30	0.58	0.60
SSI703-101Y	100	30	0.63	0.54
SSI703-121M	120	20	0.83	0.51
SSI703-151M	150	20	1.15	0.47
SSI703-181M	180	20	1.40	0.41
SSI703-221M	220	20	1.70	0.37
SSI703-271M	270	20	1.95	0.33
SSI703-331M	330	20	2.30	0.28
SSI703-391M	390	20	2.80	0.27
SSI703-471M	470	20	3.60	0.21
SSI703-561M	560	20	4.00	0.20
SSI703-681M	680	20	5.25	0.20

#### Notes:

1. Based on ΔL of 35% max or ΔT of 40°C max, whichever occurs first.

All test data based on 25°C ambient. Part temperature (ambient+temp rise) must not exceed max operating temperature under worst case operating conditions. Circuit design, components, PCB trace size and thickness, airflow and other cooling provisions all effect the part temperature.