ADT EVRC - B

DATA SHEET



Adaptive Digital Technologies, Inc.

EVRC-B Vocoder

Enhanced Variable Rate Codec B

PRODUCT DESCRIPTION

Enhanced Variable Rate Codec B (EVRC-B) is a speech codec used by CDMA (Code Division Multiple Access) networks. EVRC-B is an extension to EVRC and compresses each 20 millisecond FRAMES of 8000 Hz, 16-bit sampled speech input into output frames of one of the four different sizes: Rate 1 - 171 bits, Rate 1/2 - 80 bits, Rate 1/4 - 40 bits, Rate 1/8 - 16 bits. One significant enhancement in EVRC-B is the use of 1/4-rate frames that were not used in EVRC.

The EVRC-B operates at the bit rates 8.3, 7.57, 6.64, 6.18,5.82,5.45,5.08,4.0bkps(Source encoding rates). The EVRC-B makes use of the intermediate coding rates through increased awareness of the nature of the individual speech samples. This provides lower average data rates compared to EVRC, for a given voice quality.

FEATURES

- Functions are C-callable.
- Multi-channel capable
- Can be integrated with echo cancellers, and tone detection/regeneration.
- Encodes frames of 20 ms sampled at 8KHz (160 samples) using above algorithms.
- The implementation is xDAIS/xDM1.0 complaint.
- Mixed C and C55x assembly code implementation
- Bit-exact with all EVRCB 3GPP2 test sequences
- The implementation support run time table relocation
- Fully interruptible Code
- Efficient scratch memory management with reduced stack requirements

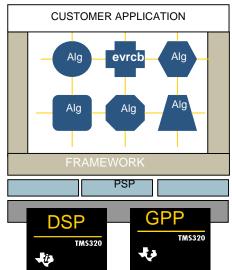
AVAILABILITY

ADT EVRC-B is available on the TMS3205000[™] DSP Family C55x[™]DSP Generation

SPECIFICATIONS

C55x

Configuration	ID
Encoder with 20ms framesize	EVRCB_001
Decoder with 20ms frame size	EVRCB_002
Full Duplex	EVRCB_003





Cycles Information – Spectrum Digital C5510 DSK

Configuration ID	Performance Statistics (IN MEGACYCLES /SEC) ¹		
Configuration iD	Average	Peak	
EVRCB_001	NA	23.10	
EVRCB_002	NA	8.10	
EVRCB_003	NA	31.20	

¹ Measured with all program, constant tables, instance and CDP in SARAM, scratch in DARAM. All performance statistics with DTMF and TTY flags turned off.

Memory Statistics -

	Memory Statistics	s^2				
Configuration	Program	Data Memory				
Configuration	MEMORY	Internal	External	Stack		Total
				SP	SSP	
EVRCB_001	83.94	45.81	0	0.51	0.51	130.77
EVRCB_002	53.09	44.46	0	0.4	0.4	98.35
EVRCB_003	118.7	48.19	0	0.51	0.51	167.91

² All memory requirements are expressed in kilobytes (1 kilobyte = 1024 8-bit bytes).

FUNCTIONS

API function call summary

```
InitializeEVRC_BencoderChannel (. . .)
InitializeEVRC_BDecoderChannel (. . .)
EncodeEVRC_B (. . .)
DecodeEVRC_B (. . .)
```

Initializ	es t	he	EVRC	С-В	encoder
Initializ	es t	he	EVRO	C−B	decoder
Executes	the	EVF	RC-B	enc	coder
Executes	the	EVF	RC-B	dec	coder

Adaptive Digital is a member of the Texas Instruments Developer Network, and ARM Connected Community.

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