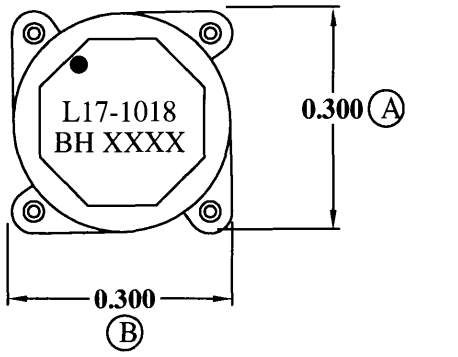
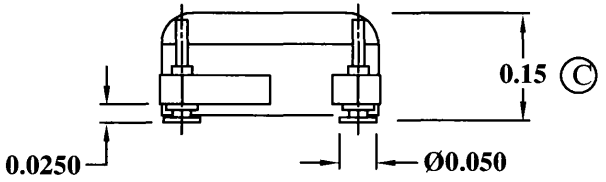
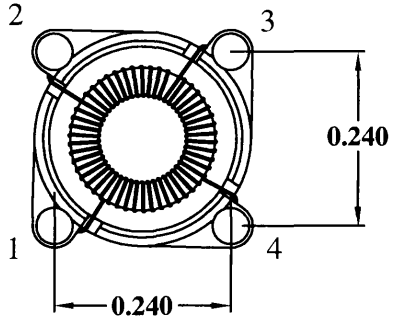
MECHANICAL

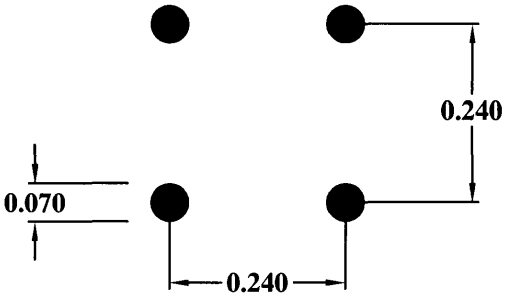




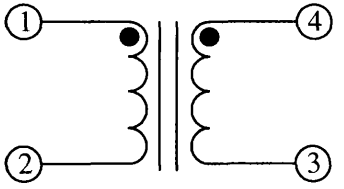


COPLANARITY 0.004 MAX.

SUGGESTED PAD LAYOUT



SCHEMATIC



ELECTRICAL SPECIFICATIONS

TURNS RATIO: 1:1

OPEN CIRCUIT INDUCTANCE: 15.03µH±10% @ 0 ADC (1, 4 - 2, 3)
10 KHz 20mV

DC BIAS: 9.77µH TYP. @ 1.82 ADC (1, 4 - 2, 3)
10 KHz 20mV

DC RESISTANCE: 0.166 OHMS MAX. (1, 4 - 2, 3)

IR: 1K MEGOHMS MIN. @ 100 VDC (1-4)

RoHS COMPLIANT

ISSUE	DATE	REVISION	BY
B	10-21-13	PAD LAYOUT	JDC
A	10-22-07	CUSTOMER PRINT	JDC

DESIGN PER: BH CATALOG

CUSTOM ☐ NON-PROPRIETARY ☒

DRAWN: K. LOFTUS

ENGR: J. DECRAMER

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS IN INCHES, TOLERANCES
NON-CUMULATIVE
.XX±.01 .XXX±.005

PAGE 1 OF 1

BH ELECTRONICS, INC.
BURNSVILLE & MARSHALL, MN

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TITLE: INDUCTOR

PART NO. L17-1018