MULTILAYER CERAMIC CAPACITORS

Ultra High Q & Low ESR Series (GUQ)



1. INTRODUCTION

MLCC Consists of a conducting material and electrodes. To manufacture a chip-type SMT and achieve miniaturization, high density and high efficiency, ceramic condensers are used.

Calchip GUQ series MLCC is used at high frequencies and generally have a small temperature coefficient of capacitance, typical within the +/-30ppm/C required for NPO (COG) classification and have excellent conductivity internal electrode. Thus, Calchip GUQ

2. FEATURES

- a. High Q and low ESR performance at high frequency.
- b. Ultra low capacitance to 0.1pF.
- c. Can offer high precision tolerance to ±0.05pF.
- d. Quality improvement of telephone calls for low power loss and better performance.

3. APPLICATIONS

- a. Telecommunication products & equipments: Mobile phone, WLAN, Base station.
- b. RF module: Power amplifier, VCO.
- c. Tuners.

HOW TO ORDER

GUQ	10	CG	101	J	250	N	T
SERIES	SIZE	DIELECTRIC	CAPACITANCE	TOLERANCE	VOLTAGE	TERMINATION	PACKAGING
GUQ=Ultra High Q & Low ESR	02=0201 04=0402 10=0603 21=0805	CG = NP0 (C0G)	Two significant digits followed b no. of zeros. An R is in place of decimal point. eg.: 0R5=0.5pF 1R0=1.0pF 100=10pF	A=±0.05pF B=±0.1pF C=±0.25pF D=±0.5pF F=±1% G=±2% J=±5%	Two significant digits followed by no. of zeros. And R is in place of decimal point. 25=25 VDC 50=50 VDC 100=100 VDC 250=250 VDC	N=Cu/Ni/Sn	T = 7" reel TD = 13 reel



5. EXTERNAL DIMENSIONS

Size Inch (mm)	L (mm)	W (mm)	T (mm)/Sym	bol	Remark	M _B (mm)
0201 (0603)	0.60±0.03	0.30±0.03	0.30±0.03	L	#	0.15±0.05
0402 (1005)	1.00±0.05	0.50±0.05	0.50±0.05	N	#	0.25+0.05/-0.10
0603 (1608)	1.60±0.10	0.80±0.10	0.80±0.07	s		0.40±0.15
0805 (2012)	2.00±0.20	1.25±0.20	0.85±0.10	Т		0.50±0.20

Fig. 1 The outline of MLCC

6. GENERAL ELECTRICAL DATA

Dielectric	NP0			
Size	0201, 0402, 0603, 0805			
Capacitance*	0201: 0.1pF to 33pF; 0402: 0.1pF to 22pF; 0603: 0.3pF to 47pF; 0805: 0.3pF to 100pF			
	Cap≤5pF: A (±0.05pF), B (±0.1pF), C (±0.25pF)			
Capacitance tolerance 5pF <cap<10pf: (±0.1pf),="" (±0.25pf),="" (±0.5pf)<="" b="" c="" d="" th=""></cap<10pf:>				
Cap≥10pF: F (±1%), G (±2%), J (±5%)				
Rated voltage (WVDC)	6.3V, 10V, 25V, 50V, 100V, 250V			
Q*	Cap≥30pF, Q≥1000; Cap<30pF,Q≥400+20C			
Insulation resistance at Ur	≥10GΩ			
Operating temperature	-55 to +125°C			
Capacitance change	±30ppm/°C; 0201Cap≥22pF, ±60ppm/°C			
Termination	Ni/Sn (lead-free termination)			

^{*} Measured at the conditions of 25°C ambient temperature and 30~70% related humidity.

 $Apply \ 1.0 \pm 0.2 Vrms, \ 1.0 MHz \pm 10\% \ for \ Cap \leq 1000 pF \ and \ 1.0 \pm 0.2 Vrms, \ 1.0 kHz \pm 10\% \ for \ Cap > 1000 pF.$

[#] Reflow soldering only is recommended.



7. CAPACITANCE RANGE

DIELECTRIC								NP0					
	SIZE		0201		04	02		0603			0805		
RAT	ED VOLTAGE (VDC)	6.3	10	25	50	100	50	100	250	50	100	250	Tolerance
	0.1pF (0R1)	L	L	L	N	N							В
	0.2pF (0R2)	L	L	L	N	N							A, B
	0.3pF (0R3)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B
	0.4pF (0R4)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B
	0.5pF (0R5)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	0.6pF (0R6)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	0.7pF (0R7)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	0.8pF (0R8)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	0.9pF (0R9)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	1.0pF (1R0)	L	L	L	Ν	N	S	S	S	Т	Т	Т	A, B, C
	1.2pF (1R2)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	1.5pF (1R5)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	1.8pF (1R8)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	2.2pF (2R2)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	2.7pF (2R7)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	3.3pF (3R3)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	3.9pF (3R9)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
	4.7pF (4R7)	L	L	L	N	N	S	S	S	Т	Т	Т	A, B, C
an an	5.6pF (5R6)	L	L	L	N	N	S	S	S	Т	Т	Т	B, C, D
Š	6.8pF (6R8)	L	L	L	N	N	S	S	S	Т	Т	Т	B, C, D
cita	8.2pF (8R2)	L	L	L	N	N	S	S	S	Т	Т	Т	B, C, D
Capacitance	10pF (100)	L	L	L	N	N	S	S	S	Т	Т	Т	F, G, J
ပိ	11pF (110)	L	L	L	N		S	S	S	Т	Т	Т	F, G, J
	12pF (120)	L	L	L	N		S	S	S	T	T	Т	F, G, J
	13pF (130)	L	L	L	N		S	S	S	T	T	T	F, G, J
	15pF (150)	L	L	L	N		S	S	S	T	T	T	F, G, J
	16pF (160)	L	L	L	N		S	S	S	T	T	T	F, G, J
	18pF (180)	L	L	L	N		S	S	S	T	T	T	F, G, J
	20pF (200)	L	L	L	N		S	S	S	T	T	T	F, G, J
	22pF (220)	L	L	L	N		S	S	S	T	T	T	F, G, J
	24pF (240)	L	L				S	S	S	T	T	T	F, G, J
	27pF (270)	L	L	L			S	S	S	T T	T	T	F, G, J
	30pF (300)	L	L	L			S	S	S		T	T	F, G, J
	33pF (330)	L	L	L			S	S	S	T	T	T	F, G, J
	36pF (360)						_	S	S	T	T	T	F, G, J
	39pF (390)						S S	S	S	T	T	T T	F, G, J F, G, J
	43pF (430)						S	S	S	T	T	T	F, G, J F, G, J
	47pF (470)						<u> </u>	<u> </u>	<u> </u>	T	T	T	-
	56pF (560)									T	T	T	F, G, J
	68pF (680)							<u> </u>		T	T	T	F, G, J
	82pF (820)											 	F, G, J
	100pF (101)									Т	Т	Т	F, G, J

^{1.} The letter in cell is expressed the symbol of product thickness.

8. PACKAGING DIMENSION AND QUANTITY

Size	Thickness (mm)/Syn	nhol	Paper tape 7" reel 13" reel		
Size	Thickness (min/syn	iiboi			
0201 (0603)	0.30±0.03	L	15k	70k	
0402 (1005)	0.50±0.05	N	10k	50k	
0603 (1608)	0.80±0.07	S	4k	15k	
0805 (2012)	0.85±0.10	Т	4k	15k	

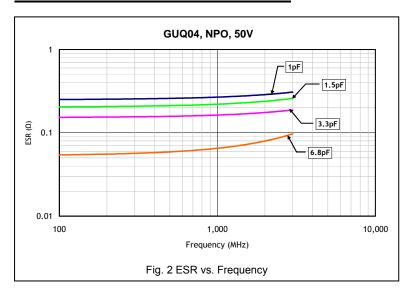
Unit: pieces

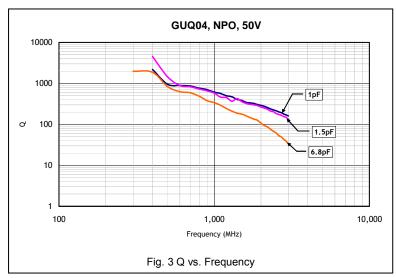
^{2.} CCE provide E96 (IEC-63) product range with which capacitance≤10pF.

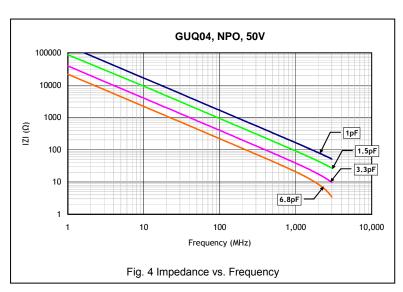
^{3.} For more information about products with special capacitance or other data, please contact CCE local representative.



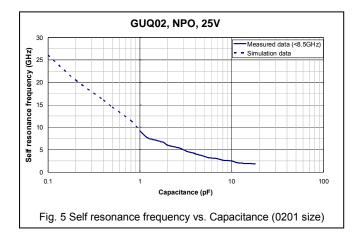
9. ELECTRICAL CHARACTERISTICS

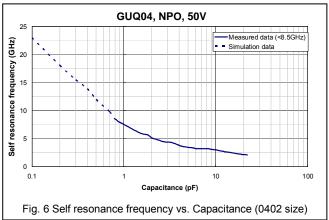


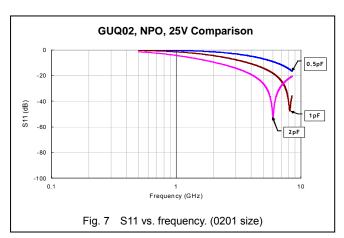


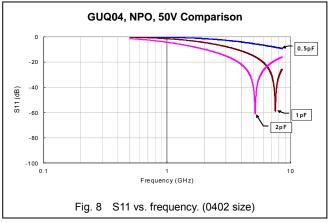


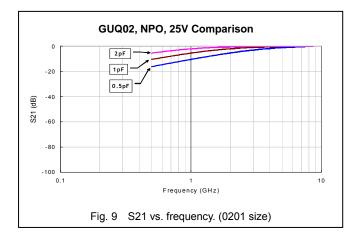


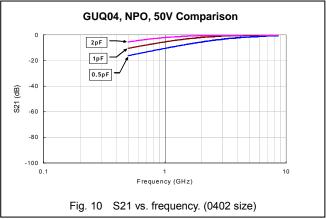














10. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

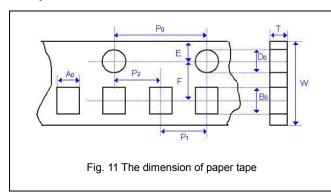
No.	Item	Test Conditions	Requirements	
1.	Visual and		* No remarkable defect.	
	Mechanical		* Dimensions to conform to individual specification sheet.	
2.	Capacitance	1.0±0.2Vrms, 1MHz±10%	* Shall not exceed the limits given in the detailed spec.	
3.	Q/ D.F.	At 25°C ambient temperature.	* Cap≥30pF, Q≥1000; Cap<30pF,Q≥400+20C	
	(Dissipation			
	Factor)			
4.	Dielectric	* To apply voltage:	* No evidence of damage or flash over during test.	
	Strength	≤100V, ≥250% of rated voltage.		
		250V, ≥200% of rated voltage.		
		* Duration: 1 to 5 sec.		
		* Charge and discharge current less than 50mA.		
5.	Insulation	To apply rated voltage for max. 120 sec.	≥10GΩ	
	Resistance			
6.	Temperature	With no electrical load.	* Capacitance change: within ±30ppm/°C;	
	Coefficient	Operating temperature: -55~125°C at 25°C	0201Cap≥22pF, within ±60ppm/°C	
7.	Adhesive	* Pressurizing force :	* No remarkable damage or removal of the terminations.	
	Strength of	0201: 2N		
	Termination	0402 & 0603: 5N		
		>0603: 10N		
_		* Test time: 10±1 sec.		
8.	Vibration	* Vibration frequency: 10~55 Hz/min.	* No remarkable damage.	
	Resistance	* Total amplitude: 1.5mm	* Cap change and Q/D.F.: To meet initial spec.	
		* Test time: 6 hrs. (Two hrs each in three mutually perpendicular directions.)		
9.	Solderability	* Solder temperature: 235±5°C	95% min. coverage of all metalized area.	
•	Colucius	* Dipping time: 2±0.5 sec.	oo // min. coverage of an mountained area.	
10.	Bending Test	*The middle part of substrate shall be pressurized by means	* No remarkable damage.	
		of the pressurizing rod at a rate of about 1 mm per second un	- I	
		the deflection becomes 1 mm and then the pressure shall be	(This capacitance change means the change of capacitance under	
		maintained for 5±1 sec.	specified flexure of substrate from the capacitance measured bet	
		* Measurement to be made after keeping at room temp. for	the test.)	
		24±2 hrs.		
11.	Resistance to	* Solder temperature: 270±5°C	* No remarkable damage.	
	Soldering Heat	* Dipping time: 10±1 sec	* Cap change: within ±2.5% or ±0.25pF whichever is larger.	
		* Preheating: 120 to 150°C for 1 minute before immerse the	* Q/D.F., I.R. and dielectric strength: To meet initial requirements.	
		capacitor in a eutectic solder.	* 25% max. leaching on each edge.	
		* Measurement to be made after keeping at room temp. for		
40	 	24±2 hrs.		
12.	Temperature	* Conduct the five cycles according to the temperatures and	* No remarkable damage.	
	Cycle	Sten Temp (°C) Time (min)	* Cap change: within ±2.5% or ±0.25pF whichever is larger. * O/D F_LP and dialoctric strongth: To most initial requirements.	
		Step Temp. (°C) Time (min.) 1 Min. operating temp. +0/-3 30±3	* Q/D.F., I.R. and dielectric strength: To meet initial requirements.	
		2 Room temp. 2~3		
		3 Max. operating temp. +3/-0 30±3		
		4 Room temp. 2~3		
		* Measurement to be made after keeping at room temp. for		
		24±2 hrs.		

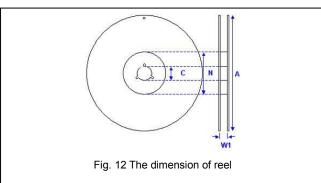


No.	Item	Test Condition	Requirements		
13.	Humidity	* Test temp.: 40±2°C	* No remarkable damage.		
	(Damp Heat)	* Humidity: 90~95% RH	* Cap change: within ±5.0% or ±0.5pF whichever is larger.		
	Steady State	* Test time: 500+24/-0hrs.	* Q/D.F. value: Cap≥30pF, Q≥350;		
		* Measurement to be made after keeping at room temp. for	10pF≤Cap<30pF, Q≥275+2.5C		
		24±2 hrs.	Cap<10pF; Q≥200+10C		
			* I.R.: ≥1GΩ.		
14.	Humidity	* Test temp.: 40±2°C	* No remarkable damage.		
	(Damp Heat)	* Humidity: 90~95%RH	* Cap change: within ±7.5% or ±0.75pF whichever is larger.		
	Load	* Test time: 500+24/-0 hrs.	* Q/D.F. value: Cap≥30pF, Q≥200;		
		* To apply voltage: rated voltage	Cap<30pF, Q≥100+10/3C		
		* Measurement to be made after keeping at room temp. for	* I.R.: ≥500MΩ.		
		24±2 hrs.			
15.	High	* Test temp.: 125±3°C	* No remarkable damage.		
	Temperature	* To apply voltage: 200% of rated voltage.	* Cap change: within ±3.0% or ±0.3pF whichever is larger.		
	Load	* Test time: 1000+24/-0 hrs.	* Q/D.F. value: Cap≥30pF, Q≥350		
	(Endurance)	* Measurement to be made after keeping at room temp. for	10pF≤Cap<30pF, Q≥275+2.5C		
		24±2 hrs.	Cap<10pF, Q≥200+10C		
			* I.R.: ≥1GΩ.		
16.	ESR	The ESR should be measured at room temperature and tested	0201, 0402 0603		
		at frequency 1±0.1 GHz.	0.5pF≤Cap≤1pF: < 350mΩ		
			1pF <cap≤5pf: 300mω<="" <="" th=""></cap≤5pf:>		
			5pF <cap≤22pf: 250mω<="" <="" th=""></cap≤22pf:>		

APPENDIXES

■ Tape & reel dimensions





Size	0201	0402	0603	0805
Thickness	L	N	S	Т
A ₀	0.37±0.03	0.62±0.05	1.00 +0.05/-0.1	1.50±0.10
B ₀	0.67±0.03	1.12±0.05	1.80±0.10	2.30±0.10
Т	0.42±0.03	0.60±0.05	0.95±0.05	0.95±0.05
K ₀	-	-	-	-
W	8.00±0.10	8.00±0.10	8.00±0.10	8.00±0.10
P ₀	4.00±0.10	4.00±0.10	4.00±0.10	4.00±0.10
10xP₀	40.0±0.10	40.0±0.10	40.0±0.20	40.0±0.20
P ₁	2.00±0.05	2.00±0.05	4.00±0.10	4.00±0.10
P ₂	2.00±0.05	2.00±0.05	2.00±0.05	2.00±0.05
D_0	1.55±0.05	1.55±0.05	1.55±0.05	1.55±0.05
D ₁	-	-	-	-
E	1.75±0.05	1.75±0.05	1.75±0.05	1.75±0.05
F	3.50±0.05	3.50±0.05	3.50±0.05	3.50±0.05

Size	0201, 0402, 0603, 0805			
Reel size 7"		13"		
C 13.0+0.5/-0.2		13.0+0.5/-0.2		
W ₁	8.4+1.5/-0	8.4+1.5/-0		
A 178.0±1.0		330.0±1.0		
N	60.0+1.0/-0	100±1.0		

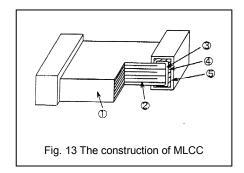


Cross Reference

<u>Size</u>	<u>wv</u>	<u>Walsin</u>	<u>Murata</u>	<u>Samsung</u>	<u>Johanson</u>	<u>ATC</u>
0201	25V	RF03*****250**	GJM03***1E******	CL03***** A*G***	250R05S******	-
0402	50V	RF15*****500**	GJM15***1H*****	CL05*****B*G***	500 R07S ******	-
0402	200V	-		-	-	600L*****200**
	50V	RF18*****500**	GQM18***1H********	-	-	-
0603	100V	RF18*****101**	GQM18***2A******	-	-	-
	250V	RF18*****251**	GQM18***2E******	-	251R14S******	600S***** 250**
0805	250V	RF21****251*	GQM21***2E******	-	251 R15S ******	600F*****250**

Constructions

No.	Na	NP0	
1	Ceramic	BaTiO₃ based	
2	Inner el	Cu	
3		Inner layer	Cu
4	Termination	Middle layer	Ni
\$		Outer layer	Sn (Matt)



■ Storage and handling conditions

- (1) To store products at 5 to 40°C ambient temperature and 20 to 70%. related humidity conditions.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

Cautions:

- a. Don't store products in a corrosive environment such as sulfide, chloride gas, or acid. It may cause oxidization of electrode, which easily be resulted in poor soldering.
- b. To store products on the shelf and avoid exposure to moisture.
- c. Don't expose products to excessive shock, vibration, direct sunlight and so on.



■ Recommended soldering conditions

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of N_2 within oven are recommended.

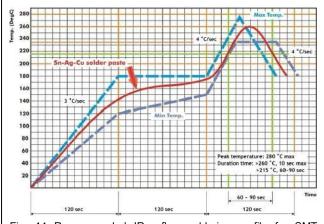


Fig. 14 Recommended IR reflow soldering profile for SMT process with SnAgCu series solder paste.

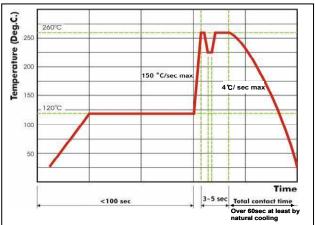


Fig. 15 Recommended wave soldering profile for SMT process with SnAgCu series solder.