Device handbook A*PLUS*-LED

Operating Instructions A*PLUS* with LED display or without display 157 679-19 (PM 1000357 000 01) 04/2016



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Legal information

Warning notices

In this document warning notices are used, which you have to observe to ensure personal safety and to prevent damage to property. Depending on the degree of danger the following symbols are used:



If the warning notice is not followed death or severe personal injury will result.



If the warning notice is not followed damage to property or severe personal injury **may** result.



If the warning notice is not followed the device **may** be damaged or **may** not fulfill the expected functionality.

Qualified personnel

The product described in this document may be handled by personnel only, which is qualified for the respective task. Qualified personnel have the training and experience to identify risks and potential hazards when working with the product. Qualified personnel are also able to understand and follow the given safety and warning notices.

Intended use

The product described in this document may be used only for the application specified. The maximum electrical supply data and ambient conditions specified in the technical data section must be adhered. For the perfect and safe operation of the device proper transport and storage as well as professional assembly, installation, handling and maintenance are required.

Disclaimer of liability

The content of this document has been reviewed to ensure correctness. Nevertheless it may contain errors or inconsistencies and we cannot guarantee completeness and correctness. This is especially true for different language versions of this document. This document is regularly reviewed and updated. Necessary corrections will be included in subsequent version and are available via our webpage http://www.camillebauer.com.

Feedback

If you detect errors in this document or if there is necessary information missing, please inform us via e-mail to: customer-support@camillebauer.com

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1. Introduction

1.1 Purpose of this document

This document describes the universal measurement device for heavy-current quantities APLUS. It is intended to be used by:

- Installation personnel and commissioning engineers
- Service and maintenance personnel
- Planners

Scope

This handbook is valid for all hardware versions of the APLUS with LED display or without display. Some of the functions described in this document are available only, if the necessary optional components are included in the device.

Required knowledge

A general knowledge in the field of electrical engineering is required. For assembly and installation of the device knowledge of applicable national safety regulations and installation standard is required.

1.2 Scope of supply

- Measurement device APLUS
- Safety instructions (multiple languages)
- Connection set basic unit: Plug-in terminals and mounting clamps
- Optional: Connection set I/O extension: Plug-in terminals

1.3 Further documents

Via our homepage http://www.camillebauer.com further documents about the APLUS can be downloaded:

- Safety instructions APLUS
- Data sheet APLUS
- Modbus basics: General description of the communication protocol
- Modbus interface APLUS: Register description of Modbus/RTU communication via RS-485
- Modbus/TCP interface APLUS: Register description of Modbus/TCP communication via Ethernet

2. Security notes





Device may only be disposed in a professional manner!

The installation and commissioning should only be carried out by trained personnel.

Check the following points before commissioning:

- that the maximum values for all the connections are not exceeded, see "Technical data" section,
- that the connection wires are not damaged, and that they are not live during wiring,
- that the power flow direction and the phase rotation are correct.

The instrument must be taken out of service if safe operation is no longer possible (e.g. visible damage). In this case, all the connections must be switched off. The instrument must be returned to the factory or to an authorized service dealer.

It is forbidden to open the housing and to make modifications to the instrument. The instrument is not equipped with an integrated circuit breaker. During installation check that a labeled switch is installed and that it can easily be reached by the operators.

Unauthorized repair or alteration of the unit invalidates the warranty.

3. Device overview

3.1 Brief description

The APLUS is a comprehensive instrument for the universal measurement, monitoring and power quality analysis in power systems. The device can be adapted fast and easily to the measurement task by means of the CB-Manager software. The universal measurement system of the device may be used directly for any power system, from single phase up to 4-wire unbalanced networks, without hardware modifications. Independent of measurement task and outer influences always the same high performance is achieved.

Using additional, optional components the opportunities of the APLUS may be extended. You may choose from I/O extensions, communication interfaces or data logger. The nameplate on the device gives further details about the present version.

The version with top-hat rail adapter instead of the display has the same dimensions and connections as the version with display and supports the same options.

3.2 Possible modes of operation

The A*PLUS* can cover a wide range of possible input ranges without any hardware variance. The adaption to the input signal is performed by means of variable amplifying levels for current and voltage inputs. Depending on the application it makes sense to fix these levels by means of the configuration or to let them stay variable to achieve a maximum accuracy during measurement. The differentiation, if the amplifying remains constant or is adapted to the present value, is done during the definition of the input configuration by means of the parameter "auto-scaling".

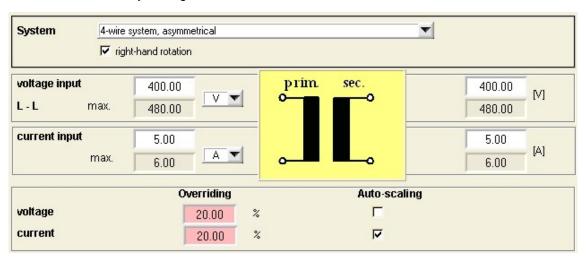
The disadvantage of auto-scaling is that when an amplifying level needs to be changed, a settling time of at least one cycle of the power frequency must be allowed until the signals have stabilized again. During this short time the measurement results remain frozen.

Continuous measurement

An absolute uninterrupted measurement of all quantities assumes that auto-scaling is deactivated for both voltage and current inputs.

Metering

The uncertainty of the active energy meters of the APLUS is given with class 0.5S. To fulfill the high requirements of the underlying meter standard EN 62053-22 also small currents have to be measured very accurate. To do so, auto-scaling must be activated for current inputs. For metering applications the system voltage is assumed to be quite constant, nominal value acc. standard, wherefore auto-scaling for voltages is not required. The subsequent example shows an appropriate configuration, which also conforms to the factory setting of the device.



Dynamic monitoring of limit values

An important criterion when monitoring the quality of the supply voltage is the possibility to detect short sags of the system voltage. To be able to follow the progress of the voltage auto-scaling of the voltage inputs should be deactivated. Thereby you have to consider that a possible swell of the voltage may be detected only up to the configured overriding (20% of rated voltage in the above example), because the switching of the measurement range is locked in both directions.

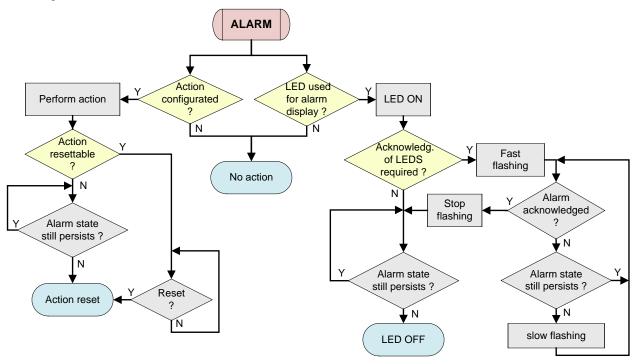
This applies analogously to all quantities of the system, whose progress should be monitored. For power quantities the voltage amplification as well as the current amplification is influenced. However, which basic quantities may vary how much can differ from application to application.

3.3 Monitoring and alarming

The logic module integrated in the APLUS is a powerful feature to monitor critical situations without delay on device side. By implementing this local intelligence a safe monitoring can be realized which is independent of the readiness of the control system.

3.3.1 Alarming concept

How alarms are handled is decided during the configuration of the device. For that in the logic module you can define if LED's are used for alarm state display and how resp. when a possibly activated action, such as the switching of a relay, will be reset. These configuration parameters are highlighted in yellow in the following chart.



► Acknowledgment: This procedure affects the state of the LED only

If an alarm state is visualized via LED, its occurence must be acknowledged via display (see: Acknowledgment of alarms via display), no matter if it is still active (fast flashing) or has dropped-out already (slow flashing). By acknowledging an alarm, only the flashing of the LED stops, but a reset of the alarm action is performed only if the display is configured as a possible source for alarm reset.



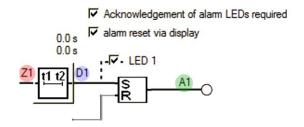
Acknowledgment is not required if "acknowledgement of alarm LEDs required" in the logic module configuration is not selected.

▶ Alarm reset: This procedure affects the states of the follow-up action and the LEDs

If an alarm state occurs a follow-up action (e.g. the switching of a relay) can be triggered. This follow-up action is normally reset as soon as the alarm condition no longer exists. But the alarm handling may be configured as well in a way that only by means of an alarm reset the subsequent operation is withdrawn. This way an alarm remains stored until a reset is performed, even if the alarm situation no longer exists. Possible sources for an alarm reset are the display, a digital input, another logical state of the logic module or a command via the bus interface.

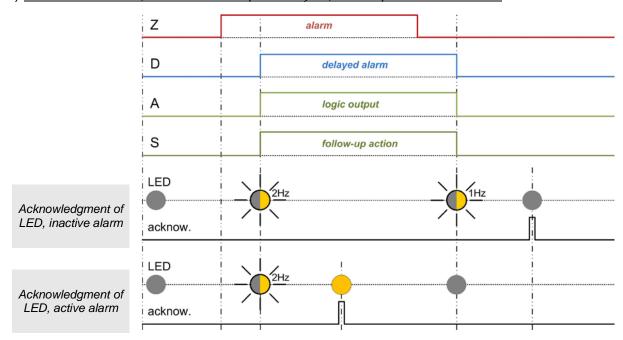
Hint: If an alarm is reset, the alarm state visualized via LED is acknowledged at the same time.

On the next page some signal flow examples are shown.

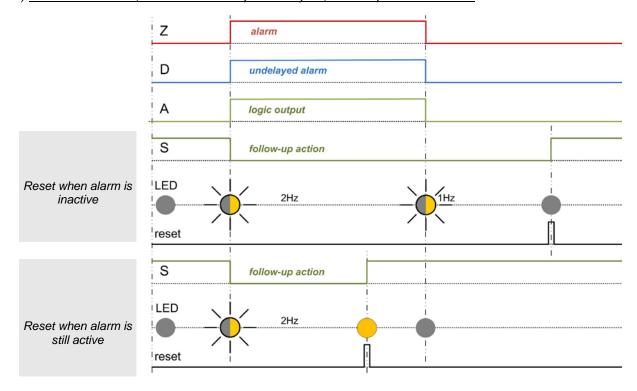


- **Z**: Logic output determined from all involved logic inputs
- **D**: Corresponds to signal Z, delayed by the switch-in resp. dropout delay
- A: Output signal of the logic function
- **S**: State of the subsequent operation (e.g. of a relay), corresponds normally to A, but may be inverted (subsequent operation: relay OFF)

1) Alarm reset inactive, switch-in and dropout delay 3s, follow-up action not inverted

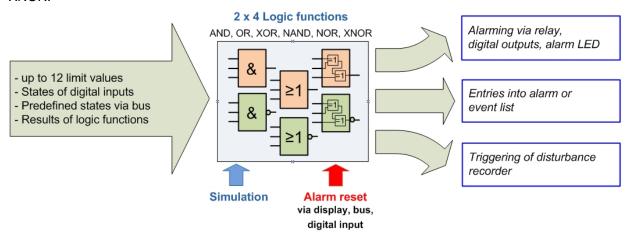


2) Alarm reset active, switch-in and dropout delay 0s, follow-up action inverted



3.3.2 Logic components

The logic outputs are calculated via a two level logical combination of states, which are present at the inputs. Usable components are AND, OR and XOR gates as well as their inversions NAND, NOR and XNOR.



The principal function of the logical gates is given in the following table, for simplicity shown for gates with two inputs only.

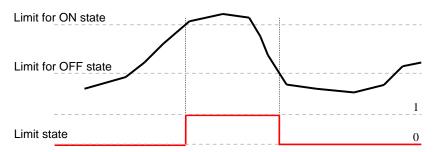
function	symbol	older symbols ANSI 91-1984 DIN 40700 (alt)		truth table	plain text
AND	A — & B — Y	A B	₽ B	A B Y 0 0 0 0 1 1 0 1 1 1	Function is true if all input conditions are fulfilled
NAND	A — & D—Y	A D	A B	A B Y 0 0 1 0 1 1 1 0 1 1 1 0	Function is true if at least one of the input conditions is not fulfilled
OR	A ≥1 Y	A Y	A Y	A B Y 0 0 0 0 1 1 1 0 1 1 1 1	Function is true if at least one of the input conditions is fulfilled
NOR	A	A DOY	A Y	A B Y 0 0 1 0 1 0 1 0 0 1 1 0	Function is true if none of the input conditions is fulfilled
XOR	A =1 Y	A B	A #	A B Y 0 0 0 0 1 1 1 0 1 1 1 0	Function is true if exactly one of the input conditions is fulfilled
XNOR	A =1 0-Y	A B	A I	A B Y 0 0 1 0 1 0 1 0 0 1 1 1	Function is true if all of the input conditions are fulfilled or all conditions are not fulfilled

The logic components of the first level may combine up to three, the components of the second level up to four input conditions. If individual inputs are not used, their state is automatically set to a condition which has no influence on the logic result.

3.3.3 Limit values

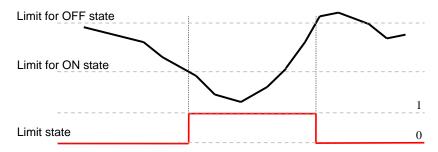
States of limit values are the most important input quantities of the logic module. Depending on the application, limits either monitor the exceeding of a given value (upper limit) or the fall below a given value (lower limit). Limits are defined by means of two parameters, the limit for the ON and the limit for the OFF state. The hysteresis is the difference between these two values.

Upper limit: The limit for ON state (*L.D.*) is higher than the limit for the OFF state (*L.D.*)



- ► The state 1 (true) results if the limit for ON state is exceeded. It remains until the value falls below the limit for OFF state again.
- ► The state 0 (false) results if the limit for ON state is not yet reached or if, following the activation of the limit value, the value falls below the limit for OFF state again.

Lower limit: The limit for ON state (*L.D.p.*) is smaller than the limit for OFF state (*L.D.F.F.*)



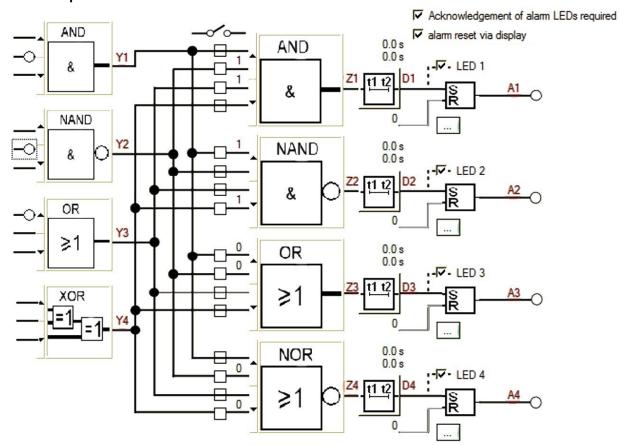
- ► The state 1 (true) results if the value falls below the limit for ON state. It remains until the value exceeds the limit for OFF state again.
- ► The state 0 (false) results if the value is higher than the limit for ON state or if, following the activation of the limit value, the value exceeds the limit for OFF state again.



If for a limit value the limit for ON state and the limit for OFF state are configured to the same value, it will be treated as an upper limit value with a hysteresis of 0%.

Limit values may be used to control the running of **operating hour counters**. As long as the limit values are fulfilled (logical 1) the operating hour counters keep on running. Not only operating times may be measured, but e.g. time under overload condition (additional stress) as well.

3.3.4 Sequence of evaluation



The evaluation of the logic module is performed from top to bottom and from left to right:

- 1. Y1, Y2, Y3, Y4
- 2. Z1, Z2, Z3, Z4
- 3. D1, D2, D3, D4
- 4. A1, A2, A3, A4
- ▶ The evaluation is performed once each cycle of the power frequency, e.g. every 20ms at 50Hz. But the time between two evaluations will never be longer than 25ms.
- ▶ If the logical states Y1...Y4, Z1...Z4, D1...D4 and A1...A4 are used as inputs, their changed states will be included in the evaluation of the next interval
- ► Exception: In the first evaluation level the state of previous logical functions may be used as input without delay, e.g. the state Y1 for the logical functions with output Y2, Y3 or Y4.

3.4 Free Modbus image

Accessing measured data of a Modbus device often needs some special effort, if the interesting measurements are stored in different, non continuous register areas. This way multiple telegrams must be sent to the device to read all data. This needs time and it's very likely, that the measurements don't originate from the same measurement cycle.

A free assembly of the data to read helps a lot. The APLUS supports, along with the still available classical Modbus image with thousands of registers, the facility to assemble two different images, which may be read with one telegram only. These freely assembled images are refreshed after each measurement cycle and therefore always provide the most present values.

The free float image

Up to 60 instantaneous, mean, unbalance or THD/TDD values may be arranged in any sequence on the register addresses 41840-41958. All of these values are floating point numbers, which allocate 2 registers per value. Meter values are not possible because they have another format.

The free integer image

Some older control systems are not able to handle float values. To make it possible to work with the data of the device up to 20 16-Bit integer values can be derived from the existing measurement values. These values will then be stored in the free Modbus image (register 41800 up to 41819) as integer values with selectable range of values.

Example: Current transformer 100/5A, measurement current phase 1, over range 20%

- ► The reference value is 120A (maximum measurable current)
- ▶ The integer value shall be 12'000 if the measurement is 120A

After selecting the measured quantity and entering the register value of 12'000 automatically a scaling factor of 100.0 is calculated. The measurement I1 therefore will be multiplied by 100.0 before it is converted into an integer value and stored in the Modbus image.

Also in the integer image instantaneous, mean, unbalance or THD/TDD values may be arranged.



For devices with Profibus interface the Modbus image is used for the assembly of the cyclical telegram. Via Modbus the same image can be used, but it's not possible to use it independently.

The Modbus communication of the A*PLUS* is described in a separate document. Depending on the communication hardware selected, either the manual for Modbus/RTU or Modbus/TCP protocol should be used. These documents can be downloaded via our homepage http://www.camillebauer.com.

- ► W157 695: Modbus/RTU interface APLUS (communication interface RS485)
- ► W162 636: Modbus/TCP interface APLUS (communication interface Ethernet)

4. Mechanical mounting

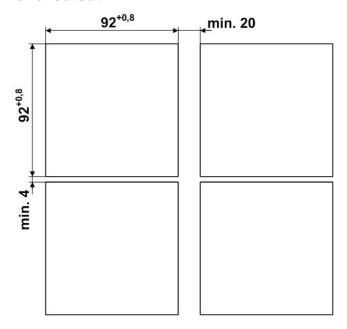
- ▶ The standard version of the APLUS is designed for panel mounting as shown below
- ► The version without display with top-hat rail adapter may be clipped onto a top-hat rail according to EN50022



Please ensure that the operating temperature limits are not exceeded when determining the place of mounting (place of measurement):

-10 ... 55°C

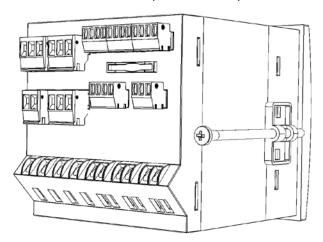
4.1 Panel cutout



Dimensional drawing APLUS: See section 10

4.2 Mounting of the device

The APLUS is suitable for panel widths up to 10mm.



- a) Slide the device into the cutout from the outside
- b) From the side slide in the mounting clamps into the intended openings and pull them back about 2 mm
- c) Tighten the fixation screws until the device is tightly fixed with the panel

4.3 Demounting of the device

The demounting of the device may be performed only if all connected wires are out of service. Remove all plug-in terminals and all connections of the current and voltage inputs. Pay attention to the fact, that current transformers must be shortened before removing the current connections to the device. Then demount the device in the opposite order of mounting (4.2).

5. Electrical connections

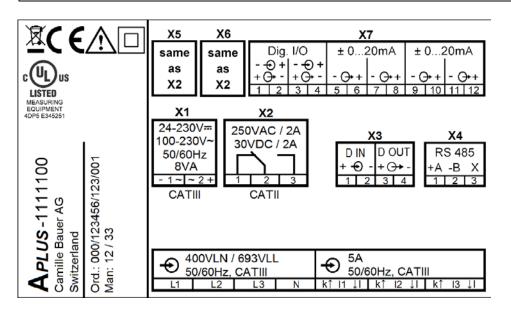


Ensure under all circumstances that the leads are free of potential when connecting them !

5.1 General safety notes

Please observe that the data on the type plate must be adhered to!

The national provisions (e.g. in Germany VDE 0100 "Conditions concerning the erection of heavy current facilities with rated voltages below 1000 V") have to be observed in the installation and material selection of electric lines!



Nameplate of a device equipped with RS485 interface and I/O extension 1

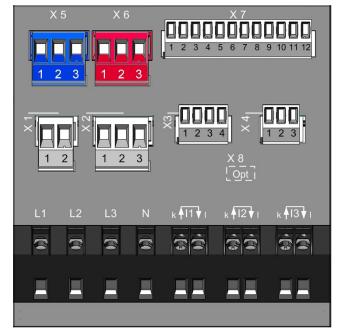
Symbol	Meaning
	Device may only be disposed of in a professional manner!
	Double insulation, device of protection class 2
CE	CE conformity mark. The device fulfills the requirements of the applicable EU directives.
CULUS	Products with this mark comply with both the Canadian (CSA) and the American (UL) requirements.
\triangle	Caution! General hazard point. Read the operating instructions.
→	General symbol: Input
\bigcirc	General symbol: Output
CAT III	Measurement category CAT III for current / voltage inputs and power supply
CAT II	Measurement category CAT II for relay outputs

5.2 Electrical connections of the I/Os

I/O no.	Terminal	No.	APLUS	I/O extension 1	I/O extension 2
1	X2	1, 2, 3	Relay		
2	Х3	1, 2	Digital input		
3	Х3	3, 4	Digital output		
4	X5	1, 2, 3		Relay	Relay
5	X6	1, 2, 3		Relay	Relay
6	X7	1, 2		Digital I/O	Digital I/O
7	X7	3, 4		Digital I/O	Digital I/O
8	X7	5, 6		Analog output ±20mA	Digital I/O
9	X7	7, 8		Analog output ±20mA	Digital I/O
10	X7	9, 10		Analog output ±20mA	Digital I/O
11	X7	11, 12		Analog output ±20mA	Digital I/O

I/O no. - as used in the CB-Manager software

5.3 Possible cross sections and tightening torques



Inputs L1, L2, L3, N, I1 k-I, I2 k-I, I3 k-I Single wire 1 x 0,5 ... 4,0mm² or 2 x 0,5 ... 2,5mm² Multiwire with end splices 1 x 0,5 ... 2,5mm² or 2 x 0,5 ... 1,5mm² Tightening torque 0,5...0,6Nm resp. 4,42...5,31 lbf in Power supply X1, Relays X2, X5, X6 1 x 0,5 ... 2,5mm² or 2 x 0,5 ... 1,0mm² Multiwire with end splices 1 x 0,5 ... 2,5mm² or 2 x 0,5 ... 1,5mm² Tightening torque 0,5...0,6Nm resp. 4,42...5,31 lbf in I/O's X3, X7 and RS485 connector X4 Single wire 1 x 0,5 ... 1,5mm² or 2 x 0,25 ... 0,75mm² Multiwire with end splices 1 x 0,5 ... 1,0mm² or 2 x 0,25 ... 0,5mm² Tightening torque

0,2...0,25Nm resp. 1,77...2,21 lbf in

5.4 Inputs



All voltage measurement inputs must originate at circuit breakers or fuses rated 10 Amps or less. This does not apply to the neutral connector. You have to provide a method for manually removing power from the device, such as a clearly labeled circuit breaker or a fused disconnect switch.

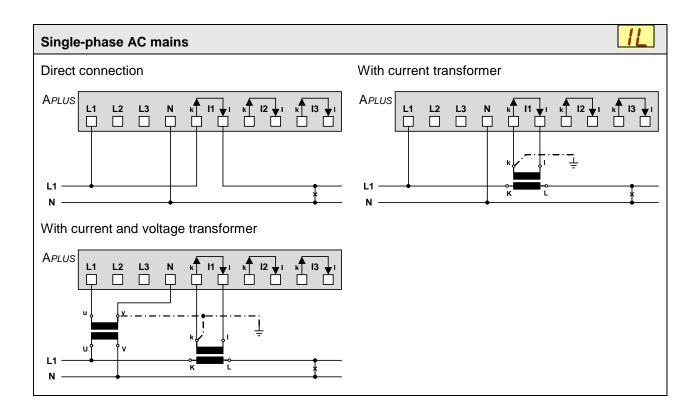
When using **voltage transformers** you have to ensure that their secondary connections never will be short-circuited.

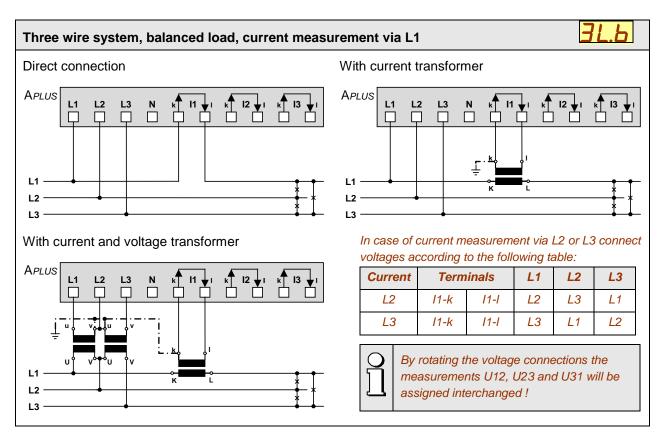


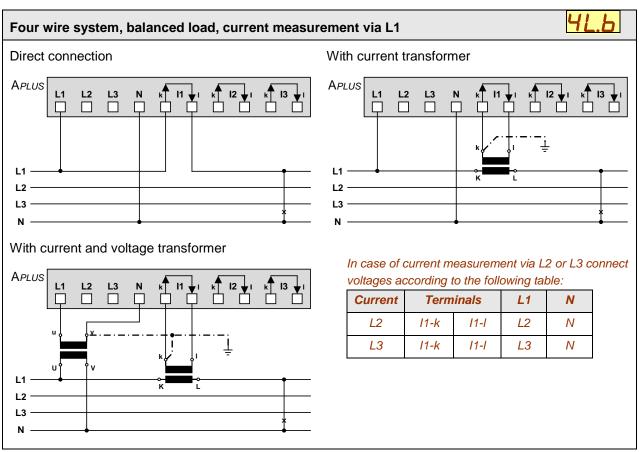
No fuse may be connected upstream of the current measurement inputs!

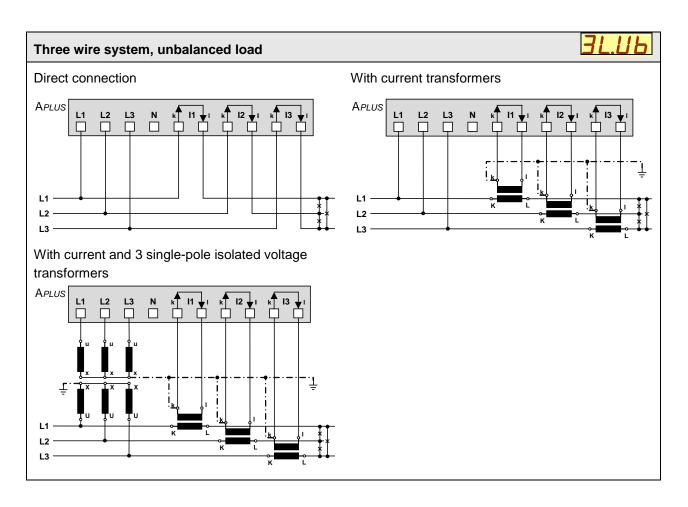
When using **current transformers** their secondary connectors must be short-circuited during installation and before removing the device. Never open the secondary circuit under load.

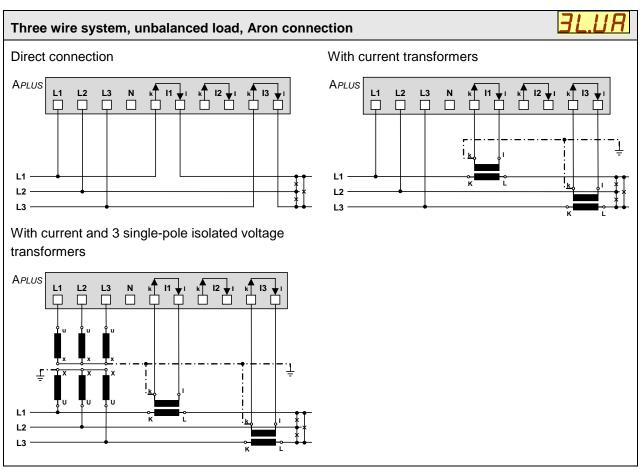
The connection of the inputs depends on the configured system (connection type). The required device external fusing of the voltage inputs is not shown in the following connection diagrams.

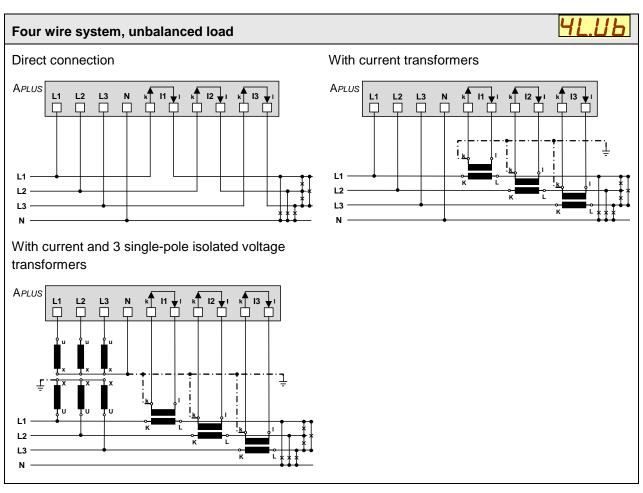


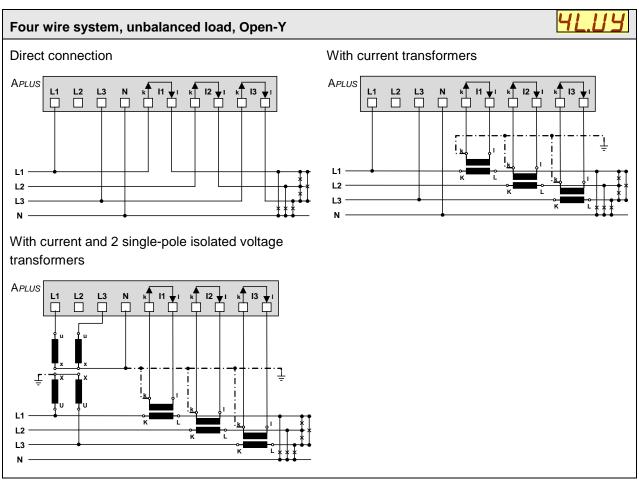


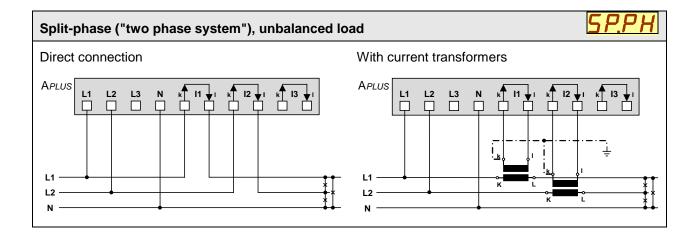






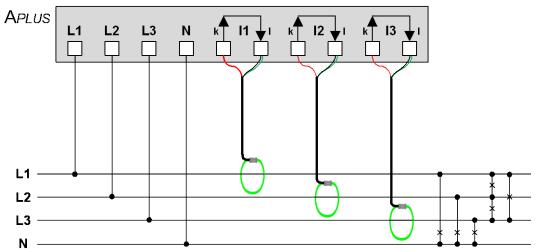






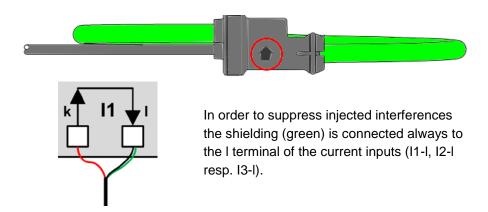
5.5 Rogowski current inputs

The connection of the Rogowski coils is performed depending on the selected system type, as shown in chapter 5.4 above. However, instead of current transformers a Rogowski coils is placed around each current-carrying conductor. This is subsequently shown for the measurement in a 4-wire low-voltage system.





When connecting the coils you must follow the safety notices given in the operating instructions of the Rogowski coil. The current direction shown on the coils must match the real current direction and has to be the same for all phases.



5.6 Power supply

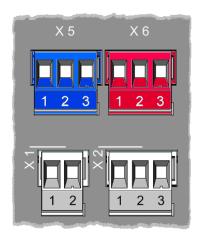


A marked and easily accessible current limiting switch has to be arranged in the vicinity of the device for turning off the power supply. Fusing should be 10 Amps or less and must be rated for the available voltage and fault current.

5.7 Relays

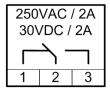


When the device is switched off the relay contacts are de-energized, but dangerous voltages may be present.



The relay X2 is part of the basic unit and therefore always available. The relays X5 and X6 are provided for device versions with I/O extension PCB only.

The plug-in terminals have different colours to prevent mixing up the connections. The pin assignment is the same for all relays:

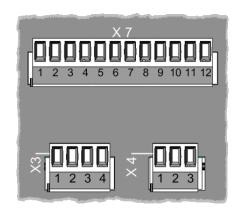


5.8 Digital inputs and outputs

For the digital inputs / outputs an external power supply of 12 / 24V DC is required.



The power supply shall not exceed 30V DC!

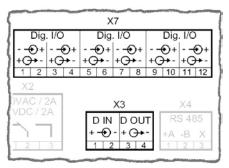


The plug-in terminal X7 is available for device versions with I/O extension PCB only.

The number of digital inputs / outputs varies depending on the optional built-in PCB, see nameplate. The operating direction of the digital I/Os on X7 may be individually selected by means of the PC software.



The assignment of the connections depends on whether an I/O is configured to be a digital input or a digital output.



Example

Device with I/O extension 2 (2 relays + 6 digital I/Os)

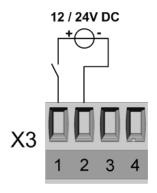
The digital I/Os on **plug-in terminal X7** are individually programmable as input

or output

On **plug-in terminal X3** a digital input and a digital output are provided statically. Their operating direction may not be modified.

Usage as digital input

- ► Meter tariff switching
- ▶ Operating feedback of loads for operating time counters
- ► Trigger and release signal for logic module
- ► Pulse input for meters of any kind of energy
- ► Clock synchronization
- ► Synchronization of billing intervals in accordance with energy provider



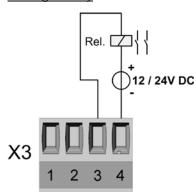
Technical data

Input current < 7,0 mACounting frequency (S0) $\le 16 \text{ Hz}$ Logical ZERO - 3 up to + 5 VLogical ONE 8 up to 30 V

Usage as digital output

- ► Alarm output for logic module
- ► State reporting
- ▶ Pulse output to an external counter (acc. EN62053-31)
- ► Remote controllable state output via bus interface

Driving a relay



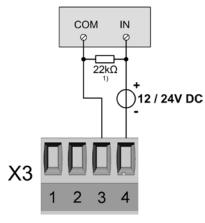
Technical data

Rated current 50 mA (60 mA max.)

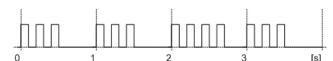
Switching frequency (S0) \leq 20 Hz Leakage current 0,01 mA Voltage drop < 3 V

Load capacity $400 \Omega \dots 1 M\Omega$

Driving a counter mechanism



1) Recommended if input impedance of counter > 100 k Ω



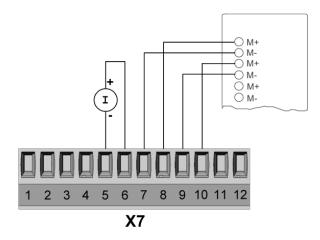
The width of the energy pulses can be selected by means of the PC software but have to be adapted to the counter mechanism. Once a second there is a decision how many pulses have to be output. Therefore the delay between two pulses may not be used to determine the present power demand.

Electro mechanical meters typically need a pulse width of 50...100ms.

Electronic meters are partly capable to detect pulses in the kHz range. There are the types NPN (active negative edge) and PNP (active positive edge). For the APLUS a PNP type is required. The pulse width has to be at least 30ms (acc. EN62053-31). The delay between to pulses corresponds at least to the pulse width. The smaller the pulse width, the higher the sensitivity to disturbances.

5.9 Analog outputs

Analog outputs are available for devices with I/O extension 1 only. See nameplate.



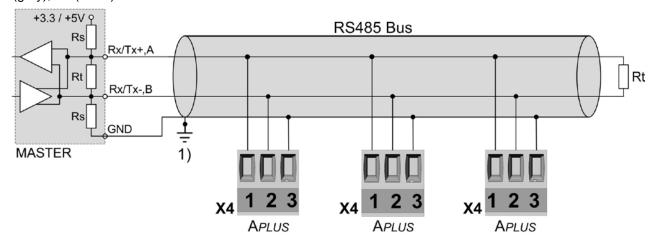
Connection to an analog input card of a PLC or a control system

The APLUS is an isolated measurement device. In addition the particular outputs are galvanically isolated. To reduce the influence of disturbances shielded a twisted-pair cables should be used. The shield should be connected to earth on both opposite ends. If there a potential differences between the ends of the cable the shield should be earthed on one side only to prevent from equalizing currents.

Under all circumstances consider as well appropriate remarks in the instruction manual of the system to connect.

5.10 Modbus interface RS485 X4 and / or X8

Depending on the device version up to two Modbus interfaces are available on the plug-in positions X4 and / or X8. These are galvanically isolated. The connection terminals are distinguished by color: X4 (gray), X8 (black).



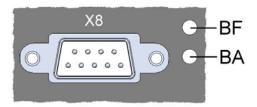
- One ground connection only. This is possibly made within the master (PC).
- Rt: Termination resistors: 120 Ω each for long cables (> approx. 10 m)
- Rs: Bus supply resistors, 390Ω each

The signal wires (X4-1, X4-2 resp. X8-1, X8-2) have to be twisted. GND (X4-3 resp. X8-3) can be connected via a wire or via the cable screen. In disturbed environments shielded cables must be used. Supply resistors (Rs) have to be present in bus master (PC) interface. Stubs should be avoided when connecting the devices. A pure daisy chain network is ideal.

You may connect up to 32 Modbus devices to each bus. A proper operation requires that all devices connected to the respective bus have equal communication settings (baud rate, transmission format) and unique Modbus addresses. If there are two Modbus interfaces, their settings may be different.

The bus system is operated half duplex and may be extended to a maximum length of 1200 m without repeater.

5.11 Profibus DP interface



The 9-pin DSUB socket serves the connection of a standard Profibus plug. In a bus terminal device, the bus line must be terminated with resistors in the bus plug. Then standard pin assignment is as follows:

Pin	Name	Description
3	В	RxD/TxD-P
4	RTS	Request to send: CNTR-P (TTL)
5	GND	Data ground
6	+5V	VP
8	Α	RxD/TxD-N

LED BF (Bus failure, yellow)

Status Description	
ON	Startup state or internal communication error
Flashing (2Hz)	Parameterization check failed
OFF	Cyclical operation; no error

LED BA (Bus alive, green)

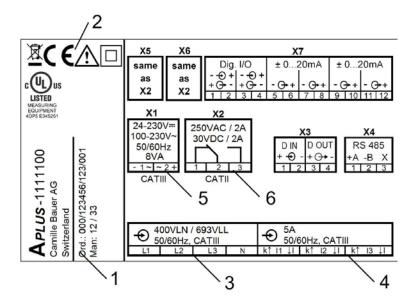
Status	Description
OFF	Startup state; no Profibus communication
Flashing (2Hz)	Profibus detected; waiting for parameterization from master
ON	Parameterization ok; Profibus communication active

6. Commissioning



Before commissioning you have to check if the connection data of the transducer match the data of the plant (see nameplate).

If so, you can start to put the device into operation by switching on the power supply and the measurement inputs.



- Measurement input
 Input voltage
 Input current
 System frequency
 - 1 Works no.
 - 2 Test and conformity marks
 - 3 Assignment voltage inputs
 - 4 Assignment current inputs
 - 5 Assignment power supply
 - 6 Load capacity relay outputs

6.1 Software installation CB-Manager

A complete parametrization of the device is possible via configuration interface only, using the PC software CB-Manager. The software may be downloaded free of charge from our homepage http://www.camillebauer.com.



The file "Read-me-first" provides all necessary information for the installation of the CB-Manager software and assistance for possible problems.

Functionality of the CB-Manager software

The software is primary a tool for the configuration of different devices (APLUS, CAM, VR660, A200R, V604s) and supports the user during commissioning and service. It allows as well the reading and visualization of measured data.

- ► Acquisition and modification of all device features
- ► Setting of real-time clock and time zone, selection of time synchronization method
- ► Archiving of configuration and measurement files
- ► Visualization of present measurements
- ► Reading, setting and resetting of meters
- ► Reading and resetting of minimum/maximum values
- ► Starting, stopping and resetting of the optional data logger
- ▶ Recording of measurement progressions during commissioning
- ► Check for correct device connection
- ▶ Simulation of states or outputs to test subsequent circuits
- ► Adjust the security system as protection against unauthorized access or manipulations

The CB-Manager software provides a comprehensive help facility, which describes in detail the operation of the software as well as all possible parameter settings.

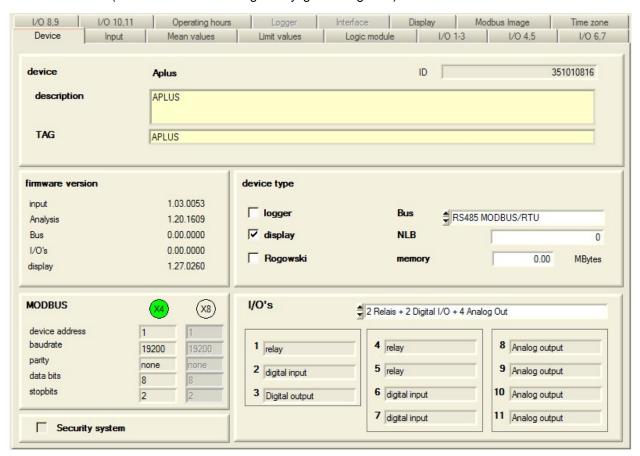
6.2 Parametrization of the device functionality

Operating the software

The device configuration is divided into registers, which contain thematically the different function blocks of the device, e.g. "input", "limit values", "display". Thereby of course there are interdependencies, which have to be considered. If e.g. a current limit value is defined and subsequently the ratio of the current transformer is changed, there is a high probability that the limit value is changed as well. Therefore a meaningful sequence must be kept during setting the parameters. The easiest way is to handle register by register and line by line:

- ▶ Device (set the device version, if not read directly from the device)

 If an I/O extension unit is used: Fix the data direction of the digital I/O's. Do to so just click on the appropriate entry and change the data direction in the I/O register. So it's assured that these I/O's can be used in the intended way. If e.g. you miss to change de basic setting "digital input" the appropriate channel can't be used as output in the logic module.
- ▶ Input, especially system and transformer ratios
- ► Mean values >> Limit values >> Logic module >> I/O 1-3
- ▶ if present: I/O 4,5 >> I/O 6,7 >> I/O 8,9 >> I/O 10,11
- **▶** Operating hours
- ▶ if present: Logger >> Interface (Ethernet, Profibus DP) >> Display
- ▶ Modbus-Image (if you want to define your own Modbus image)
- ► Time zone (for automatical handling of daylight saving time)



ONLINE / OFFLINE

The parametrization may be performed ONLINE (with existing connection to the device) or OFFLINE (without connection to the device). To perform an ONLINE configuration first the configuration of the connected device, and therewith its hardware version, is read. A modified configuration can then be downloaded to the device and stored on the hard disk of the computer for archiving.

An OFFLINE parametrization can be used to prepare device configurations, to store them on disk and to download it to the devices, once you are in the field where the devices are installed. To make this work, the device versions selected during parametrization must agree with the versions on site.

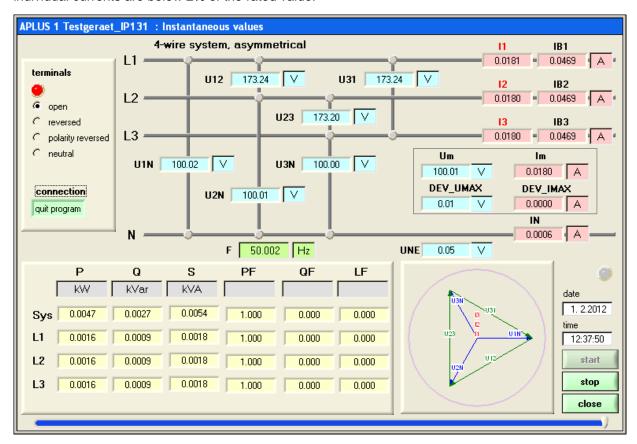
6.3 Installation check

Check if inputs are connected correctly

► Voltage (at least 20% U_{rated}) and current (at least 2% I_{rated}) must be present

Using the connection check, which is integrated in the visualization of the instantaneous values, the correct connection of the current and voltage inputs may be checked. The phase sequence will be checked, as well as if there are open connections or reversed current connections (which change the direction of the current).

The image below shows open current connections (red description I1, I2, I3). This arises because the individual currents are below 2% of the rated value.



Simulation of I/O's

To check if subsequent circuits will work properly with the measurement data provided by the APLUS all analog, digital and relay outputs may be simulated, by predefining any output value resp. discrete state by means of the CB-Manager software.

Also all functions of the logic module, which allows performing any combination of logical states, may be predefined. This way e.g. an alarming due to a violation of a limit value can be simulated.

6.4 Installation of Ethernet devices

6.4.1 Connection

Before devices can be connected to an existing Ethernet network, you have to ensure that they will not disturb the normal network service. The rule is:



None of the devices to connect is allowed to have the same IP address than another device already installed

The factory setting of the IP address of APLUS is: 192.168.1.101

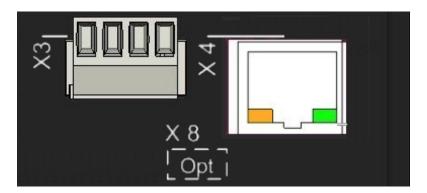
The standard RJ45 connector serves for direct connecting an Ethernet cable. If the PC is directly connected to the device a cross-wired cable must be used.

The network installation of the devices is done by means of the CB-Manager software (see $\underline{6.4.2}$) or directly via the local programming on the display. As soon as all devices have a unique network address they may be accessed by means of a suitable Modbus master client.

Interface: RJ45 connector, Ethernet 100BaseTX

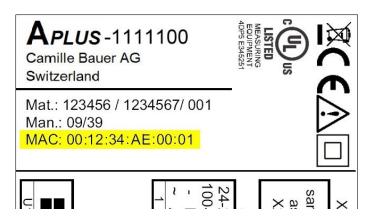
Mode: 10/100 MBit/s, full / half duplex, Auto-negotiation

Protocols: Modbus/TCP, NTP



Function of the LED's

LED 1 (Green)	ON as soon as a network connection exists
LED I (OICCII)	Flashing when data is transmitted via Ethernet connection
LED 2 (Orange)	Flashing with 4 Hz during start-up
LLD 2 (Orange)	ON during Modbus/TCP communication with the device



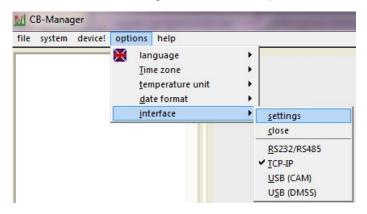
To have a unique identification of Ethernet devices in a network, to each connection a unique MAC address is assigned. This address is given on the nameplate, in the example 00-12-34-AE-00-01.

Compared to the IP address, which may be modified by the user any time, the MAC address is statically.

6.4.2 Network installation using the CB-Manager software

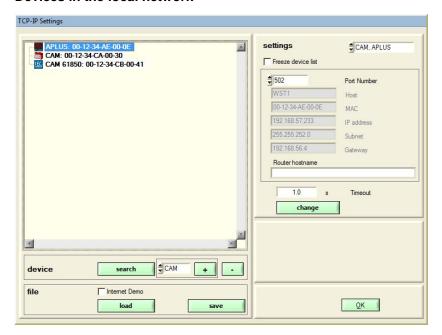
For the subsequent Modbus/TCP communication a unique network address must be assigned to each of the devices. This can be done very easily, using the CB-Manager software to search for devices which have a MAC address 00-12-34-AE-xx-xx, which identifies the device as APLUS of Camille Bauer. Because this is performed by means of a UDP broadcast telegram, the devices are allowed to have the same network address at the beginning, e.g. "192.168.1.101" as factory default.

As soon as to all the devices network settings with unique IP address have been assigned, they may be accessed and read using the Modbus/TCP protocol.



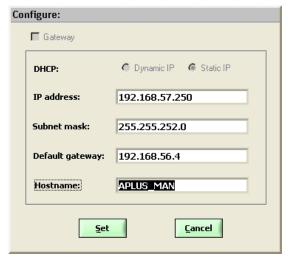
Select "settings" under options | interface. The interface type has to be set to "TCP-IP".

Devices in the local network



Set settings to "CAM, APLUS". Along with all APLUS also SINEAX CAM devices installed in the same network will be shown. The identification of the devices is possible by means of their MAC address, which is given on the nameplate (see chapter 6.4.1).

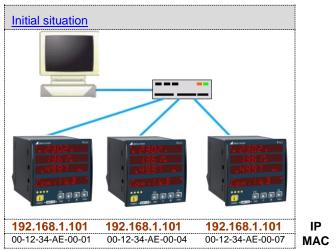
To assign a **unique** network address to a device, select it in the list and the click on "**change**".

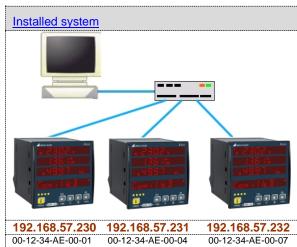


The following settings have to be arranged with the network administrator:

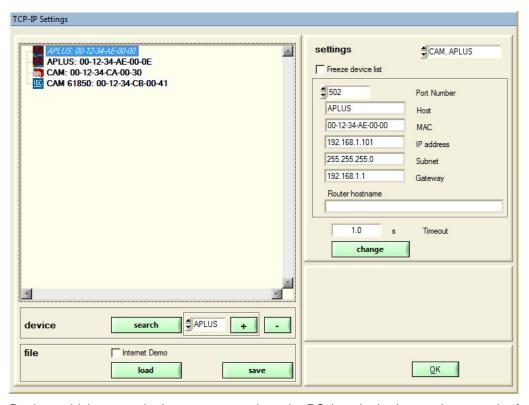
- IP address: This one must be unique, i.e. may be assigned in the network only once.
- Subnet mask: Defines how many devices are directly addressable in the network. This setting is equal for all the devices.
- Default gateway: Is used to resolve addresses during communication between different networks.
 Should contain a valid address within the own network.
- Hostname: Individual designation for each device.
 Helps to identify the device in the device list.

Example





Devices outside the local network



Devices which are not in the same network as the PC (e.g. in the Internet) can not be found and have to be added manually to the device list by means of ____. The type of the device must be selected previously. To each entry you have to assign a unique IP and MAC address, which are different from the initial value. Otherwise it's not possible to add further entries.

The setting of the network parameters must be performed before mounting the device. As an alternative this may be done in the destination network via Ethernet interface.

6.4.3 Network installation by means of local programming

The network settings IP address, subnet mask and gateway can also be configured directly via the local programming of the APLUS on site.

This facility is shown in chapter 7.8

6.4.4 Time synchronization via NTP-protocol

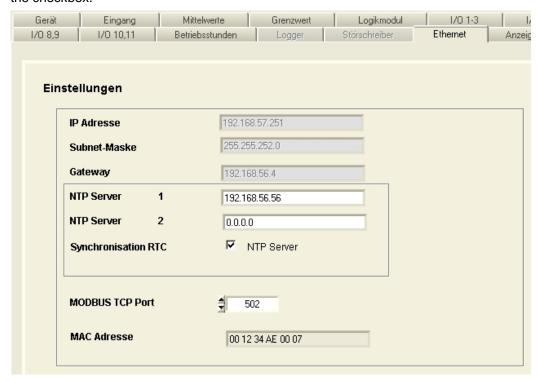
For the *time synchronization* via Ethernet *NTP* (Network Time Protocol) is the standard. Corresponding time servers are used in computer networks, but are also available for free via Internet. Using NTP it's possible to hold all devices on a common time base.

Two different NTP servers may be defined. If the first server is not available the second server is used for trying to synchronize the time. Adjusting of the clock is performed in the interval selected (15min. up to 24h). If no time synchronization is desired, to both NTP servers the address 0.0.0.0 have to be assigned.

The setting of the addresses is done by means of the CB-Manager software. The NTP data is arranged in the register "Ethernet" of the device configuration.

Activation

To activate the time synchronization via NTP, the "Synchronisation RTC" must be checked by means of the checkbox.



6.4.5 TCP ports for data transmission

TCP ports

The TCP communication is done via so-called ports. The number of the used port allows determining the type of communication. As a standard Modbus/TCP communication is performed via TCP port 502, NTP uses port 123. However, the port for the Modbus/TCP telegrams may be modified. You may provide a unique port to each of the devices, e.g. 503, 504, 505 etc., for an easier analysis of the telegram traffic. The setting of the Modbus TCP port is done as shown above. Independent of these setting a communication via port 502 is always supported. The device allows at least 5 connections to different clients at the same time.

Firewall

Due to security reasons nowadays each network is protected by means of a firewall. When configuring the firewall you have to decide which communication is desired and which have to be blocked. The TCP port 502 for the Modbus/TCP communication normally is considered to be unsafe and is very often disabled. This may lead to a situation where no communication between networks (e.g. via Internet) is possible.

6.5 Installation of Profibus DP devices

The Profibus DP interface allows data exchange with a control system via Profibus-DP V0. The modular device model provides maximum protocol efficiency.

Required measured variables are determined during engineering and arranged as a fixed process image. The control system does not require any intelligence for the evaluation of the data (no tunneling protocol).

Bus parameterising facilitates simple and fast commissioning. On-site the parameters in accordance with the <u>configuration menu</u> can be set, especially:

- Device address
- Accepting master parameterization (Check_User_Prm)
- Establishing communication to the master (Go_Online)
- Setting device address via master (Set_Slave_Addr_Supp)



For the assembly of the cyclical Profibus telegram the Modbus image is used. Via Modbus the same image can be used, but it's no longer possible to use it independently.

GSD parameterization

Typically the parameterization of the Profibus slave is done on the control system. During startup the APLUS adopts these settings. Doing so the parameterization of the input parameters (input system, transformer ratios etc.) as well as the assembly of the Modbus image will be overwritten. Other parts of the configuration, such as parameterization of I/O's or settings of limit values, remain unchanged.

All necessary informations for the parameterization are part of the DMF (GSD) file. This one can be downloaded from our homepage http://www.camillebauer.com.

The assumption of the engineered parameters can be prevented by deactivating the Check_User_Prm flag. The parameterization locally set will not be changed this way.

Cyclical data exchange

The user can compose its own "station" with all required quantities. Up to 60 measured quantities can be modularly concatenated. You may choose from instantaneous values of the system and imbalance analysis, mean-values of power quantities and freely selectable quantities as well as meter values.

Subsequent to the adoption of the parameterization, the APLUS is ready for the cyclical data exchange with the control system.

6.6 Protection against device data changing

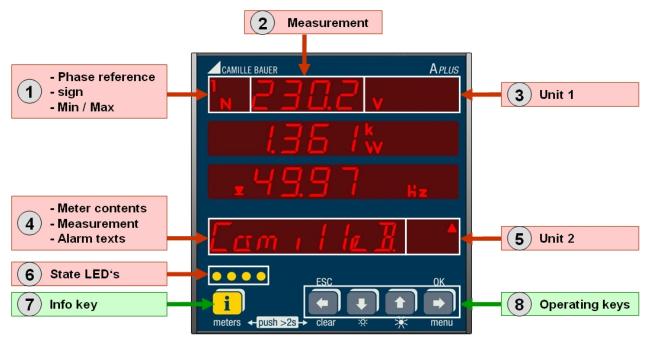
Data stored in the device may be modified or reset via communication interface or via the keys on the device itself. To restrict these possibilities on-site, via CB-Manager the security system in the device can be activated (factory default: not activated). For the definition of these user rights in the software the input of an administrator login is required. The factory default is:

user: admin The administrator password may be modified, but a reset can be performed in our factory only!

For one user via device and one user via interface (special login) the access to the following functions can individually be granted: Configuration of the device, modification of RTC parameters, modification of limit values, reset of min/max or meter values, alarm acknowledgment, display mode changing.

7. Operating the device

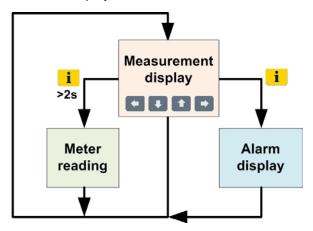
7.1 Display and operating elements



1	12 <u>x</u> 3N <u>x</u>	Phase reference of measurement, sign of measurement, minimum or maximum value, e.g. U _{1N} (maximum value)
2	230.4	4-digit display of measurements. On each change of the measurement display the short form of the quantities to display is shown first.
	oL	If a measurement is out of the measurable range the string "oL" is shown instead of a measured value.
3	kMGΣ%øφ WArHzkk	Unit, measuring procedure, measurement type e.g. kVAr (reactive power)
4	POWER FR	8-digit meter display, 4-digit measurement display (P,Q,S,U,I) or 20-digit Alarm text display (e.g. "POWER FAILURE L1")
(5)	kMG ▲ WArh▼	Unit for meter quantities, high or low tariff, e.g. MWh high tariff Unit for the quantities Px, Qx, Sx, Ux, Ix
6	• • • •	State display of alarms, e.g. Alarm 1 active
7	short	Display of alarm state texts
	i >2s	Reading of meter contents
8	+ + + +	Functionality depends on operating time, either 'short' or > 2s. To be used for measurement selection, brightness adjustment, navigation in menus, reset operations.

7.2 Operating modes

The device supports, along with the <u>configuration mode</u>, three different operating modes. Normally the device is in the measurement display mode, but may be temporarily switched for the reading of the meters or for the display of alarm texts.





Measurement display: Is the normal operating mode of the device. By means of the navigation keys different measurement display can be selected. Depending on the selected display mode and the system monitored different measurement displays are available.

► Available display modes



Meter reading: By pressing the key **i** for a longer time an operating mode is started, which allows to read all the meter contents via line 4. This mode is automatically stopped after 30s without any key pressed or via the key **...** If this mode is active no measurement info is displayed on line 1 to 3.

▶ Meter reading



Alarm display: By shortly pressing the key an operating mode is started, which allows to display alarm state texts and to acknowledge alarms via line 4. If there are no configured alarms the message "No LED used" is displayed and then the mode is stopped. Otherwise the mode is automatically stopped after 30s without any key pressed or via the key. If this mode is active no measurement info is displayed on line 1 to.

- ► Monitoring and alarming
- ► Alarm handling

7.3 Setting the display brightness

The brightness of the display can be set to one of thirteen levels.



Brighter: Press key longer than 2s; brightness will increase in steps

Darker: Press key longer than 2s; brightness will decrease in steps

7.4 Display modes

The device supports four different display modes. They differ in the way measurement data is presented and which measurement data is provided.

► The selection of the display mode is described under Configuration

FULL mode

The measurement images of all displayable data are arranged in a matrix form. The selection is performed by means of the arrow keys:

(*	One image to the left. If first: most right image is displayed
•	Most left image of the next line is displayed. If last: First line.
1	Most left image of the previous line is displayed. If first: Last line.
•	One image to the right. If last: most left image is displayed

The fourth line of each image is allocated to a programmable meter value (METER), which does not change even if another measurement image is selected.

► The complete display matrices are shown in Annex B

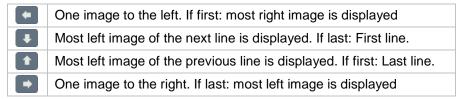
				-
U12	U12_MAX	U12_MIN	DEV_UMAX	
U23	U23_MAX	U23_MIUN	DEV_UMAX_MAX	
U31	U31_MAX	U31_MIN		
METER	METER	METER	METER	
UR1	UNB_UR2_UR1		l.	1
UR2	UNB_UR2_UR1_MAX			
UO				
METER	METER			
11	I1_MAX	IB1	IB1_MAX	DEV_IMAX
12	I2 MAX	IB2	IB2_MAX	
13	I3_MAX	IB3	IB3_MAX	DEV_IMAX_
METER	METER	METER	METER	METER
		WEIEK	METER	METER
IR1	UNB_IR2_IR1			
IR2	UNB_IR2_IR1_MAX			
10				
METER	METER			
P		=		
P_MAX				
METER		•		
Q				
Q_MAX	4			
METER		•		
S				
S_MAX				
METER				
PF	PF	PFG	PFG	1
PF_MIN_IN_L	PF_MIN_OUT_L	PFG_MIN_IN_L	PFG_MIN_OUT_L	
PF_MIN_IN_C	PF_MIN_OUT_C	PFG_MIN_IN_C	PFG_MIN_OUT_C	
METER	METER	METER	METER	
	METER	METER	METER	j
F_MAX F				
F_MIN				
METER				_
Р	U_MEAN	PF	P	
Q	I_MEAN	Р	S	
S	Р	Q	F	
METER	METER	METER	METER	
D	QG			-
D_MAX	QG_MAX			
METER	METER			
dd.mm	OPR_CNTR1	OPR_CNTR	1	
hh.mm	OPR_CNTR2	_		
ss	OPR_CNTR3			
METER	METER	METER		
THD_U12	THD U23	THD_U31	1	
THD_U12_MAX	THD_U23_MAX	THD_U31_MAX		
TTID_UTZ_WAX	TTID_UZU_WAX	TTD_031_WAX		
METER	METER	METER		
TDD_I1	TDD_I2	TDD_I3		
TDD_I1_MAX	TDD_I2_MAX	TDD_I3_MAX		
METER	METER	METER		

Example for 3-wire system, unbalanced load (harmonics and power mean-values not shown)

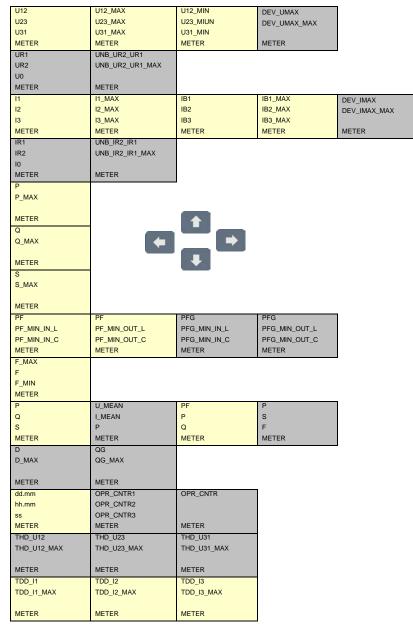
REDUCED mode

This display mode is a reduced version of the FULL mode. Some of the images or complete lines, e.g. the grayed data in the below example, can be hidden. So the display may be adapted easily to the information requirements on-site.

The selection of the measurement images is done via the arrow keys:



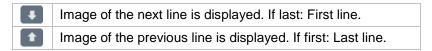
The fourth line of each image is allocated to a programmable meter value (METER), which does not change even if another measurement image is selected.



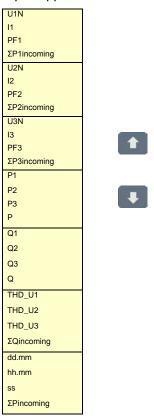
Example for 3-wire system, unbalanced load (harmonics and power mean-values not shown)

USER mode

This display mode allows a free assembly of up to 20 measurement images. Also the fourth line may be different for each image. Any meter value or another quantity (Ux, Ix, Px, Qx, Sx) may be assigned. The images are arranged among each other and selectable via the keys and \blacksquare :



The USER mode also allows defining one of the 20 measurement images to be a predefined image, which is displayed always after a programmable time without user action. This switch back is performed even if in the meantime a change to the FULL or REDUCED mode was performed. This way an always equal appearance of the device can be defined in advance.



Example with 8 free assembled measurement images

LOOP mode

In the LOOP mode all of the measurement images of the USER mode are displayed one after the other with a programmable time delay. When a change to the LOOP mode is performed a possibly active preference display (USER mode) is deactivated. When leaving the LOOP mode the preference display is activated again.



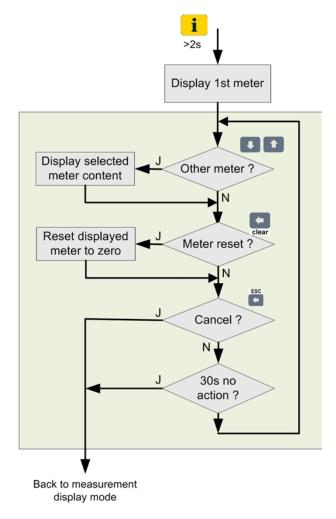
USER and LOOP mode can be activated only, if at least one free measurement image has been defined!

7.5 Meter reading

A reading of the meter contents may be performed at any time, independent of the present selected display mode. When a meter content is displayed it <u>may be reset to zero</u> if the necessary rights have been granted during the configuration of the device.

Start reading: Press key i longer than 2s;

Stop reading: Press key ,;



- ▶ The first displayed meter is always active energy incoming, high tariff
- ▶ Using the keys 🚹 and 🚺 other values from the <u>list of meters</u> may be read as well
- After a time of 30s with no key pressed the meter reading is automatically stopped!

7.6 Alarm handling

How alarms are handled is fixed during the configuration of the device. A detailed description about the alarming concept is here:

► Monitoring und alarming

7.6.1 Alarm state display on the device

The yellow state LED's are intended for alarming and alarm state display on-site. The displayed states are the result of the state information analysis, defined by the user in the logic module. The type of signaling is comparable to the operating philosophy in control rooms.

LED	Meaning
OFF	Alarm is not active
ON	Alarm is active and acknowledged
Fast FLASHING 1)	Alarm is active but not yet acknowledged
Slow FLASHING 1)	Alarm was temporarily active and not yet acknowledged

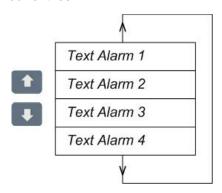
¹⁾ If "acknowledgement of alarm LEDs required" in the logic module configuration is not selected flashing is omitted.



The status display of the LED's is performed only, if the associated logic functions have been configured accordingly

7.6.2 Display of alarm texts

The displayed alarm texts are the result of the state information analysis, defined by the user in the logic module. The number of entries in the alarm text list depends on how many logic functions are used. If no function is used, when changing to the alarm display mode an appropriate error message is displayed and then the mode is terminated immediately. If logic functions are defined, the alarm list may contain up to four entries.



To each alarm a state text for the active and the inactive state is assigned. The table of the present alarm state texts contains, depending on the present state, either the text for the active or the inactive alarm. These may be retrieved and displayed on line 4. The first displayed alarm text after starting the alarm text display is the one with the highest priority (see flow diagram, next page).

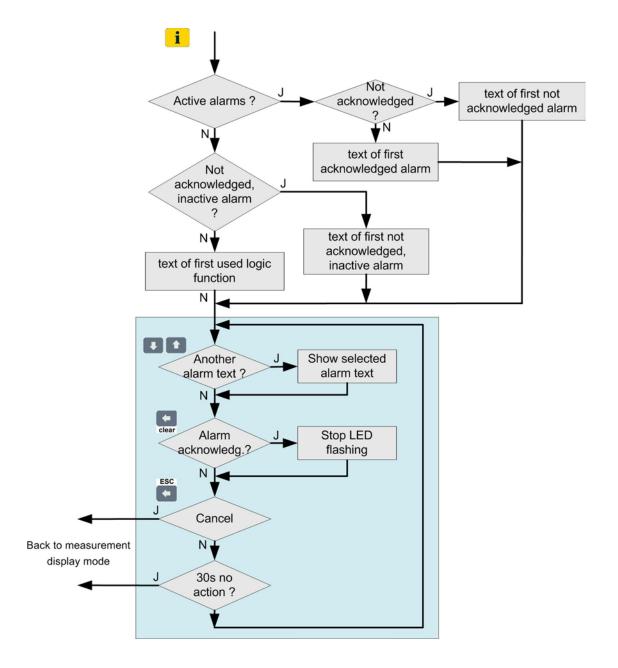
Start alarm text display: Press ishortly;

Stop alarm text display: Press key



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After a time of 30s with no key pressed the display of alarm texts is automatically stopped!



7.6.3 Acknowledgment of alarms via display

Acknowledgment is not required if "acknowledgement of alarm LEDs required" in the logic module configuration is not selected.

The acknowledgment of alarms may be performed via the keys on the device. To do so, the alarm to acknowledge must be actually displayed.

ACKNOWLEDGMENT: Press key [(longer than 2s);

	ear
LED before acknowledgment	LED after acknowledgment
#C: Fast FLASHING	#B: ON
#D: Slow FLASHING	#A: OFF

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If the display is configured for alarm reset, the acknowledgment also undoes the possible alarm operation (e.g. the switching of a relay).

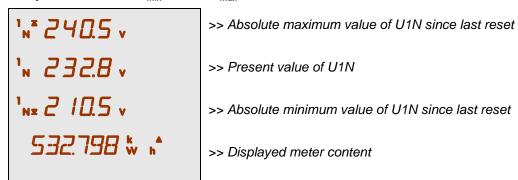
7.7 Resetting of measurements

The APLUS provides minimum and maximum values of different measured quantities as well as energy meters and operating hour counters. All of them may be reset during operation.

Basic principle

RESET: Press key (longer than 2s) while the quantity to reset is displayed

Example: Reset of U1N_{min} and U1N_{max}



>> Absolute maximum value of U1N since last reset

0:	Initial	Initial position as shown above		
1:	clear	240.5V starts flashing, on line 4 CLERR? is flashing as well		
2a:	ok →	Confirm reset of U1N _{max} , go to 3		
2b:	•	No reset of U1N _{max} , go to 3		
2c:	ESC	Cancel the reset procedure, go to 4		
3:		210.5V starts flashing, on line 4 LERR? is flashing as well		
3a:	ok →	Confirm reset of U1N _{min} , go to 4		
3b:		Cancel the reset procedure, go to 4		
4:	Resetting done			

Example: Reset of meter content

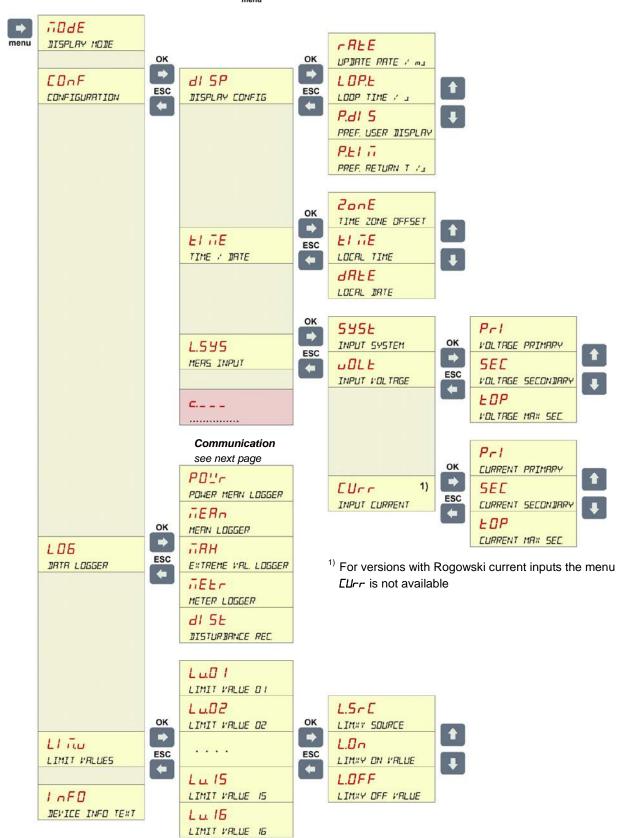
0:	Displa	Display the meter to reset, see Meter reading	
1:	clear	On line 4 LLEAR? is displayed flashing	
1a:	ok →	Confirm meter reset, go to 2	
1b:	ESC	Cancel meter reset, go to 2	
2:	Rücksetzen beendet		

Resetting of measurements may be protected via the security system implemented in the device. For further information see protection against device data changing.

7.8 Configuration

A complete configuration of the APLUS is possible via CB-Manager software only using the configuration interface of the device. On device side only the parameters described below may be modified. To do so, a configuration menu is provided.

Starting the configuration menu: Press (longer than 2s);



Overview of the navigation structure

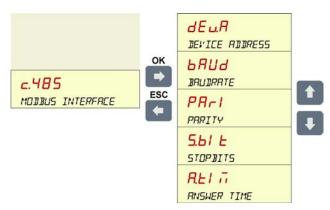
Communication interface c.___

The possible settings depend on the device version selected. The following combinations may be available:

Bus connection	Menu 1	Menu 2
RS-485 (Modbus/RTU protocol)	c.485	
Ethernet (Modbus/TCP protocol)	cE+h	
RS-485 (Modbus/RTU protocol) + Profibus DP	c.485	cPr0
RS-485 (Modbus/RTU protocol) + RS-485 (Modbus/RTU protocol)	c.485	<u>a</u> 485
Ethernet (Modbus/TCP protocol) + RS-485 (Modbus/RTU protocol)	cE+h	<i>¤</i> 485

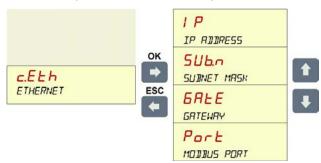
► RS-485 (Modbus/RTU interface)

A maximum of two RS-485 interfaces (X4 and / or X8) with Modbus/RTU protocol can be implemented in the device. These interfaces are independent. Their settings may be different, because they are not used in the same Modbus network.



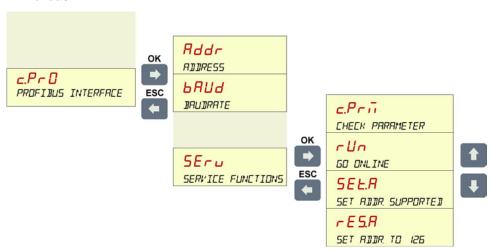
Menu	Range of values	Description
Addr	1247	Modbus device address; must be unique within a Modbus network.
ЬЯИА	2400,4800,9600,19.2k, 38.4k,57.6k,115.2k Bd	Transmission speed on the Modbus interface.
PRrI	NONE, ODD, EVEN	Parity (none, odd, even)
5.61 E	1Sb, 2Sb	Number of stop bits (Sb) per transmitted data byte.
RELIT	0.1S, 64P, 32P, 16P, 8P, 4P, 2P, 1P S=seconds P=pause time	Delay time until the device sends an answer to a Modbus request. The time must be selected the way, that the requesting master is still able to understand the answer. Pause time = "Time to transmit 3.5 characters"

► Ethernet (Modbus/TCP-interface)



Menu	Range of values	Description
1 P	z.B. 192.168.057.011	IP address: Must be unique for each device!
5Ub.n	z.B. 255.255.255.000	Subnet mask
6AFE	z.B. 192.168.057.001	Gateway address
Port	165535	The TCP port for the Modbus/TCP communication, usually this is port 502.

▶ Profibus DP



Menu	Range of values	Description
Addr	0125	Device address; must be unique within the Profibus network.
ьяиа	9.6 kBd 12 MBd	Transmission speed on the Profibus interface. The present value set is displayed (auto detection).
c.Prī	On / OFF	Check_User_Prm: The parameters of the control system will be used (On) or declined (OFF). Default: On.
гИп	On / OFF	Go_Online: Device is able to connect to the control system (On) or is separated from the Profibus system (OFF). Default: On.
SEŁ.A	On / OFF	Set_Slave_Addr_Supp: Setting of the device address via Profibus master is allowed (On) or disabled (OFF). Default: On.
rES.A	On / OFF	If On the device address is reset to the factory setting (126). In this case the device is no longer able to communicate with the control system.

Further menu parameters

Menu	Range of values	Description
iode Jisplry Moje	FULL, rEdU, USEr, LOOP see <u>Display modes</u>	Display mode of the device. USER and LOOP mode can be activated only, if at least one free measurement image has been defined!
rale UPIRTE RRTE / ms	1005000	Refresh rate of the display. This is the time gap between two updates of the display.
LOOP TIME / 1	210s	The time gap between changes of the displayed measurement image, if the LOOP mode is active.
P.dl S PREF. USER DISPLRY	120	Number of the preferred image of the USER mode which is automatically displayed after "P.tiM" without user action. LOOP mode must be activated.
PREF. RETURN T / 3	10255	Time without user action until the USER image "P.dIS" is automatically displayed in the LOOP mode.
SYSE INPUT SYSTEM	see <u>Inputs</u>	System connected to the device. A modification may cause that e.g. limit values or outputs will no longer properly, because the associated measured quantities are no longer valid. Possibly also the existing wiring must be changed.
Pri VOLTAGE PRIMARY CURRENT PRIMARY	< 1000 MV < 200.0 kA	Rated primary voltage of the voltage transformer connected upstream. If the measurement is done directly this value must be the same as "SEC".
SEC VOLTAGE SECONDARY CURRENT SECONDARY	50832V _{LL} / 28,9480.3 _{LN} 17.5 A	Rated secondary value of the voltage transformer connected upstream.
LOP VOLTAGE MAX SEC. CURRENT MAX SEC.	SEC \leq tOP \leq (max. U) or SEC \leq tOP \leq (max. I)	Maximum value which should be measurable on the secondary side of the voltage transformer. Maximum values see "SEC".
L.SrC LIMXY SOURCE.		The measured quantity assigned to the limit value. Can not be modified. XY=0116.
L.On LIMXY ON VALUE.	Depends on quantity	Limit for ON state of limit value XY; XY=0116. See <u>Limit values</u> .
L.OFF LIMXY OFF VALUE	Depends on quantity	Limit for OFF state of limit value XY; XY=0116. See <u>Limit values</u> .
I of O Device info text		Here the configured short description text (TAG) of the device is displayed. Can be modified via CB-Manager only.
POUC POWER MEAN LOGGER.	On / OFF	Switch on (On) or off (OFF) recording of power mean values logger.
<mark>∴EA∩</mark> MERN LOGGER	On / OFF	Switch on (On) or off (OFF) recording of mean values logger.
∴AH E×TREME VAL LOGGER	On / OFF	Switch on (On) or off (OFF) recording of extreme values logger.
<mark>∴EE</mark> r METER LOGGER	On / OFF	Switch on (On) or off (OFF) recording of meter logger.
di SE Disturbance Rec.	On / OFF	Switch on (On) or off (OFF) recording of disturbance logger.

Setting time and date

All time information stored in the device is referenced to UTC¹⁾ (Universal Time Coordinated). For a better understanding the time/date information displayed on the display can be converted to local time by defining a time zone offset. This offset is added to the internal UTC time before the time information is displayed. Keep in mind that the offset may be variable if daylight saving time is used locally (see below).

Hint: If time is set via CB-Manager software the difference between local time and UTC rather results from the local time settings of the PC than from the time zone offset configured via display. There may be a discrepancy.

Menu: El īĒ	Range of values	Description
20nE TIME ZONE OFFSET	-840840 [min]	Offset of the local time to UTC time ¹⁾ , which is used as the time reference in the device.
EI TE TIME		Setting of hours, minutes and seconds of the built-in real-time clock.
dALE DRTE		Setting of day, month and year of the built-in real-time clock.

¹⁾ **UTC** (**U**niversal **T**ime **C**oordinated)

Sometimes UTC is called world time as well. The reference corresponds to the Greenwich Mean Time (GMT). The time zones of the world nowadays are all referenced with an offset to UTC. UTC time doesn't use time shifts, which may occur due to a change to daylight saving time.

Example: In Switzerland the CET (Central European Time) is valid, which has an offset of +1[h] to UTC. But during half of the year the CEST (Central European Summer Time) is used, which has an offset of +2[h] to the UTC time used in the device.

7.8.1 Selection of the parameter to edit

To modify a value you have to navigate through the menu tree by means of the arrow keys until the appropriate parameter is displayed. For the parameter selected on line 4 a detailed description is displayed.



If the description text on line 4 is wider than 8 characters it's shown as a ticker.



- >> 1 Previous menu. If blank: End of list
- >> Presently selectable submenu. Choose via



- >> Next menu. If blank: End of list
- >> Description of the submenu of line 2 (ticker)

Depending on the parameter either a discrete value from a list may be selected or the associated numerical value may be modified.

7.8.2 Discrete selection

The configuration of parameters, which can accept a limited number of values only, is implemented by means of selecting a value from a list. In the example shown below to modify the display mode normally the discrete values FULL, REDU, USER and LOOP are available.

Example: Change MODE (DISPLAY MODE) from r EdU to USEr



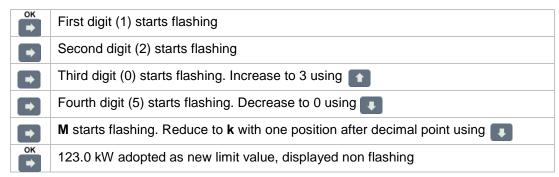
- >> 1 Previous element. If blank: End of selection list
- >> Present selection. Change via
- >> Next element. If blank: End of selection list
- >> Description of the selection on line 2 (ticker)
- ► The **modification mode** is left automatically after a time of 15s with no key pressed and the previous displayed menu is shown again!
 - ► The **configuration mode** is left automatically after a time of 30s with no key pressed and the measurement display is shown again!

7.8.3 Setting value

For quantities which may accept a huge number of possible values, the present value may be modified digit per digit. In most cases a possible range of values is predefined, which limits possible input values.

Example: Modification of limit value 1 from 1.205 MW to 123.0 kW





The modification mode is left automatically after a time of 15s with no key pressed and the previous displayed menu is shown again!
 The configuration mode is left automatically after a time of 30s with no key pressed and the measurement display is shown again!

7.9 Data logger

The data logger offers a periodical acquisition of measurement data, such as recording load profiles, measurement fluctuations or meter readings as well as event triggered recordings of alarm states or distubances. This storage medium used is an SD card, which allows almost unlimited recordings and an easy exchanging on-site.

The following recording types are supported:

Logger	Triggered by	Recording	Resettable
Power mean values	Interval t1	ON / OFF	YES
Configurable mean values quantities	Interval t2	ON / OFF	YES
Extreme values	Interval t3	ON / OFF	YES
Meter readings	Calendar based	ON / OFF	YES
Disturbance recorder	Event	ON / OFF	YES
Alarm / event list	Event	always active	NO
Operator list	Event	always active	NO

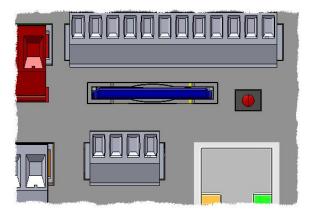
7.9.1 Activation of data logger recording

By configuring the different data loggers their state will not be changed. If it was active it remains active, if it was inactive it remains inactive. The activation / deactivation of a specific logger may be performed via PC software or via the <u>local programming menu</u>. Only via PC software, respectively by using the corresponding commands via the configuration interface, contents of the individual logger can be reset.

Lists are exceptional, because they are always active to prevent manipulations. They record events in endless mode and can't be reset.

7.9.2 SD card

The device is supplied with a 2 GByte SD card, which allows long-term recodings. The device can be equipped with all other SD cards available.



The red LED of the key located next to the SD card signalizes that the logger is active. During writing to the card the LED becomes dark for a short time.

To exchange an SD card the key must be pressed. As soon as the red LED becomes dark, the SD card can be removed and the new card inserted. Data can't be latched in the device. Therefore there is no recording for the time no card is present in the device.

Status messages on LED display	Meaning
NO CRRI	The logger is active, but no SD card has been inserted.
ERRILDEK	The SD card inserted is write-protected.
CR31_FULL	For at least one of the logger parts, which are not used in endless mode, the assigned memory space is full. No more data can be recorded.
CRRI_ERR	Faulty SD card. Possibly no more data will be recorded.

7.9.3 Access to logger data

Only for device versions with Ethernet a direct access to the logger data via interface is possible. For all other versions you have to remove the SD card first and to access the recorded data using an internal or external card reader. The analysis of the data is performed using the CB-Analyzer software.

7.9.4 Logger data analysis

The analysis of recorded logger data can be done using the PC software CB-Analyzer. The software may be downloaded free of charge from our homepage http://www.camillebauer.com.



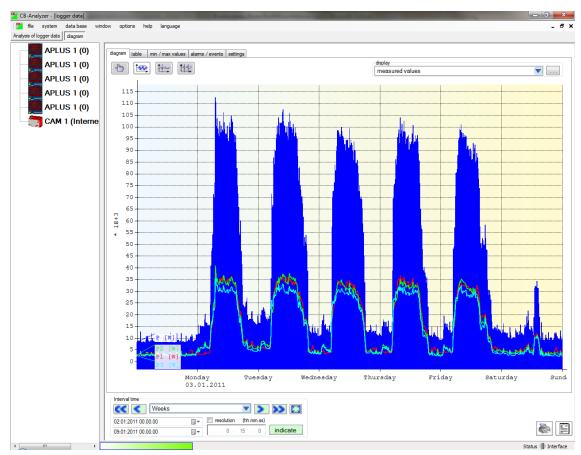
The file "Read-me-first" provides all necessary information for the installation of the CB-Analyzer software and assistance for possible problems.

Functionality of the CB-Analyzer software

This .NET-based software facilitates the data acquisition and analysis of the optional data loggers and lists of SINEAX CAM and APLUS. The data read from the devices will be stored in a database. The program is capable of processing several devices simultaneously.

- ► Acquisition of logger and list data of several devices
- ► Storage of the data in a database (Access, SQLClient)
- ▶ Different analyzing options of the acquired data, also across devices
- ► Report generation in list or graphic format
- ► Selectable time range in the preparation of reports
- ► Export of report data to Excel or as an Acrobat PDF file

The CB-Analyzer software provides a comprehensive help facility, which describes in detail the operation of the software. Below a screen-shot is shown, which shows as an example the graphical analysis of the power demand of a factory over one week.



8. Service, maintenance and disposal

8.1 Protection of data integrity

The APLUS supports security mechanism, which serve to prevent manipulation or undesired modifications of device data.

► Protection against device data modifications

8.2 Calibration and new adjustment

Each device is adjusted and checked before delivery. The condition as supplied to the customer is measured and stored in electronic form.

The uncertainty of measurement devices may be altered during normal operation if, for example, the specified ambient conditions are not met. If desired, in our factory a calibration can be performed, including a new adjustment if necessary, to assure the accuracy of the device.

8.3 Cleaning

The display and the operating keys should be cleaned in regular intervalls. Use a dry or slightly moist cloth for this.



Damage due to detergents

Detergents may not only affect the clearness of the display but also can damage the device. Therefore, do not use detergents.

8.4 Battery

The device contains a battery for buffering the internal clock. It cannot be changed by the user. The replacement can be done at the factory only.

8.5 Disposal

The product must be disposed in compliance with local regulations. This particularly applies to the built-in battery.

9. Technical data

Inputs

Nominal current: adjustable 1...5 A Maximum: 7.5 A (sinusoidal) Consumption: $\leq I^2 \times 0.01 \Omega$ per phase

Overload capacity: 10 A continuous

100 A, 10 x 1 s, interval 100 s

Nominal voltage: $57.7...400 \text{ V}_{LN}$, $100...693 \text{ V}_{LL}$ Maximum: 480 V_{LN} , 832 V_{LL} (sinusoidal)

Consumption: $\leq U^2 / 3 M\Omega$ per phase

Impedance: 3 M Ω per phase

Overload capacity: 480 V_{LN}, 832 V_{LL} continuous

 $600~V_{LN},~1040~V_{LL},~10~x~10~s,$ interval 10s $800~V_{LN},~1386~V_{LL},~10~x~1~s,$ interval 10s

Systems: Single phase

Split phase (2-phase system)

3-wire, balanced load 3-wire, unbalanced load

3-wire, unbalanced load, Aron connection

4-wire, balanced load 4-wire, unbalanced load

4-wire, unbalanced load, Open-Y

Nominal frequency: 45... 50 / 60 ...65Hz Measurement TRMS: Up to the 63rd harmonic

Measurement uncertainty

Version with Rogowski current inputs

The additional uncertainty of the Rogowski coils ACF 3000 is not included in the following specifications: See operating instructions of Rogowski coil ACF3000

Reference conditions: Ambient 15...30°C,

(acc. IEC/EN 60688) sinusoidal input signals (form factor 1.1107)

Measurement over 8 cycles, no fixed system frequency for sampling,

PF=1, frequency 50...60Hz

Voltage, current: $\pm (0.08\% \text{ MV} + 0.02\% \text{ MR})^{-1/2}$ Power: $\pm (0.16\% \text{ MV} + 0.04\% \text{ MR})^{-3/2}$

Power factor: $\pm 0.1^{\circ 4}$ Frequency: ± 0.01 Hz Imbalance U, I: $\pm 0.5\%$ Harmonics: $\pm 0.5\%$ THD Voltage: $\pm 0.5\%$ TDD Current: $\pm 0.5\%$

Active energy: Class 0.5S, EN 62053-22 Reactive energy: Class 2, EN 62053-23

Measurement with fixed system frequency:

General \pm Basic uncertainty x ($F_{konfiq}-F_{ist}$) [Hz] x 10

Imbalance U \pm 1.5% up to \pm 0.5 Hz Harmonics \pm 1.5% up to \pm 0.5 Hz THD, TDD \pm 2.0% up to \pm 0.5 Hz Current measurement via Rogowski coils

Range: 0...3000A, auto-ranging

See operating instructions of Rogowski coil

ACF3000 for further information

¹⁾ MV: Measured value, MR: measurement range (maximum)

²⁾ Additional uncertainty of 0.1% MV if neutral wire not connected (3-wire connections)

³⁾ MR: maximum voltage x maximum current

⁴⁾ Additional uncertainty of 0.1° if neutral wire not connected (3-wire connections)

Zero suppression, range limitations

The measurement of specific quantities is related to a pre-condition which must be fulfilled, that the corresponding value can be determined and sent via interface or displayed. If this condition is not fulfilled, a default value is used for the measurement.

Quantity	Condition	Default
Voltage	Ux < 1% Ux _{max}	0.00
Current	Ix < 0,1% Ix _{rated}	0.00
PF	Sx < 1% Sx _{max}	1.00
QF, LF, tanφ	Sx < 1% Sx _{max}	0.00
Frequency	voltage and/or current input too low 1)	44.90
Voltage unbalance	Ux < 5% Ux _{max}	0.00
Current unbalance	mean value of phase currents < 5% Ix_{max}	0.00
Phase angle	at least one voltage Ux < 5% Ux _{max}	120°
Harmonics U, THD-U	fundamental < 5% Ux _{max}	0.00

¹⁾ specific level depends on the device configuration

Power supply via plug-in terminal

Nominal voltage: 100...230V AC ±15%, 50...400Hz

24...230V DC ±15%

Consumption: ≤ 7...10 VA, depending on the device hardware used

I/O interface

Available inputs and outputs

-	
Basic unit	- 1 relay output, changeover contact
	- 1 digital output (fixed)
	- 1 digital input (fixed)
I/O extension 1	- 2 relay outputs, changeover contact
	- 4 bipolar analog outputs
	- 2 digital inputs/outputs, each configurable as input or output
I/O extension 2	- 2 relay outputs, changeover contact
	- 6 digital inputs/outputs, each configurable as input or output

Analog outputs via plug-in terminals, galvanically isolated

Linearization: Linear, quadratic, kinked
Range: ± 20 mA (24 mA max.), bipolar

Uncertainty: $\pm 0.2\%$ of 20 mA

Burden: $\leq 500 \Omega \text{ (max. } 10 \text{ V / } 20 \text{ mA)}$

Burden influence: $\leq 0.2\%$ Residual ripple: $\leq 0.4\%$

Response time: 60...100ms (for 2 cycles averaging time of RMS values)

Relays via plug-in terminals

Contact: changeover contact, bistabil Load capacity: 250 V AC, 2 A, 500 VA

30 V DC, 2 A, 60 W

Digital inputs/outputsvia plug-in terminalsDigital inputs (acc. EN 61 131-2 DC 24 V type 3):Nominal voltage12 / 24 V DC (30 V max.)

Logical ZERO - 3 up to + 5 V Logical ONE 8 up to 30 V <u>Digital outputs</u> (partly acc. EN 61 131-2):

 $\begin{array}{ll} \mbox{Nominal voltage} & \mbox{12 / 24 V DC (30 V max.)} \\ \mbox{Nominal current} & \mbox{50 mA (60 mA max.)} \\ \mbox{Load capability} & \mbox{400 } \Omega \dots 1 \mbox{ M} \Omega \end{array}$

Interfaces

Modbus/RTU X4 / X8 via plug-in terminals

Protocol: Modbus RTU

Physics: RS-485, max. 1200m (4000 ft)

Baud rate: 2'400, 4'800, 9'600, 19'200, 38'400, 57'600, 115'200 Baud

Number of participants: ≤ 32

Profibus X8 via 9-pin D-sub socket

Protocol: Profibus DP

Physics: RS-485, 100...1200m (depending on baud rate and cable type used)

Baud rate: Automatic baud rate recognition (9.6kBit/s ... 12MBit/s)

Address: 0...125 (default: 126)

Ethernet X4via RJ45 connectorProtocol:Modbus/TCP, NTPPhysics:Ethernet 100BaseTX

Mode: 10/100 MBit/s, full/half duplex, auto-negotiation

Internal clock (RTC)

Uncertainty: $\pm 2 \text{ minutes / month (15 up to } 30^{\circ}\text{C})$

Synchronization: via Synchronization pulse

Running reserve: > 10 years

Ambient conditions, general information

Operating temperature: -10 up to 15 up to 30 up to + 55°C

Storage temperature: -25 up to + 70°C

Temperature influence: 0.5 x measurement uncertainty per 10 K Long term drift: 0.5 x measurement uncertainty per year

Others: Usage group II (EN 60 688)
Relative humidity: < 95% no condensation

Altitude: ≤ 2000 m max.

Device to be used indoor only!

Mechanical attributes

Orientation: Any

Housing material: Polycarbonat (Makrolon)

Flammability class: V-0 acc. UL94, non-dripping, free of halogen

Weight: 500 g

Dimensions: Dimensional drawings

Vibration withstand (test according to DIN EN 60 068-2-6)

Acceleration: ± 2 g

Frequency range: 10 ... 150 ... 10 Hz, rate of frequency sweep: 1 octave/minute

Number of cycles: 10 in each of the 3 axes

Security

The current inputs are galvanically isolated from each other

Protection class: II (protective insulation, voltage inputs via protective impedance)

Pollution degree: 2

Protection: IP64 (front), IP40 (housing), IP20 (terminals)

Measurement category: CAT III, CATII (relays)
Rated voltage power supply: 265 V AC

(versus earth): Relays: 250 V AC

I/O's: 30 V DC

Test voltages: DC, 1 min., acc. IEC/EN 61010-1

7504V DC, power supply versus inputs U, I 5008V DC, power supply versus bus, I/O's, relays

6030V DC, inputs U versus inputs I

4690V DC, inputs U after protective impedance versus bus, I/O's, relays

7504V DC, inputs U versus relays

7504V DC, inputs I versus bus, I/O's, relays

6030V DC, inputs I versus inputs I 3130V DC, relay versus relay, bus, I/O's

Applied regulations, standards and directives

IEC/EN 61 010-1 Safety regulations for electrical measuring, control and laboratory equipment

IEC/EN 60 688 Electrical measuring transducers for converting AC electrical variables into

analog or digital signals

DIN 40 110 AC quantities IEC/EN 60 068-2-1/ Ambient tests

-2/-3/-6/-27: -1 Cold, -2 Dry heat, -3 Damp heat, -6 Vibration, -27 Shock

IEC/EN 60 529 Protection type by case

IEC/EN 61 000-6-2/ Electromagnetic compatibility (EMC)

61 000-6-4: Generic standard for industrial environment

IEC/EN 61 131-2 Programmable controllers - equipment, requirements and tests

(digital inputs/outputs 12/24V DC)

IEC/EN 61 326 Electrical equipment for measurement, control and laboratory use - EMC

requirements

IEC/EN 62 053-31 Pulse output devices for electromechanical and electronic meters (S0 output)

UL94 Tests for flammability of plastic materials for parts in devices and appliances

2011/65/EU (RoHS) EU directive on the restriction of the use of certain hazardous substances

Warning

This is a class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

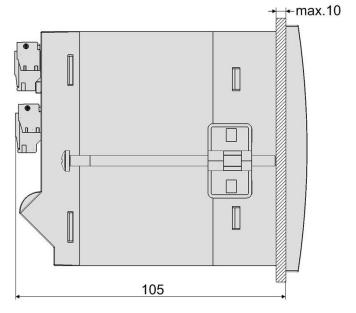
This device complies with part 15 of the FCC:

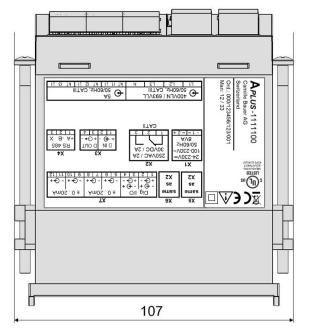
Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

This Class A digital apparatus complies with Canadian ICES-0003.

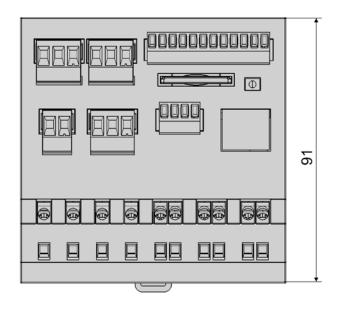
10. Dimensional drawings

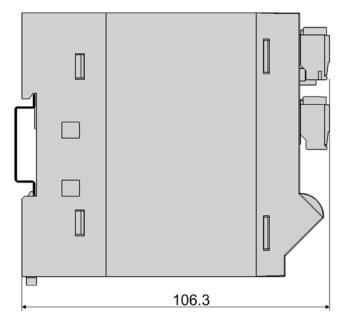


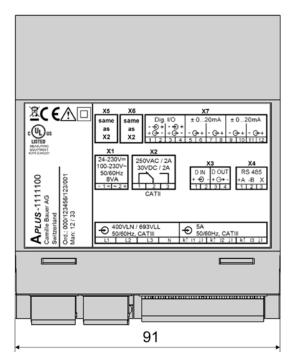




APLUS with display







APLUS without display

Annex

A Description of measured quantities

Used abbreviations

1L Single phase system

2L Split phase; system with 2 phases and centre tap

3Lb 3-wire system with balanced load3Lu 3-wire system with unbalanced load

3Lu.A 3-wire system with unbalanced load, Aron connection (only 2 currents connected)

4Lb 4-wire system with balanced load4Lu 4-wire system with unbalanced load

4Lu.O 4-wire system with unbalanced load, Open-Y (reduced voltage connection)

A1 Basic measurements

These measured quantities are determined using the configured measurement time (2...1024 cycles, in steps of 2 cycles). If a measurement is available depends on the selected system.

Depending on the measured quantity also minimum and maximum values are determined and non-volatile stored with timestamp. These values may be reset by the user via the display unit or via the configuration interface, see <u>resetting</u> of measurements.

Measurement	present	max	min	11	2L	3ГР	3Ги	3Lu.A	4Lb	4Lu.O	4Lu
Voltage U	•	•	•						\checkmark		
Voltage U _{1N}	•	•	•							$\sqrt{}$	$\sqrt{}$
Voltage U _{2N}	•	•	•								
Voltage U _{3N}	•	•	•								
Voltage U ₁₂	•	•	•					\checkmark			
Voltage U ₂₃	•	•	•			~	~	\checkmark		\checkmark	\checkmark
Voltage U ₃₁	•	•	•			\checkmark	\checkmark	\checkmark		\checkmark	\checkmark
Zero displacement voltage U _{NE}	•	•									
Current I	•	•		\checkmark		~			\checkmark		
Current I1	•	•			\checkmark		\checkmark	\checkmark		\checkmark	\checkmark
Current I2	•	•			\checkmark		\checkmark	\checkmark		\checkmark	\checkmark
Current I3	•	•									
Bimetal current 160min. IB	•	•		\checkmark		~			\checkmark		
Bimetal current 160min. IB1	•	•			\checkmark		\checkmark	\checkmark		\checkmark	\checkmark
Bimetal current 160min. IB2	•	•						\checkmark			
Bimetal current 160min. IB3	•	•									
Neutral current I _N	•	•									
Active power P	•	•									
Active power P1	•	•			~					\checkmark	~
Active power P2	•	•			\checkmark					\checkmark	\checkmark
Active power P3	•	•								\checkmark	\checkmark
Reactive power Q	•	•		\checkmark							
Reactive power Q1	•	•			\checkmark					\checkmark	\checkmark
Reactive power Q2	•	•			\checkmark					\checkmark	\checkmark
Reactive power Q3	•	•								\checkmark	\checkmark
Apparent power S	•	•									
Apparent power S1	•	•									
Apparent power S2	•	•									
Apparent power S3	•	•									
Frequency F	•	•	•							$\sqrt{}$	

Measurement	present	max	min	, 1L	2L	3Lb	3Lu			4Lu.O	4Lu
Power factor PF	•									\checkmark	$\sqrt{}$
Power factor PF1	•				$\sqrt{}$						$\sqrt{}$
Power factor PF2	•										$\sqrt{}$
Power factor PF3	•										$\sqrt{}$
PF incoming inductive			•			$\sqrt{}$			$\sqrt{}$		$\sqrt{}$
PF incoming capacitive			•			$\sqrt{}$			$\sqrt{}$	$\sqrt{}$	$\sqrt{}$
PF outgoing inductive			•								$\sqrt{}$
PF outgoing capacitive			•			$\sqrt{}$				$\sqrt{}$	$\sqrt{}$
Reactive power factor QF	•					$\sqrt{}$			$\sqrt{}$	$\sqrt{}$	$\sqrt{}$
Reactive power factor QF1	•										$\sqrt{}$
Reactive power factor QF2	•										$\sqrt{}$
Reactive power factor QF3	•									$\sqrt{}$	$\sqrt{}$
Load factor LF	•					$\sqrt{}$			$\sqrt{}$	$\sqrt{}$	$\sqrt{}$
Load factor LF1	•				\checkmark					\checkmark	$\sqrt{}$
Load factor LF2	•				\checkmark					\checkmark	$\sqrt{}$
Load factor LF3	•									\checkmark	$\sqrt{}$
U _{mean} =(U1N+U2N)/2	•										
U _{mean} =(U1N+U2N+U3N)/3	•									\checkmark	$\sqrt{}$
U _{mean} =(U12+U23+U31)/3	•						$\sqrt{}$	$\sqrt{}$			
I _{mean} =(I1+I2)/2	•				\checkmark						
I _{mean} =(I1+I2+I3)/3	•									\checkmark	$\sqrt{}$
Phase angle between U1 and U2	•									\checkmark	$\sqrt{}$
Phase angle between U2 and U3	•										$\sqrt{}$
Phase angle between U3 and U1	•					$\sqrt{}$					$\sqrt{}$
Maximum ΔU <> Um 1)	•	•				$\sqrt{}$					$\sqrt{}$
Maximum ΔI <> Im ²⁾	•	•									$\sqrt{}$
IMS, Average current with sign of P	•										$\sqrt{}$

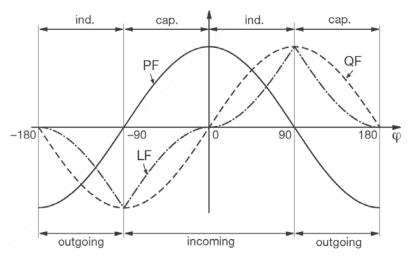
¹⁾ maximum deviation from the mean value of all voltages (see A3)

Power factors

The **power factor PF** gives the relation between active and apparent power. If there are no harmonics present in the system, it corresponds to the cosφ (see also <u>Reactive power</u>). The PF has a range of -1...0...+1, where the sign gives the direction of energy flow.

The **load factor LF** is a quantity derived from the PF, which allows making a statement about the load type. Only this way it's possible to measure a range like 0.5 capacitive ... 1 ... 0.5 inductive in a non-ambiguous way.

The **reactive power factor QF** gives the relation between reactive and apparent power.



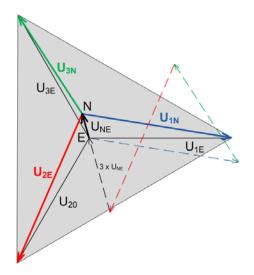
²⁾ maximum deviation from the mean value of all currents (see A3)

Zero displacement voltage UNE

Starting from the generating system with star point E (which is normally earthed), the star point (N) on load side is shifted in case of unbalanced load. The zero displacement voltage between E und N may be determined by a vectorial addition of the voltage vectors of the three phases:

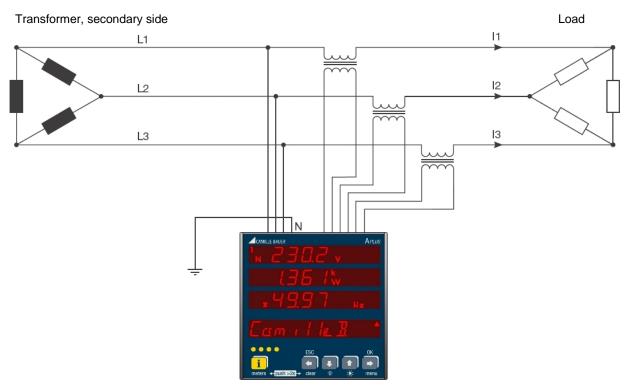
$$\underline{U}_{NE} = -\left(\underline{U}_{1N} + \underline{U}_{2N} + \underline{U}_{3N}\right) / 3$$

A displacement voltage may also occur due to harmonics of order 3, 9, 15, 21 etc., because the dedicated currents add in the neutral wire.



Earth fault monitoring in IT systems

Via the determination of the zero displacement voltage it's possible to detect a first earth fault in an unearthed IT system. To do so, the device is configured for measurement in a 4-wire system with unbalanced load and the neutral connector is connected to earth. In case of a single phase earth fault there is a resulting zero displacement voltage of $U_{LL}/\sqrt{3}$. The alarming may be done e.g. by means of a relay output.



Because in case of a fault the voltage triangle formed by the three phases does not change the voltage and current measurements as well as the system power values will be still measured and displayed correctly. Also the meters carry on to work as expected.

The method is suited to detect a fault condition during normal operation. A declination of the isolation resistance may not be detected this way. This should be measured during a periodical control of the system using a mobile system.

Another possibility to analyze fault conditions in a grid offers the method of the <u>symmetrical components</u> as described in A3.

A2 Harmonic analysis

Measurement	present	max	11	2L	ЗГР	3Lu	3Lu.A	4Lb	4Lu.O	4Lu
THD Voltage U1N/U	•	•								
THD Voltage U2N	•	•	^	^					^	\checkmark
THD Voltage U3N	•	•								
THD Voltage U12	•	•			7	7				
THD Voltage U23	•	•					\checkmark			
THD Voltage U31	•	•					\checkmark			
TDD Current I1/I	•	•	\checkmark	\checkmark			\checkmark	\checkmark	\checkmark	\checkmark
TDD Current I2	•	•				7				
TDD Current I3	•	•					\checkmark		^	\checkmark
Harmonic contents 2nd50th U1N/U	•	•	\checkmark	\checkmark				\checkmark	\checkmark	\checkmark
Harmonic contents 2nd50th U2N	•	•		\checkmark					\checkmark	\checkmark
Harmonic contents 2nd50th U3N	•	•								
Harmonic contents 2nd50th U12	•	•			√		\checkmark			
Harmonic contents 2nd50th U23	•	•					\checkmark			
Harmonic contents 2nd50th 250. U31	•	•					\checkmark			
Harmonic contents 2nd50th 250. I1/I	•	•				√				
Harmonic contents 2nd50th 250. I2	•	•		\checkmark		7	\checkmark		\checkmark	
Harmonic contents 2nd50th 250. I3	•	•								

Harmonics

Harmonics are multiple of the fundamental resp. system frequency. They arise if non-linear loads, such as RPM regulated drives, rectifiers, thyristor controlled systems or fluorescent lamps are present in the power system. Thus undesired side effects occur, such as additional thermical stress to operational resources or electrical mains, which lead to an advanced aging or even damage. Also the reliability of sensitive loads can be affected and unexplainable disturbances may occur. In industrial networks the image of the harmonics gives good information about the kind of loads connected. See also:

► Increase of reactive power due to harmonic currents

TDD (Total Demand Distortion)

In the APLUS the complete harmonic content of the currents is shown as Total Demand Distortion, briefly TDD. This value is scaled to the rated current resp. rated power. Only this way it's possible to estimate the influence of the current harmonics on the connected equipment correctly.

Maximum values

The maximum values of the harmonic analysis arise from the monitoring of THD and TDD. The maximum values of individual harmonics are not monitored separately, but are stored if a maximum value of THD or TDD is detected. The image of the maximum harmonics therefore always corresponds to the dedicated THD resp. TDD.



The accuracy of the harmonic analysis depends strongly on the quality of the current and voltage transformers possibly used. In the harmonics range transformers normally change both, the amplitude and the phase of the signals to measure. It's valid: The higher the frequency of the harmonic, the higher its damping resp. phase shift.

A3 System imbalance

Measured quantity	present	max	min	1	2L	ЗГР	3Lu	3Lu.A	4Lb	4Lu.0	4Lu
UR1: Positive sequence [V]	•										\checkmark
UR2: Negative sequence [V]	•										\checkmark
U0: Zero sequence [V]	•										\checkmark
U: Imbalance UR2/UR1	•	•				√	√	√			\checkmark
U: Imbalance U0/UR1	•	•									\checkmark
IR1: Positive sequence [A]	•						√				\checkmark
IR2: Negative sequence [A]	•						√				\checkmark
I0: Zero sequence [A]	•						√				\checkmark
I: Imbalance IR2/IR1	•	•					√				\checkmark
I: Imbalance I0/IR1	•	•									

Available via interface only

Imbalance in three-phase systems may occur due to single-phase loads, but also due to failures, such as e.g. the blowing of a fuse, an earth fault, a phase failure or an isolation defect. Also harmonics of the 3rd, 9th, 15th, 21st etc. order, which add in the neutral wire, may lead to imbalance. Operating resources dimensioned to rated values, such as three-phase generators, transformers or motors on load side, may be excessively stressed by imbalance. So a shorter life cycle, a damage or failure due to thermical stress can result. Therefore monitoring imbalance helps to reduce the costs for maintenance and extends the undisturbed operating time of the used resources.

Imbalance or unbalanced load relays use different measurement principles. One of them is the approach of the symmetrical components, the other one calculates the maximum deviation from the mean-value of the three phase values. The results of these methods are not equal and don't have the same intention. Both of these principles are implemented in the APLUS.

Symmetrical components (acc. Fortescue)

The imbalance calculation method by means of the symmetrical components is ambitious and intensive to calculate. The results may be used for disturbance analysis and for protection purposes in three-phase systems. The real existing system is divided in symmetrical system parts: A positive sequence, a negative sequence and (for systems with neutral conductor) a zero sequence system. The approach is easiest to understand for rotating machines. The positive sequence represents a positive rotating field, the negative sequence a negative (braking) rotating field with opposite sense of direction. Therefore the negative sequence prevents that the machine can generate the full turning moment. For e.g. generators the maximum permissible current imbalance is typically limited to a value of 8...12%.

Maximum deviation from the mean value

The calculation of the maximum deviation from the mean value of the phase currents resp. phase voltages gives the information if a grid or substation is imbalanced loaded. The results are independent of rated values and the present load situation. So a more symmetrical system can be aspired, e.g. by changing loads from one phase to another.

Also failure detection is possible. The capacitors used in compensation systems are wear parts, which fail quite often and then have to be replaced. When using three phase power capacitors all phases will be compensated equally which leads to almost identical currents flowing through the capacitors, if the system load is comparable. By monitoring the current imbalance it's then possible to estimate if a capacitor failure is present.

The maximum deviations are calculated in the same steps as the instantaneous values and therefore are arranged there (see A1).

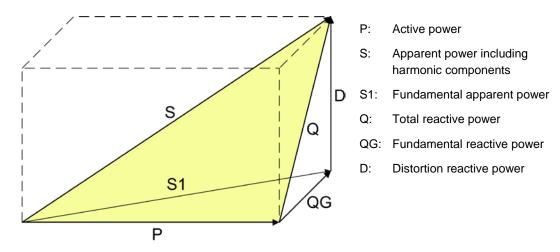
A4 Reactive power

Measured quantity	pres.	max	min	1	2L	ЗГР	3Lu	3Lu.A	4Lb	4Lu.0	4Lu
Distortion reactive power D	•	•		1	√	√	√	√	√	√	1
Distortion reactive power D1	•	•			V					V	V
Distortion reactive power D2	•	•			V					V	V
Distortion reactive power D3	•	•								V	V
Fundamental reactive power QG	•	•		V	V	V	V	V	V	V	V
Fundamental reactive power QG1	•	•			√					√	√
Fundamental reactive power QG2	•	•			V					V	√
Fundamental reactive power QG3	•	•								√	V
cosφ of fundamental	•		•	V	V	V	V	V	$\sqrt{}$	√	$\sqrt{}$
cosφ of fundamental L1	•		•		$\sqrt{}$						V
cosφ of fundamental L2	•		•		$\sqrt{}$						V
cosφ of fundamental L3	•		•							√	$\sqrt{}$
cosφ of fundamental, incoming inductive			•	V	$\sqrt{}$	V	V	V	$\sqrt{}$		V
cosφ of fundamental, incoming capacitive			•	V	V	$\sqrt{}$	V	V	$\sqrt{}$	√	$\sqrt{}$
cosφ of fundamental, outgoing inductive			•	V	V	$\sqrt{}$	V	V	$\sqrt{}$	√	$\sqrt{}$
cosφ of fundamental, outgoing capacitive			•	V	V	V	V	V		V	V
tanφ of fundamental	•			1	√	√	1	√	√	√	1
tanφ of fundamental L1	•				V					V	1
tanφ of fundamental L2	•				V					√	1
tanφ of fundamental L3	•									V	1

Available via interface only

Most of the loads consume a combination of ohmic and inductive current from the power system. Reactive power arises by means of the inductive load. But the number of non-linear loads, such as RPM regulated drives, rectifiers, thyristor controlled systems or fluorescent lamps, is increasing. They cause non-sinusoidal AC currents, which may be represented as a sum of harmonics. Thus the reactive power to transmit increases and leads to higher transmission losses und higher energy costs. This part of the reactive power is called distortion reactive power.

Normally reactive power is unwanted, because there is no usable active component in it. Because the transmission of reactive power over long distances is uneconomic, it makes sense to install compensation systems close to the consumers. So transmission capacities may be used better and losses and voltage drops by means of harmonic currents can be avoided.



The reactive power may be divided in a fundamental and a distortion component. Only the fundamental reactive power may be compensated directly by means of the classical capacitive method. The distortion components have to be combated using inductors or active harmonic conditioners.

The APLUS reports a **load factor PF** which is the relation between active power P and apparent power S, including all possibly existing harmonic parts. This factor is often called $\cos\varphi$, which is only partly correct. The PF corresponds to the $\cos\varphi$ only, if there is no harmonic content present in the system. So the $\cos\varphi$ represents the relation between the active power P and the fundamental apparent power S1.

Also calculated is the $tan\phi$, which is especially known as a target quantity for the reactive power compensative using capacitors. It corresponds to the relation of the fundamental reactive power QG and the active power P. Here intentionally the fundamental reactive power is used for the calculation, because this is the only component which may be directly compensated via capacitors.

A5 Mean values and trend

Measured quantity		Present	Trend	max	min	History
Active power incoming	1s60min. 1)	•	•	•	•	5
Active power outgoing	1s60min. 1)	•	•	•	•	5
Reactive power incoming		•	•	•	•	5
Reactive power outgoing	1s60min. 1)	•	•	•	•	5
Reactive power inductive	1s60min. 1)	•	•	•	•	5
Reactive power capacitive	1s60min. 1)	•	•	•	•	5
Apparent power	1s60min. 1)	•	•	•	•	5
Mean value quantity 1	1s60min. 2)	•	•	•	•	1
Mean value quantity 2	1s60min. ²⁾	•	•	•	•	1
Mean value quantity 3	1s60min. ²⁾	•	•	•	•	1
Mean value quantity 4	1s60min. ²⁾	•	•	•	•	1
Mean value quantity 5	1s60min. ²⁾	•	•	•	•	1
Mean value quantity 6	1s60min. ²⁾	•	•	•	•	1
Mean value quantity 7	1s60min. ²⁾	•	•	•	•	1
Mean value quantity 8	1s60min. ²⁾	•	•	•	•	1
Mean value quantity 9	1s60min. ²⁾	•	•	•	•	1
Mean value quantity 10	1s60min. ²⁾	•	•	•	•	1
Mean value quantity 11	1s60min. ²⁾	•	•	•	•	1
Mean value quantity 12	1s60min. ²⁾	•	•	•	•	1

Available via interface only 1) Interval time t1 2) Interval time t2

The device calculates automatically the mean values of all system power quantities. In addition up to 12 further mean value quantities can be freely selected.

Calculating the mean-values

The mean value calculation is performed via integration of the measured instantaneous values over a configurable averaging interval. The interval time may be selected in the range from one second up to one hour. Possible interim values are set the way that a multiple of it is equal to a minute or an hour. Mean values of power quantities (interval time t1) and free quantities (interval time t2) may have different averaging intervals.

Synchronization

For the synchronization of the averaging intervals the internal clock or an external signal via digital input may be used. In case of an external synchronization the interval should be within the given range of one second up to one hour. The synchronization is important for making e.g. the mean value of power quantities on generating and demand side comparable.

Trend

The estimated final value (trend) of mean values is determined by weighted addition of measurements of the past and the present interval. It serves for early detection of a possible exceeding of a given maximum value. This can then be avoided, e.g. by switching off an active load.

History

For mean values of system powers the last 5 interval values may be displayed on the device or read via interface. For configurable quantities the value of the last interval is provided via communication interface.

A6 Meters

Measured quantity		11	2L	ЗГР	3Lu	3Lu.A	4Lb	4Lu.0	4Lu
Active energy incoming,	high tariff	•	•	•	•	•	•	•	•
Active energy outgoing,	high tariff	•	•	•	•	•	•	•	•
Reactive energy inductive,	high tariff	•	•	•	•	•	•	•	•
Reactive energy capacitive,	high tariff	•	•	•	•	•	•	•	•
Reactive energy incoming,	high tariff	•	•	•	•	•	•	•	•
Reactive energy outgoing,	high tariff	•	•	•	•	•	•	•	•
Active energy incoming,	low tariff	•	•	•	•	•	•	•	•
Active energy outgoing,	low tariff	•	•	•	•	•	•	•	•
Reactive energy inductive,	low tariff	•	•	•	•	•	•	•	•
Reactive energy capacitive,	low tariff	•	•	•	•	•	•	•	•
Reactive energy incoming,	low tariff	•	•	•	•	•	•	•	•
Reactive energy outgoing,	low tariff	•	•	•	•	•	•	•	•
Active energy incoming L1,	high tariff		•					•	•
Active energy incoming L2,	high tariff		•					•	•
Active energy incoming L3,	high tariff							•	•
Reactive energy incoming L1,	high tariff		•					•	•
Reactive energy incoming L2,	high tariff		•					•	•
Reactive energy incoming L3,	high tariff							•	•
Active energy incoming L1,	low tariff		•					•	•
Active energy incoming L2,	low tariff		•					•	•
Active energy incoming L3,	low tariff							•	•
Reactive energy incoming L1,	low tariff		•					•	•
Reactive energy incoming L2,	low tariff		•					•	•
Reactive energy incoming L3,	low tariff							•	•
Meter I/O 2,	high tariff								
Meter I/O 6,	high tariff								
Meter I/O 7,	high tariff								
Meter I/O 8,	high tariff								
Meter I/O 9,	high tariff								
Meter I/O 10,	high tariff								
Meter I/O 11,	high tariff		Inde	pen			ıeası	ured	
Meter I/O 2,	low tariff				sys	tem			
Meter I/O 6,	low tariff								
Meter I/O 7,	low tariff								
Meter I/O 8,	low tariff								
Meter I/O 9,	low tariff								
Meter I/O 10,	low tariff								
Meter I/O 11,	low tariff								

Standard meters

The meters for active and reactive energy of the system are always active. The meters for active and reactive energy demand per phase are active only, if the measured system is a multiple phase system with unbalanced load, otherwise they are removed from the above list.

► Meter reading on the display

I/O meters

The meters of the I/O's are available only if the appropriate I/O's are configured as digital inputs for pulse counting, otherwise they are removed from the above list. No specific unit is shown for this kind of meters, because any energy form may be recorded here.

B Display matrices in FULL mode

The fourth line of each image is allocated to a programmable meter value, which does not change even if another measurement image is selected. In the subsequent matrices, arranged in accordance with the measured system, this fourth line is not included.

B0 Used abbreviations for the measurements

No.	Name	Description	Name (Display)
0		not used	
1	U	Voltage system in single, 3- or 4-wire systems	Ш
2	U1N	Voltage between phase L1 and neutral	U In
3	U2N	Voltage between phase L2 and neutral	U 2n
4	U3N	Voltage between phase L3 and neutral	И ∃п
5	U12	Voltage between phases L1 and L2	П 15
6	U23	Voltage between phases L2 and L3	П 53
7	U31	Voltage between phases L3 and L1	ИЗΙ
8	UNE	Zero displacement voltage 4-wire systems	U nE
9	I	Current system in single, 3- or 4-wire systems	1
10	I1	Current phase L1	1 1
11	12	Current phase L2	1 2
12	13	Current phase L3	1 3
13	IN	Neutral current	l n
14	IB	Current damped, balanced system (bimetal)	16
15	IB1	Current damped phase L1 (bimetal)	16-1
16	IB2	Current damped phase L2 (bimetal)	165
17	IB3	Current damped phase L3 (bimetal)	16 3
18	Р	Active power system (P=P1+P2+P3)	P
19	P1	Active power phase L1	P I
20	P2	Active power phase L2	P 2
21	P3	Active power phase L3	Р 3
22	Q	Reactive power system (Q=Q1+Q2+Q3)	9
23	Q1	Reactive power phase L1	9 1
24	Q2	Reactive power phase L2	9 2
25	Q3	Reactive power phase L3	9 3
26	S	Apparent power system	5
27	S1	Apparent power phase L1	5 1
28	S2	Apparent power phase L2	5 2
29	S3	Apparent power phase L3	5 3
30	F	System frequency	F
31	PF	Active power factor P/S, system	PF
32	PF1	Active power factor P1/S1, phase 1	PF I
33	PF2	Active power factor P2/S2, phase 2	PF 2
34	PF3	Active power factor P3/S3, phase 3	PF 3
35	QF	Reactive power factor P/S, system	9F
36	QF1	Reactive power factor P1/S1, phase 1	9F I
37	QF2	Reactive power factor P2/S2, phase 2	9F 2
38	QF3	Reactive power factor P3/S3, phase 3	9F 3
39	LF	Load factor system, sign(Q)x(1 – abs(PF)	LF
40	LF1	Load factor phase L1	LF I
41	LF2	Load factor phase L2	LF 2
42	LF3	Load factor phase L3	LF 3
43	U_MEAN	Average voltage (U1N+U2N+U3N)/3	πEAn
44	I_MEAN	Average current (I1+I2+I3)/3	πEAn
45	UF12	Phase angle U1-U2	RU 12
46	UF23	Phase angle U2-U3	RU23

No.	Name	Description	Name (Display)
47	UF31	Phase angle U3-U1	RU3 I
48	DEV_UMAX	Max. deviation from average of voltages	dE u.U
49	DEV_IMAX	Max. deviation from average of currents	dEu.l
50	DEV_U1	U1: deviation from average of voltages	dE u.U
51	DEV_U2	U2: deviation from average of voltages	dE u.U
52	DEV_U3	U3: deviation from average of voltages	dE u.U
53	DEV_I1	I1: deviation from average of currents	dEul
54	DEV_I2	I2: deviation from average of currents	dEul
55	DEV_I3	I3: deviation from average of currents	dEul
56	U MAX	Maximum value of U	П
57	U1N_MAX	Maximum value of U1N	U In
58	U2N MAX	Maximum value of U2N	U 2n
59	U3N_MAX	Maximum value of U3N	⊔ ∃п
60	U12_MAX	Maximum value of U12	П 15
61	U23_MAX	Maximum value of U23	П 53
62	U31_MAX	Maximum value of U31	и зі
63	UNE MAX	Maximum value of UNE	U nE
64	I MAX	Maximum value of I	1
65	I1_MAX	Maximum value of I1	1 1
66	I2 MAX	Maximum value of I2	1 2
67	I3_MAX	Maximum value of I3	1 3
68	IN_MAX	Maximum value of IN	1 0
69	IB_MAX	Maximum value of IB	16
70	IB1_MAX	Maximum value of IB1	16 1
71	IB2_MAX	Maximum value of IB2	162
72	IB3 MAX	Maximum value of IB3	163
73	P_MAX	Maximum value of P	P
74	P1_MAX	Maximum value of P1	PI
75	P2 MAX	Maximum value of P2	P 2
76	P3_MAX	Maximum value of P3	P 3
77	Q_MAX	Maximum value of Q	9
78	Q1 MAX	Maximum value of Q1	9 1
79	Q2_MAX	Maximum value of Q2	9 2
-	Q2_WAX	Maximum value of Q3	9 3
80	S MAX	Maximum value of Q3	5
	_		5 1
82	S1_MAX	Maximum value of S1	
83	S2_MAX S3_MAX	Maximum value of S2 Maximum value of S3	5 <i>2</i> 5 <i>3</i>
84	F MAX	Maximum value of F	F
85	_		
86	DEV_UMAX_MAX	Maximum value of DEV_UMAX	dE⊓l dE⊓l
87	DEV_IMAX_MAX	Maximum value of DEV_IMAX	U U
88	U_MIN	Minimum value of U	1
89	U1N_MIN	Minimum value of U1N	
90	U2N_MIN	Minimum value of U2N	U 2n
91	U3N_MIN	Minimum value of U3N	U 3n
92	U12_MIN	Minimum value of U12	U 12
93	U23_MIN	Minimum value of U23	U 23
94	U31_MIN	Minimum value of U31	U 31
95	PF_MIN_IN_L	Minimum active power factor, incoming/inductive	PF. IL
96	PF_MIN_IN_C	Minimum active power factor, incoming/capacitive	PF. IC
97	PF_MIN_OUT_L	Minimum active power factor, outgoing/inductive	PF.oL
98	PF_MIN_OUT_C	Minimum active power factor, outgoing/capacitive	PF.o.C
99	F_MIN	Minimum value of f	F
100	PIN	P incoming	Pin

No.	Name	Description	Name (Display)
101	P1IN	P1 incoming	Pinl
102	P2IN	P2 incoming	P in2
103	P3IN	P3 incoming	P in 3
104	POUT	P outgoing	PoUL
105	P1OUT	P1 outgoing	PoUL
106	P2OUT	P2 outgoing	PoUL
107	P3OUT	P3 outgoing	PoUL
108	PIN_OUT	P incoming-outgoing	P in 0
109	P1IN_OUT	P1 incoming-outgoing	PinO
110	P2IN_OUT	P2 incoming-outgoing	PinO
111	P3IN_OUT	P3 incoming-outgoing	P in D
112	QIND	Q inductive	9 ind
113	Q1IND	Q1 inductive	9 ind
114	Q2IND	Q2 inductive	9 ind
115	Q3IND	Q3 inductive	9 ind
116	QCAP	Q capacitive	9c AP
117	Q1CAP	Q1 capacitive	9c AP
118	Q2CAP	Q2 capacitive	9c AP
119	Q3CAP	Q3 capacitive	9 <i>c</i>
120	QIN	Q incoming	9 10
121	Q1IN	Q1 incoming	9 10
122	Q2IN	Q2 incoming	9 10
123	Q3IN	Q3 incoming	9 10
124	QOUT	Q outgoing	90UE
125	Q10UT	Q1 outgoing	90UE
126	Q2OUT	Q2 outgoing	9 ₀ UL
127	Q3OUT	Q3 outgoing	9 ₀ UE
128	QIN_OUT	Q incoming-outgoing	9 100
129	Q1IN_OUT	Q1 incoming-outgoing	9 100
130	Q2IN_OUT	Q2 incoming-outgoing	9 100
131	Q3IN_OUT UR1	Q3 incoming-outgoing Positive sequence voltage	Ur I
133	UR2	Negative sequence voltage	Ur2
134	U0	Zero sequence voltage	UD UD
135	IR1	Positive sequence current	Irl
136	IR2	Negative sequence current	1-2
137	10	Zero sequence current	10
138	UNB_UR2_UR1	Unbalance factor voltage UR2/UR1	Ur21
139	UNB IR2 IR1	Unbalance factor current IR2/IR1	1,21
140	UNB_U0_UR1	Unbalance factor voltage U0/UR1	Ur O I
141	UNB_I0_IR1	Unbalance factor current I0/IR1	1-01
142	THD_U	Total Harmonic Distortion of U	EhdU
143	THD_U1N	Total Harmonic Distortion of U1N	EhdU
144	THD_U2N	Total Harmonic Distortion of U2N	EhdU
145	THD_U3N	Total Harmonic Distortion of U3N	EhdU
146	THD_U12	Total Harmonic Distortion of U12	EhdU
147	THD_U23	Total Harmonic Distortion of U23	EhdU
148	THD_U31	Total Harmonic Distortion of U31	EhdU
149	TDD_I	Total Demand Distortion of I	Eddl
150	TDD_I1	Total Demand Distortion of I1	Eddl
151	TDD_I2	Total Demand Distortion of I2	Eddl
152	TDD_I3	Total Demand Distortion of I3	Eddl
153	D	Distortion reactive power system	d
154	D1	Distortion reactive power phase L1	dΙ

155 D2 Distortion reactive power phase L2 d2 156 D3 Distortion reactive power phase L3 d3 157 QG Reactive power fundamental system 9 H I 158 QG1 Reactive power fundamental phase L1 9 H I 159 QG2 Reactive power fundamental phase L2 9 H I 160 QG3 Reactive power fundamental phase L3 9 H I 161 PFG cos(q) of fundamental phase L3 2 P h I 162 PFG1 cos(q) of fundamental phase L2 2 P h I 163 PFG2 cos(q) of fundamental phase L2 2 P h I 164 PFG3 cos(q) of fundamental phase L3 2 P h I 165 TG1 tan(q) of fundamental phase L3 2 P h I 162 TG1 tan(q) of fundamental phase L2 2 P h I 163 TG2 tan(q) of fundamental phase L3 2 P h I 164 TG3 tan(q) of fundamental phase L3 2 P h I 164 TG3 tan(q) of fundamental phase L2 2 P h I 164<	No.	Name	Description	Name (Display)
157 QG Reactive power fundamental system 9 H I 158 QG1 Reactive power fundamental phase L1 9 H I 159 QG2 Reactive power fundamental phase L1 9 H I 150 QG2 Reactive power fundamental phase L2 9 H I 150 QG3 Reactive power fundamental phase L3 9 H I 150 QG3 Reactive power fundamental phase L3 9 H I 151 PFG cos(φ) of fundamental phase L1 cPh . 152 PFG1 cos(φ) of fundamental phase L1 cPh . 153 PFG2 cos(φ) of fundamental phase L2 cPh . 154 PFG3 cos(φ) of fundamental phase L3 cPh . 155 PFG3 cos(φ) of fundamental phase L3 cPh . 156 PFG3 cos(φ) of fundamental phase L3 cPh . 157 TG1 tan(φ) of fundamental phase L3 cPh . 158 TG2 tan(φ) of fundamental phase L1 LPh . 159 TG3 TG2 tan(φ) of fundamental phase L1 LPh . 150 TG3 TG2 tan(φ) of fundamental phase L2 LPh . 150 UNB_UR2_UR1_MAX Max. unbalance factor voltage UR2/UR1 LP 2 I 157 UNB_UR2_UR1_MAX Max. unbalance factor current R2/IR1 LP 2 I 157 UNB_UR2_UR1_MAX Max. unbalance factor voltage UR2/UR1 LP 2 I 157 UNB_UR2_UR1_MAX Max. unbalance factor voltage UR2/UR1 LP 0 I 157 THD_UNM_X Max. vnbalance factor voltage UR2/UR1 LP 0 I 157 THD_UNM_X Max. Total Harmonic Distortion of U LP LP 0 I 157 THD_UNM_X Max. Total Harmonic Distortion of UN LP LP 0 I 157 THD_UNM_X Max. Total Harmonic Distortion of UN LP LP 0 I 157 THD_USM_X Max. Total Harmonic Distortion of UN LP LP 0 I 158 THD_USM_X Max. Total Harmonic Distortion of UN LP LP 0 I 158 THD_USM_X Max. Total Harmonic Distortion of UN LP 0 I 159 THD_USM_X Max. Total Harmonic Distortion of UN LP 0 I 150 THD_USM_X Max. Total Harmonic Distortion of UN LP 0 I 150 THD_USM_X Max. Total Demand Distortion of UN LP 0 I 150 THD_USM_X Max. Total Demand Distortion of UN LP 0 I 150 THD_USM_X Max. Total Demand Distortion of UN LP 0 I 150 THD_USM_X Max. Total Demand Distortion of UN LP 0 I 150 THD_USM_X Max. Total Demand Distortion of UN LP 0 I 150 THD_USM_X Max. Total Demand Distortion of UN LP 0 I 150 THD_USM_X Max. Total Demand Distortion of UN LP 0 I 150 THD_USM_X Max. Total Demand Distortion of UN LP 0 I 150 THD_USM_X Max. Total Demand Distorti	155	D2		1 1 1 1 1 1 1
158 QG1	156	D3	Distortion reactive power phase L3	d3
159 QG2 Reactive power fundamental phase L2 9 H 1 160 QG3 Reactive power fundamental phase L3 9 H 1 161 PFG Cos(φ) of fundamental phase L3 9 H 1 162 PFG Cos(φ) of fundamental phase L3 c.Ph c.Ph Cos(φ) of fundamental phase L1 c.Ph c.Ph Cos(φ) of fundamental phase L2 c.Ph c.Ph Cos(φ) of fundamental phase L3 c.Ph c.Ph Cos(φ) of fundamental phase L1 c.Ph c.Ph Cos(φ) of fundamental phase L1 c.Ph c.Ph Cos(φ) of fundamental phase L3 c.Ph c.	157	QG	Reactive power fundamental system	9 Н І
160 QG3	158	QG1	Reactive power fundamental phase L1	9 Н І
161 PFG	159	QG2	Reactive power fundamental phase L2	9 Н І
162 PFG1 cos(φ) of fundamental phase L1 ePh , 163 PFG2 cos(φ) of fundamental phase L2 ePh , 164 PFG3 cos(φ) of fundamental phase L3 ePh , 164 PFG3 cos(φ) of fundamental phase L3 ePh , 165 TG1 tan(φ) of fundamental phase L1 ePh , 162 TG1 tan(φ) of fundamental phase L2 ePh , 164 TG3 tan(φ) of fundamental phase L3 ePh , 169 UNB_URZ_UR1_MAX Max. unbalance factor vottage UR2/UR1 Ur-2 I 170 UNB_LRZ_IR1_MAX Max. unbalance factor current IR2/IR1 Ur-2 I 171 UNB_LO_IR1_MAX Max. unbalance factor current IR2/IR1 Ur-2 I 171 UNB_LO_IR1_MAX Max. unbalance factor current IR2/IR1 Ur-2 I 172 UNB_LO_IR1_MAX Max. unbalance factor current IR2/IR1 Ur-2 I 171 UNB_LO_IR1_MAX Max. Total Harmonic Distortion of UR1 EhdU 173 THD_UANA Max. Total Harmonic Distortion of UR1 EhdU 175 THD_US1_MAX	160	QG3	Reactive power fundamental phase L3	9 Н І
163 PFG2 cos(φ) of fundamental phase L3 cPh 164 PFG3 cos(φ) of fundamental phase L3 cPh 161 TG tan(φ) of fundamental system bPh 162 TG1 tan(φ) of fundamental phase L1 bPh 163 TG2 tan(φ) of fundamental phase L3 bPh 169 UNB_UR2_UR1_MAX Max. unbalance factor voltage UR2/UR1 Ur ē I 170 UNB_JRZ_IR1_MAX Max. unbalance factor current IO/R1 Ur ē I 171 UNB_UR1_RAMX Max. unbalance factor current IO/R1 Ur ē I 172 UNB_UR1_MAX Max. unbalance factor current IO/R1 Ur ē I 173 THD_UAMAX Max. Total Harmonic Distortion of U IN b AdU 174 THD_UAMAX Max. Total Harmonic Distortion of U2N b AdU 175 THD_UAN_MAX Max. Total Harmonic Distortion of U2N b AdU 175 THD_UAN_MAX Max. Total Harmonic Distortion of U2N b AdU 176 THD_UAN_MAX Max. Total Harmonic Distortion of U2N b AdU 177 THD_UANA <td< td=""><td>161</td><td>PFG</td><td></td><td>cPh i</td></td<>	161	PFG		cPh i
164 PFG3 Cos(q) of fundamental phase L3 cPh 161 TG tan(q) of fundamental system EPh 162 TG1 tan(q) of fundamental phase L1 EPh 163 TG2 tan(q) of fundamental phase L2 EPh 164 TG3 tan(q) of fundamental phase L3 EPh 165 UNB_UR2_UR1_MAX Max. unbalance factor voltage UR2/UR1 Ur2 170 UNB_UR2_UR1_MAX Max. unbalance factor voltage UR2/UR1 Ur2 171 UNB_UO_UR1_MAX Max. unbalance factor voltage UNUR1 Ur2 172 UNB_IO_UR1_MAX Max. unbalance factor current IO/IR1 Ir2 173 THD_U_JMAX Max. unbalance factor current IO/IR1 Ir2 174 UNB_IO_UR1_MAX Max. unbalance factor current IO/IR1 Ir2 175 THD_UNAX Max. Total Harmonic Distortion of U EhdU 176 THD_UN_MAX Max. Total Harmonic Distortion of UN EhdU 177 THD_USN_MAX Max. Total Harmonic Distortion of UN EhdU 178 THD_USN_MAX Max. Total Harmonic Distortion of U3N EhdU 179 THD_USN_MAX Max. Total Harmonic Distortion of U3 EhdU 170 THD_USN_MAX Max. Total Harmonic Distortion of U3 EhdU 178 THD_USN_MAX Max. Total Harmonic Distortion of U3 EhdU 179 THD_USN_MAX Max. Total Harmonic Distortion of U3 EhdU 180 TDD_IAMAX Max. Total Demand Distortion of U1 EddI 181 TDD_IS_MAX Max. Total Demand Distortion of I1 EddI 182 TDD_IS_MAX Max. Total Demand Distortion of I2 EddI 183 TDD_IS_MAX Max. Total Demand Distortion of I3 EddI 184 D_MAX Max. distortion reactive power phase L1 d I 185 DI_MAX Max. distortion reactive power phase L2 d EddI 186 DZ_MAX Max. distortion reactive power phase L2 d EddI 186 DZ_MAX Max. distortion reactive power phase L2 d EddI 187 DS_MAX Max. reactive power fundamental phase L2 q H 190 QGZ_MAX Max. reactive power fundamental phase L2 q H 191 QGS_MAX Max. reactive power fundamental phase L2 q H 191 QGS_MAX Max. reactive power fundamental phase L3 q H 191 QGS_MA	162		***	cPh i
164 PFG3 cos(φ) of fundamental phase L3 cPh i 161 TG tan(φ) of fundamental system be ph i 162 TG1 tan(φ) of fundamental phase L1 be ph i 163 TG2 tan(φ) of fundamental phase L3 be ph i 164 TG3 tan(φ) of fundamental phase L3 be ph i 169 UNB_UR2_UR1_MAX Max. unbalance factor voltage UR2/UR1 Ur c2 I 170 UNB_IR2_IR1_MAX Max. unbalance factor current IR2/IR1 Ur c2 I 171 UNB_UO_UR1_MAX Max. unbalance factor current IO/IR1 Ur D I 172 UNB_IR2_IR1_MAX Max. unbalance factor current IO/IR1 Ur D I 172 UNB_IR2_IR1_MAX Max. unbalance factor current IO/IR1 Ur D I 173 THD_UANAX Max. Total Harmonic Distortion of U IV BehdU behdU 174 THD_UANAX Max. Total Harmonic Distortion of U2N BehdU behdU 175 THD_U3N_MAX Max. Total Harmonic Distortion of U2 BehdU behdU 176 THD_U31_MAX Max. Total Harmonic Distortion of U2 BehdU behdU	163	PFG2	cos(φ) of fundamental phase L2	cPh i
162 TG1 tan(q) of fundamental phase L1 £Ph i 163 TG2 tan(q) of fundamental phase L2 £Ph i 164 TG3 tan(q) of fundamental phase L3 £Ph i 169 UNB_UR2_UR1_MAX Max. unbalance factor voltage UR2UR1 Ur ē I 170 UNB_GLRZ_IR1_MAX Max. unbalance factor voltage UO/UR1 Ur ē I 171 UNB_OU_R1_MAX Max. unbalance factor current IO/IR1 I r ē I 172 UNB_OU_R1_MAX Max. Total Harmonic Distortion of U IN £hdU 173 THD_UMAX Max. Total Harmonic Distortion of U1N £hdU 174 THD_U1N_MAX Max. Total Harmonic Distortion of U2N £hdU 175 THD_U2N_MAX Max. Total Harmonic Distortion of U3N £hdU 176 THD_U3N_MAX Max. Total Harmonic Distortion of U12 £hdU 177 THD_U3A_MAX Max. Total Harmonic Distortion of U12 £hdU 178 THD_U31_MAX Max. Total Demand Distortion of U31 £hdU 180 TDD_I3_MAX Max. Total Demand Distortion of I1 £ddI 181	164	PFG3		cPh i
163 TG2 tan(φ) of fundamental phase L3 EPh , 164 TG3 tan(φ) of fundamental phase L3 EPh , 169 UNB_UR2_UR1_MAX Max. unbalance factor voltage UR2/UR1 Ur ē I 170 UNB_IR2_IR1_MAX Max. unbalance factor voltage UO/UR1 Ur ē I 171 UNB_IO_UR1_MAX Max. unbalance factor current IO/IR1 I r ē I 172 UNB_IO_UR1_MAX Max. unbalance factor current IO/IR1 I r ē I 173 THD_UMAX Max. Total Harmonic Distortion of U IN EhdU 174 THD_UTA_MAX Max. Total Harmonic Distortion of U1N EhdU 175 THD_USN_MAX Max. Total Harmonic Distortion of U3N EhdU 176 THD_USN_MAX Max. Total Harmonic Distortion of U3 EhdU 177 THD_USA_MAX Max. Total Harmonic Distortion of U3 EhdU 178 THD_USA_MAX Max. Total Harmonic Distortion of U3 EhdU 179 THD_USA_MAX Max. Total Harmonic Distortion of U3 EhdU 180 TDD_IS_MAX Max. Total Demand Distortion of U3 EhdU	161	TG	tan(φ) of fundamental system	EPh i
164 TG3 tan(φ) of fundamental phase L3 EPh ₁ 169 UNB_UR2_UR1_MAX Max. unbalance factor voltage UR2/UR1 Ur ≥ I 170 UNB_UR2_IR1_MAX Max. unbalance factor voltage U0/UR1 Ur ≥ I 171 UNB_UO_UR1_MAX Max. unbalance factor voltage U0/UR1 Ur □ I 172 UNB_IO_IR1_MAX Max. unbalance factor current I0/IR1 I r □ I 173 THD_UAMAX Max. Total Harmonic Distortion of U EhdU 174 THD_UN_MAX Max. Total Harmonic Distortion of U1N EhdU 175 THD_UN_MAX Max. Total Harmonic Distortion of U3N EhdU 176 THD_UN_MAX Max. Total Harmonic Distortion of U3N EhdU 177 THD_U2_MAX Max. Total Harmonic Distortion of U31 EhdU 178 THD_U3_MAX Max. Total Harmonic Distortion of U31 EhdU 180 TDD_IAMAX Max. Total Demand Distortion of U31 EhdU 181 TDD_I2_MAX Max. Total Demand Distortion of I3 EddI 182 TDD_I2_MAX Max. Total Demand Distortion of I3 EddI	162	TG1	tan(φ) of fundamental phase L1	EPh i
169 UNB_UR2_UR1_MAX Max. unbalance factor voltage UR2/UR1 Ur2 I 170 UNB_IR2_IR1_MAX Max. unbalance factor current IR2/IR1 I r2 I 171 UNB_UO_UR1_MAX Max. unbalance factor voltage UOUR1 Ur D I 172 UNB_IO_IR1_MAX Max. unbalance factor current IO/IR1 I r D I 173 THD_U_MAX Max. Total Harmonic Distortion of U E hdU 174 THD_U1N_MAX Max. Total Harmonic Distortion of U2N E hdU 175 THD_U1N_MAX Max. Total Harmonic Distortion of U2N E hdU 176 THD_U12_MAX Max. Total Harmonic Distortion of U3N E hdU 177 THD_U12_MAX Max. Total Harmonic Distortion of U3N E hdU 178 THD_U3_MAX Max. Total Harmonic Distortion of U12 E hdU 179 THD_U3_MAX Max. Total Harmonic Distortion of U12 E hdU 180 TDD_I3_MAX Max. Total Demand Distortion of I E hdU 181 TDD_I2_MAX Max. Total Demand Distortion of I1 E hdU 182 TDD_I3_MAX Max. Total Demand Distortion of I2 E hdU	163	TG2	tan(φ) of fundamental phase L2	EPh i
170	164	TG3	tan(φ) of fundamental phase L3	EPh i
171 UNB_UO_UR1_MAX	169	UNB_UR2_UR1_MAX	Max. unbalance factor voltage UR2/UR1	Ur21
172	170	UNB_IR2_IR1_MAX	Max. unbalance factor current IR2/IR1	1-21
172 UNB_I0_IR1_MAX Max. unbalance factor current I0/IR1 I r II I 173 THD_U_MAX Max. Total Harmonic Distortion of U EhdU 174 THD_UIN_MAX Max. Total Harmonic Distortion of U1N EhdU 175 THD_UIN_MAX Max. Total Harmonic Distortion of U2N EhdU 176 THD_U3N_MAX Max. Total Harmonic Distortion of U3N EhdU 177 THD_U12_MAX Max. Total Harmonic Distortion of U12 EhdU 178 THD_U23_MAX Total Harmonic Distortion of U23 EhdU 179 THD_U3_MAX Max. Total Demand Distortion of U31 EhdU 180 TDD_I_MAX Max. Total Demand Distortion of I3 EddI 181 TDD_I3_MAX Max. Total Demand Distortion of I2 EddI 182 TDD_I3_MAX Max. Total Demand Distortion of I3 EddI 184 D_MAX Max. Total Demand Distortion of I3 EddI 185 D1_MAX Max. Total Demand Distortion of I3 EddI 186 D2_MAX Max. distortion reactive power phase L1 d I 186 <	171		Max. unbalance factor voltage U0/UR1	+
173 THD_U_MAX Max. Total Harmonic Distortion of U EhdU 174 THD_U1N_MAX Max. Total Harmonic Distortion of U1N EhdU 175 THD_U2N_MAX Max. Total Harmonic Distortion of U2N EhdU 176 THD_U3N_MAX Max. Total Harmonic Distortion of U2N EhdU 177 THD_U31_MAX Max. Total Harmonic Distortion of U12 EhdU 178 THD_U31_MAX Max. Total Harmonic Distortion of U31 EhdU 179 THD_U31_MAX Max. Total Demand Distortion of U31 EhdU 180 TDD_I_MAX Max. Total Demand Distortion of I1 EddI 181 TDD_I_MAX Max. Total Demand Distortion of I2 EddI 182 TDD_I_MAX Max. Total Demand Distortion of I3 EddI 183 TDD_I_S_MAX Max. distortion reactive power system d 184 D_MAX Max. distortion reactive power phase L1 d I 185 D1_MAX Max. distortion reactive power phase L2 d 2 186 D2_MAX Max. reactive power fundamental system 9 H I 188			•	
175 THD_U2N_MAX Max. Total Harmonic Distortion of U2N E hdU 176 THD_U3N_MAX Max. Total Harmonic Distortion of U3N E hdU 177 THD_U12_MAX Max. Total Harmonic Distortion of U12 E hdU 178 THD_U23_MAX Total Harmonic Distortion of U23 E hdU 179 THD_U3_MAX Max. Total Harmonic Distortion of U31 E hdU 180 TDD_I3_MAX Max. Total Demand Distortion of U31 E hdU 181 TDD_I3_MAX Max. Total Demand Distortion of I1 E hdU 182 TDD_I2_MAX Max. Total Demand Distortion of I2 E hdU 183 TDD_I3_MAX Max. Total Demand Distortion of I3 E hdU 184 D_MAX Max. Total Demand Distortion of I3 E hdU 185 D1_MAX Max. distortion reactive power system d 185 D1_MAX Max. distortion reactive power phase L1 d I 186 D2_MAX Max. distortion reactive power phase L3 d J 187 D3_MAX Max. distortion reactive power phase L3 d J 188 <t< td=""><td>173</td><td></td><td>Max. Total Harmonic Distortion of U</td><td>EhdU</td></t<>	173		Max. Total Harmonic Distortion of U	EhdU
175 THD_U2N_MAX Max. Total Harmonic Distortion of U2N EhdU 176 THD_U3N_MAX Max. Total Harmonic Distortion of U3N EhdU 177 THD_U12_MAX Max. Total Harmonic Distortion of U12 EhdU 178 THD_U23_MAX Total Harmonic Distortion of U23 EhdU 179 THD_U31_MAX Max. Total Harmonic Distortion of U31 EhdU 180 TDD_I3_MAX Max. Total Demand Distortion of U31 EhdU 180 TDD_I3_MAX Max. Total Demand Distortion of I1 EddI 181 TDD_I2_MAX Max. Total Demand Distortion of I2 EddI 182 TDD_I3_MAX Max. Total Demand Distortion of I3 EddI 183 TDD_I3_MAX Max. Total Demand Distortion of I3 EddI 184 D_MAX Max. distortion reactive power system d 185 D1_MAX Max. distortion reactive power phase L1 d I 186 D2_MAX Max. distortion reactive power phase L2 d 2 187 D3_MAX Max. distortion reactive power phase L3 d 3 188 QG	174		Max. Total Harmonic Distortion of U1N	EhdU
THD_U3N_MAX	175		Max. Total Harmonic Distortion of U2N	EhdU
177 THD_U12_MAX Max. Total Harmonic Distortion of U12 EhdU 178 THD_U23_MAX Total Harmonic Distortion of U23 EhdU 179 THD_U31_MAX Max. Total Harmonic Distortion of U31 EhdU 180 TDD_L_MAX Max. Total Demand Distortion of I EddI 181 TDD_L1_MAX Max. Total Demand Distortion of I1 EddI 182 TDD_L2_MAX Max. Total Demand Distortion of I2 EddI 183 TDD_L3_MAX Max. Total Demand Distortion of I3 EddI 184 D_MAX Max. Demand Distortion of I3 EddI 185 D1_MAX Max. Demand Distortion of I3 EddI 186 D2_MAX Max. Demand Distortion of I3 EddI 186 D1_MAX Max. Demand Distortion of I3 EddI 186 D2_MAX Max. distortion reactive power system d d 187 D3_MAX Max. distortion reactive power phase L2 d2 d2 187 D3_MAX Max. distortion reactive power phase L3 d3 d3 d3 <	176		Max. Total Harmonic Distortion of U3N	EhdU
178 THD_U23_MAX Total Harmonic Distortion of U23 EhdU 179 THD_U31_MAX Max. Total Harmonic Distortion of U31 EhdU 180 TDD_L_MAX Max. Total Demand Distortion of I EddI 181 TDD_I3_MAX Max. Total Demand Distortion of I3 EddI 182 TDD_I3_MAX Max. Total Demand Distortion of I3 EddI 183 TDD_I3_MAX Max. Total Demand Distortion of I3 EddI 184 D_MAX Max. distortion reactive power system d 185 D1_MAX Max. distortion reactive power phase L1 d I 186 D2_MAX Max. distortion reactive power phase L2 d 2 187 D3_MAX Max. distortion reactive power phase L3 d 3 188 QG_MAX Max. reactive power fundamental system 9 H I 189 QG1_MAX Max. reactive power fundamental phase L1 9 H I 190 QG2_MAX Max. reactive power fundamental phase L3 9 H I 191 QG3_MAX Max. reactive power fundamental phase L3 9 H I 192 PFG_M	177		Max. Total Harmonic Distortion of U12	EhdU
THD_U31_MAX				
TDD_I_MAX				
TDD_I1_MAX				<u> </u>
TDD_I2_MAX	181	TDD I1 MAX	Max. Total Demand Distortion of I1	Eddl
184 D_MAX Max. distortion reactive power system d 185 D1_MAX Max. distortion reactive power phase L1 d I 186 D2_MAX Max. distortion reactive power phase L2 d 2 187 D3_MAX Max. distortion reactive power phase L3 d 3 188 QG_MAX Max. reactive power fundamental system 9 H I 189 QG1_MAX Max. reactive power fundamental phase L1 9 H I 190 QG2_MAX Max. reactive power fundamental phase L2 9 H I 191 QG3_MAX Max. reactive power fundamental phase L3 9 H I 192 PFG_MIN_IN_L Min. cos(φ) fundamental, incoming/inductive c P. i.L 193 PFG_MIN_IN_L Min. cos(φ) fundamental, incoming/inductive c P. i.L 194 PFG_MIN_OUT_L Min. cos(φ) fundamental, outgoing/capacitive c P. i.c 195 PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive c P. i.c 195 PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive c P. i.c 196 M1_PIN Mean-value 1: P incoming (last interval) P. i.nc 197 M2_PIN Mea				Eddl
185 D1_MAX Max. distortion reactive power phase L1 d I 186 D2_MAX Max. distortion reactive power phase L2 d Z 187 D3_MAX Max. distortion reactive power phase L3 d Z 188 QG_MAX Max. reactive power fundamental system 9 H I 189 QG1_MAX Max. reactive power fundamental phase L1 9 H I 190 QG2_MAX Max. reactive power fundamental phase L2 9 H I 191 QG3_MAX Max. reactive power fundamental phase L3 9 H I 192 PFG_MIN_IN_L Min. cos(φ) fundamental, incoming/inductive c P. i.L 193 PFG_MIN_IN_C Min. cos(φ) fundamental, incoming/inductive c P. i.L 194 PFG_MIN_OUT_L Min. cos(φ) fundamental, outgoing/capacitive c P. i.L 195 PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive c P. i.L 195 PFG_MIN_OUT_C Min. cos(φ) fundamental, incoming (last interval) P. i.nc 196 M1_PIN Mean-value 1: P incoming (last interval) P. i.nc 197 M2_PIN Mean-value 2: P incoming (interval t-2) P. i.nc 198 M3_PIN	183	TDD_I3_MAX	Max. Total Demand Distortion of I3	Eddi
186 D2_MAX Max. distortion reactive power phase L2 d² 187 D3_MAX Max. distortion reactive power phase L3 d³ 188 QG_MAX Max. reactive power fundamental system 9 H I 189 QG1_MAX Max. reactive power fundamental phase L1 9 H I 190 QG2_MAX Max. reactive power fundamental phase L2 9 H I 191 QG3_MAX Max. reactive power fundamental phase L3 9 H I 192 PFG_MIN_IN_L Min. cos(φ) fundamental, incoming/inductive cP. L 193 PFG_MIN_IN_C Min. cos(φ) fundamental, incoming/capacitive cP. L 194 PFG_MIN_OUT_L Min. cos(φ) fundamental, outgoing/capacitive cP.ac 195 PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive cP.ac 196 M1_PIN Mean-value 1: P incoming (last interval) P. Inc 197 M2_PIN Mean-value 2: P incoming (interval t-1) P. Inc 198 M3_PIN Mean-value 3: P incoming (interval t-2) P. Inc 200 M5_PIN Mean-value 5: P incoming (interval t-4) P. Inc 201 M1_POUT Mean-value 9: P out	184	D_MAX	Max. distortion reactive power system	В
187 D3_MAX Max. distortion reactive power phase L3 d3 188 QG_MAX Max. reactive power fundamental system 9 H I 189 QG1_MAX Max. reactive power fundamental phase L1 9 H I 190 QG2_MAX Max. reactive power fundamental phase L2 9 H I 191 QG3_MAX Max. reactive power fundamental phase L3 9 H I 192 PFG_MIN_IN_L Min. cos(φ) fundamental, incoming/inductive cP. iL 193 PFG_MIN_IN_C Min. cos(φ) fundamental, incoming/capacitive cP. ic 194 PFG_MIN_OUT_L Min. cos(φ) fundamental, outgoing/inductive cP.a. 195 PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive cP.a. 195 PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive cP.a. 196 M1_PIN Mean-value 1: P incoming (last interval) P. inc 197 M2_PIN Mean-value 2: P incoming (interval t-1) P. inc 198 M3_PIN Mean-value 4: P incoming (interval t-3) P. inc 199 M4_PIN Mean-value 5: P incoming (interval t-4) P. inc 201 M1_POUT Mea	185	D1_MAX	Max. distortion reactive power phase L1	d 1
188QG_MAXMax. reactive power fundamental system9 H I189QG1_MAXMax. reactive power fundamental phase L19 H I190QG2_MAXMax. reactive power fundamental phase L29 H I191QG3_MAXMax. reactive power fundamental phase L39 H I192PFG_MIN_IN_LMin. cos(φ) fundamental, incoming/inductivecP. ιL193PFG_MIN_IN_CMin. cos(φ) fundamental, incoming/capacitivecP. ιc194PFG_MIN_OUT_LMin. cos(φ) fundamental, outgoing/capacitivecP.ac195PFG_MIN_OUT_CMin. cos(φ) fundamental, outgoing/capacitivecP.ac196M1_PINMean-value 1: P incoming (last interval)P. ιnc197M2_PINMean-value 2: P incoming (interval t-1)P. ιnc198M3_PINMean-value 3: P incoming (interval t-2)P. ιnc199M4_PINMean-value 4: P incoming (interval t-4)P. ιnc200M5_PINMean-value 5: P incoming (interval t-4)P. ınc201M1_POUTMean-value 1: P outgoing (last interval)P.aUŁ202M2_POUTMean-value 2: P outgoing (interval t-1)P.aUŁ203M3_POUTMean-value 4: P outgoing (interval t-3)P.aUŁ204M4_POUTMean-value 5: P outgoing (interval t-4)P.aUŁ205M5_POUTMean-value 5: P outgoing (interval t-4)P.aUŁ206M1_QINMean-value 1: Q incoming (last interval)9 ınc207M2_QINMean-value 2: Q incoming (interval t-1)9 ınc <td>186</td> <td>D2_MAX</td> <td>Max. distortion reactive power phase L2</td> <td>42</td>	186	D2_MAX	Max. distortion reactive power phase L2	42
189QG1_MAXMax. reactive power fundamental phase L19 H I190QG2_MAXMax. reactive power fundamental phase L29 H I191QG3_MAXMax. reactive power fundamental phase L39 H I192PFG_MIN_IN_LMin. cos(φ) fundamental, incoming/inductivecP. iL193PFG_MIN_IN_CMin. cos(φ) fundamental, incoming/capacitivecP. ic194PFG_MIN_OUT_LMin. cos(φ) fundamental, outgoing/inductivecP.a.195PFG_MIN_OUT_CMin. cos(φ) fundamental, outgoing/capacitivecP.a.196M1_PINMean-value 1: P incoming (last interval)P. inc197M2_PINMean-value 2: P incoming (interval t-1)P. inc198M3_PINMean-value 3: P incoming (interval t-2)P. inc199M4_PINMean-value 4: P incoming (interval t-3)P. inc200M5_PINMean-value 5: P incoming (interval t-4)P. inc201M1_POUTMean-value 1: P outgoing (last interval)P.aUL202M2_POUTMean-value 2: P outgoing (interval t-1)P.aUL203M3_POUTMean-value 3: P outgoing (interval t-2)P.aUL204M4_POUTMean-value 4: P outgoing (interval t-3)P.aUL205M5_POUTMean-value 5: P outgoing (interval t-4)P.aUL206M1_QINMean-value 2: Q incoming (interval t-1)P.aUL207M2_QINMean-value 2: Q incoming (interval t-1)9 inc	187	D3_MAX	Max. distortion reactive power phase L3	d3
190 QG2_MAX Max. reactive power fundamental phase L2 9 H I 191 QG3_MAX Max. reactive power fundamental phase L3 9 H I 192 PFG_MIN_IN_L Min. cos(φ) fundamental, incoming/inductive cP. i.L 193 PFG_MIN_IN_C Min. cos(φ) fundamental, incoming/capacitive cP. i.c 194 PFG_MIN_OUT_L Min. cos(φ) fundamental, outgoing/inductive cP.a.c 195 PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive cP.a.c 196 M1_PIN Mean-value 1: P incoming (last interval) P. i.n.c 197 M2_PIN Mean-value 2: P incoming (interval t-1) P. i.n.c 198 M3_PIN Mean-value 3: P incoming (interval t-2) P. i.n.c 199 M4_PIN Mean-value 4: P incoming (interval t-3) P. i.n.c 200 M5_PIN Mean-value 5: P incoming (interval t-4) P. i.n.c 201 M1_POUT Mean-value 1: P outgoing (last interval) P. a.l.b 202 M2_POUT Mean-value 2: P outgoing (interval t-1) P. a.l.b 203 M3_POUT Mean-value 3: P outgoing (interval t-2) P. a.l.b 204 M4_POUT Mean-value 4: P outgoing (interval t-3) P. a.l.b 205 M5_POUT Mean-value 5: P outgoing (interval t-3) P. a.l.b 206 M1_QIN Mean-value 1: Q incoming (last interval) P. a.l.b 207 M2_QIN Mean-value 2: Q incoming (interval t-1) P. a.l.c	188	QG_MAX	Max. reactive power fundamental system	9 Н І
191 QG3_MAX Max. reactive power fundamental phase L3	189	QG1_MAX	Max. reactive power fundamental phase L1	9 Н І
PFG_MIN_IN_L Min. cos(φ) fundamental, incoming/inductive PFG_MIN_IN_C Min. cos(φ) fundamental, incoming/capacitive PFG_MIN_OUT_L Min. cos(φ) fundamental, outgoing/inductive PFG_MIN_OUT_L Min. cos(φ) fundamental, outgoing/inductive PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive PFG_MIN_OUT_C Mean-value 1: P incoming (last interval) PFINC PFG_MIN_OUT_C Mean-value 2: P incoming (interval t-1) PFINC PFINC Mean-value 3: P incoming (interval t-2) PFINC Mean-value 4: P incoming (interval t-3) PFINC Mean-value 5: P incoming (interval t-4) PFINC MEAN-value 1: P outgoing (last interval) PFINC MEAN-value 2: P outgoing (interval t-1) PFINC MEAN-value 3: P outgoing (interval t-2) MA_POUT Mean-value 3: P outgoing (interval t-2) MA_POUT Mean-value 4: P outgoing (interval t-3) PFINC MA_POUT Mean-value 5: P outgoing (interval t-3) PAULE MA_POUT Mean-value 5: P outgoing (interval t-4) Mean-value 5: P outgoing (interval t-4) Mean-value 5: P outgoing (interval t-4) PFINC MEAN-value 5: P outgoing (interval t-4) PFINC MEAN-value 7: Paule MEAN-value 1: Q incoming (last interval) PFINC MEAN-value 1: Q incoming (last interval) PFINC MEAN-value 1: Q incoming (interval t-1) PFINC MEAN-value 1: Q incoming (interval t-1) PFINC MEAN-value 2: Q incoming (interval t-1)	190	QG2_MAX	Max. reactive power fundamental phase L2	9 Н І
PFG_MIN_IN_C Min. cos(φ) fundamental, incoming/capacitive <i>c.P. i.c.</i> 194 PFG_MIN_OUT_L Min. cos(φ) fundamental, outgoing/inductive <i>c.P.a.L.</i> 195 PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive <i>c.P.a.c.</i> 196 M1_PIN Mean-value 1: P incoming (last interval) <i>P. i.n.c.</i> 197 M2_PIN Mean-value 2: P incoming (interval t-1) <i>P. i.n.c.</i> 198 M3_PIN Mean-value 3: P incoming (interval t-2) <i>P. i.n.c.</i> 199 M4_PIN Mean-value 4: P incoming (interval t-3) <i>P. i.n.c.</i> 200 M5_PIN Mean-value 5: P incoming (interval t-4) <i>P. i.n.c.</i> 201 M1_POUT Mean-value 1: P outgoing (last interval) <i>P. all.t.</i> 202 M2_POUT Mean-value 2: P outgoing (interval t-1) <i>P. all.t.</i> 203 M3_POUT Mean-value 3: P outgoing (interval t-2) <i>P. all.t.</i> 204 M4_POUT Mean-value 4: P outgoing (interval t-3) <i>P. all.t.</i> 205 M5_POUT Mean-value 5: P outgoing (interval t-4) <i>P. all.t.</i> 206 M1_QIN Mean-value 1: Q incoming (last interval) <i>P. all.t.</i> 207 M2_QIN Mean-value 2: Q incoming (interval t-1)	191	QG3_MAX	Max. reactive power fundamental phase L3	9 H I
PFG_MIN_IN_C Min. cos(φ) fundamental, incoming/capacitive <i>cP. ic</i> 194 PFG_MIN_OUT_L Min. cos(φ) fundamental, outgoing/inductive <i>cP.a.</i> 195 PFG_MIN_OUT_C Min. cos(φ) fundamental, outgoing/capacitive <i>cP.a.</i> 196 M1_PIN Mean-value 1: P incoming (last interval) <i>P. inc</i> 197 M2_PIN Mean-value 2: P incoming (interval t-1) <i>P. inc</i> 198 M3_PIN Mean-value 3: P incoming (interval t-2) <i>P. inc</i> 199 M4_PIN Mean-value 4: P incoming (interval t-3) <i>P. inc</i> 200 M5_PIN Mean-value 5: P incoming (interval t-4) <i>P. inc</i> 201 M1_POUT Mean-value 1: P outgoing (last interval) <i>P. all</i> ± 202 M2_POUT Mean-value 2: P outgoing (interval t-1) <i>P. all</i> ± 203 M3_POUT Mean-value 3: P outgoing (interval t-2) <i>P. all</i> ± 204 M4_POUT Mean-value 4: P outgoing (interval t-3) <i>P. all</i> ± 205 M5_POUT Mean-value 5: P outgoing (interval t-4) <i>P. all</i> ± 206 M1_QIN Mean-value 1: Q incoming (last interval) <i>P. all</i> ± 207 M2_QIN Mean-value 2: Q incoming (interval t-1) <i>P. all</i> ±	192	PFG_MIN_IN_L	Min. cos(φ) fundamental, incoming/inductive	cP. iL
195PFG_MIN_OUT_CMin. cos(φ) fundamental, outgoing/capacitivec P. c196M1_PINMean-value 1: P incoming (last interval)P. cc197M2_PINMean-value 2: P incoming (interval t-1)P. cc198M3_PINMean-value 3: P incoming (interval t-2)P. cc199M4_PINMean-value 4: P incoming (interval t-3)P. cc200M5_PINMean-value 5: P incoming (interval t-4)P. cc201M1_POUTMean-value 1: P outgoing (last interval)P.aUL202M2_POUTMean-value 2: P outgoing (interval t-1)P.aUL203M3_POUTMean-value 3: P outgoing (interval t-2)P.aUL204M4_POUTMean-value 4: P outgoing (interval t-3)P.aUL205M5_POUTMean-value 5: P outgoing (interval t-4)P.aUL206M1_QINMean-value 1: Q incoming (last interval)Q cc207M2_QINMean-value 2: Q incoming (interval t-1)Q cc	193	PFG_MIN_IN_C		cP. 1c
196M1_PINMean-value 1: P incoming (last interval)P. Inc197M2_PINMean-value 2: P incoming (interval t-1)P. Inc198M3_PINMean-value 3: P incoming (interval t-2)P. Inc199M4_PINMean-value 4: P incoming (interval t-3)P. Inc200M5_PINMean-value 5: P incoming (interval t-4)P. Inc201M1_POUTMean-value 1: P outgoing (last interval)P.aUL202M2_POUTMean-value 2: P outgoing (interval t-1)P.aUL203M3_POUTMean-value 3: P outgoing (interval t-2)P.aUL204M4_POUTMean-value 4: P outgoing (interval t-3)P.aUL205M5_POUTMean-value 5: P outgoing (interval t-4)P.aUL206M1_QINMean-value 1: Q incoming (last interval)Q Inc207M2_QINMean-value 2: Q incoming (interval t-1)Q Inc	194	PFG_MIN_OUT_L		cP.oL
197 M2_PIN Mean-value 2: P incoming (interval t-1) P. Inc 198 M3_PIN Mean-value 3: P incoming (interval t-2) P. Inc 199 M4_PIN Mean-value 4: P incoming (interval t-3) P. Inc 200 M5_PIN Mean-value 5: P incoming (interval t-4) P. Inc 201 M1_POUT Mean-value 1: P outgoing (last interval) P.aUE 202 M2_POUT Mean-value 2: P outgoing (interval t-1) P.aUE 203 M3_POUT Mean-value 3: P outgoing (interval t-2) P.aUE 204 M4_POUT Mean-value 4: P outgoing (interval t-3) P.aUE 205 M5_POUT Mean-value 5: P outgoing (interval t-4) P.aUE 206 M1_QIN Mean-value 1: Q incoming (last interval) P.aUE 207 M2_QIN Mean-value 2: Q incoming (interval t-1) P.aUE	195	PFG_MIN_OUT_C	Min. cos(φ) fundamental, outgoing/capacitive	cP.oc
198M3_PINMean-value 3: P incoming (interval t-2)P. Inc199M4_PINMean-value 4: P incoming (interval t-3)P. Inc200M5_PINMean-value 5: P incoming (interval t-4)P. Inc201M1_POUTMean-value 1: P outgoing (last interval)P.aUL202M2_POUTMean-value 2: P outgoing (interval t-1)P.aUL203M3_POUTMean-value 3: P outgoing (interval t-2)P.aUL204M4_POUTMean-value 4: P outgoing (interval t-3)P.aUL205M5_POUTMean-value 5: P outgoing (interval t-4)P.aUL206M1_QINMean-value 1: Q incoming (last interval)Q Inc207M2_QINMean-value 2: Q incoming (interval t-1)Q Inc	196	M1_PIN	Mean-value 1: P incoming (last interval)	P. inc
199M4_PINMean-value 4: P incoming (interval t-3)P. Inc200M5_PINMean-value 5: P incoming (interval t-4)P. Inc201M1_POUTMean-value 1: P outgoing (last interval)P.aUL202M2_POUTMean-value 2: P outgoing (interval t-1)P.aUL203M3_POUTMean-value 3: P outgoing (interval t-2)P.aUL204M4_POUTMean-value 4: P outgoing (interval t-3)P.aUL205M5_POUTMean-value 5: P outgoing (interval t-4)P.aUL206M1_QINMean-value 1: Q incoming (last interval)Q Inc207M2_QINMean-value 2: Q incoming (interval t-1)Q Inc	197	M2_PIN	Mean-value 2: P incoming (interval t-1)	P. inc
199M4_PINMean-value 4: P incoming (interval t-3)P. Inc200M5_PINMean-value 5: P incoming (interval t-4)P. Inc201M1_POUTMean-value 1: P outgoing (last interval)P.aUE202M2_POUTMean-value 2: P outgoing (interval t-1)P.aUE203M3_POUTMean-value 3: P outgoing (interval t-2)P.aUE204M4_POUTMean-value 4: P outgoing (interval t-3)P.aUE205M5_POUTMean-value 5: P outgoing (interval t-4)P.aUE206M1_QINMean-value 1: Q incoming (last interval)Q Inc207M2_QINMean-value 2: Q incoming (interval t-1)Q Inc	198	M3_PIN	Mean-value 3: P incoming (interval t-2)	P. inc
201 M1_POUT Mean-value 1: P outgoing (last interval) P.aUE 202 M2_POUT Mean-value 2: P outgoing (interval t-1) P.aUE 203 M3_POUT Mean-value 3: P outgoing (interval t-2) P.aUE 204 M4_POUT Mean-value 4: P outgoing (interval t-3) P.aUE 205 M5_POUT Mean-value 5: P outgoing (interval t-4) P.aUE 206 M1_QIN Mean-value 1: Q incoming (last interval) Q inc 207 M2_QIN Mean-value 2: Q incoming (interval t-1) Q inc	199	M4_PIN	Mean-value 4: P incoming (interval t-3)	P. inc
202M2_POUTMean-value 2: P outgoing (interval t-1)P.aUL203M3_POUTMean-value 3: P outgoing (interval t-2)P.aUL204M4_POUTMean-value 4: P outgoing (interval t-3)P.aUL205M5_POUTMean-value 5: P outgoing (interval t-4)P.aUL206M1_QINMean-value 1: Q incoming (last interval)P.aUL207M2_QINMean-value 2: Q incoming (interval t-1)P.auc	200	M5_PIN	Mean-value 5: P incoming (interval t-4)	P. inc
203 M3_POUT Mean-value 3: P outgoing (interval t-2) 204 M4_POUT Mean-value 4: P outgoing (interval t-3) 205 M5_POUT Mean-value 5: P outgoing (interval t-4) 206 M1_QIN Mean-value 1: Q incoming (last interval) 207 M2_QIN Mean-value 2: Q incoming (interval t-1) P.aUE P.aUE P.aUE P.aUE P.aUE P.aUE P.aUE P.aUE P.aUE	201	M1_POUT	Mean-value 1: P outgoing (last interval)	P.oUL
204M4_POUTMean-value 4: P outgoing (interval t-3)P.aUE205M5_POUTMean-value 5: P outgoing (interval t-4)P.aUE206M1_QINMean-value 1: Q incoming (last interval)Q inc207M2_QINMean-value 2: Q incoming (interval t-1)Q inc	202	M2_POUT	Mean-value 2: P outgoing (interval t-1)	P.oUL
205 M5_POUT Mean-value 5: P outgoing (interval t-4) 206 M1_QIN Mean-value 1: Q incoming (last interval) 207 M2_QIN Mean-value 2: Q incoming (interval t-1) P.DUE P.DUE P.DUE	203	M3_POUT	Mean-value 3: P outgoing (interval t-2)	P.oUL
206 M1_QIN Mean-value 1: Q incoming (last interval) Q INC 207 M2_QIN Mean-value 2: Q incoming (interval t-1) Q INC	204	M4_POUT	Mean-value 4: P outgoing (interval t-3)	P.oUL
207 M2_QIN Mean-value 2: Q incoming (interval t-1)	205	M5_POUT	Mean-value 5: P outgoing (interval t-4)	P.oUL
207 M2_QIN Mean-value 2: Q incoming (interval t-1)	206	M1_QIN	Mean-value 1: Q incoming (last interval)	q inc
208 M3 QIN Mean-value 3: Q incoming (interval t-2)	207	M2_QIN	Mean-value 2: Q incoming (interval t-1)	q inc
	208	M3_QIN	Mean-value 3: Q incoming (interval t-2)	q inc

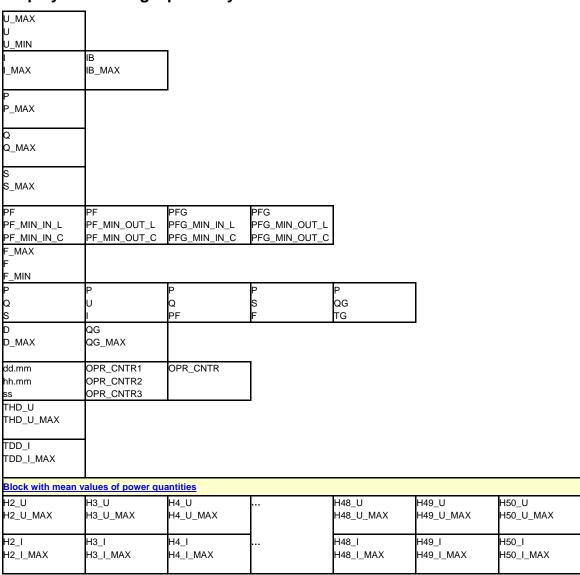
No.	Name	Description	Name (Display)
209	M4_QIN	Mean-value 4: Q incoming (interval t-3)	q inc
210	M5_QIN	Mean-value 5: Q incoming (interval t-4)	9 100
211	M1_QCAP	Mean-value 1: Q capacitive (last interval)	₹ CAP
212	M2_QCAP	Mean-value 2: Q capacitive (interval t-1)	₹ CAP
213	M3_QCAP	Mean-value 3: Q capacitive (interval t-2)	₹ CAP
214	M4_QCAP	Mean-value 4: Q capacitive (interval t-3)	₹ CAP
215	M5_QCAP	Mean-value 5: Q capacitive (interval t-4)	₹ CAP
216	M1_QIND	Mean-value 1: Q inductive (last interval)	9 ind
217	M2_QIND	Mean-value 2: Q inductive (interval t-1)	9 ind
218	M3_QIND	Mean-value 3: Q inductive (interval t-2)	9 ind
219	M4_QIND	Mean-value 4: Q inductive (interval t-3)	q ınd
220	M5_QIND	Mean-value 5: Q inductive (interval t-4)	q ınd
221	M1_QOUT	Mean-value 1: Q outgoing (last interval)	₹ oUŁ
222	M2_QOUT	Mean-value 2: Q outgoing (interval t-1)	₹ oUŁ
223	M3_QOUT	Mean-value 3: Q outgoing (interval t-2)	9.oUE
224	M4_QOUT	Mean-value 4: Q outgoing (interval t-3)	9.oUE
225	M5_QOUT	Mean-value 5: Q outgoing (interval t-4)	9.oUE
226	M1_S	Mean-value 1: S (last interval)	5
227	M2_S	Mean-value 2: S (interval t-1)	5
228	M3_S	Mean-value 3: S (interval t-2)	5
229	M4_S	Mean-value 4: S (interval t-3)	5
230	M5_S	Mean-value 5: S (interval t-4)	5
231	TR_PIN	Trend mean-value P incoming	Er.Pl
232	TR_POUT	Trend mean-value P outgoing	£r.PO
233	TR_QIND	Trend mean-value Q inductive	Er.9L
234	TR_QCAP	Trend mean-value Q capacitive	Er.9E
235	TR_QIN	Trend mean-value Q incoming	Er.91
236	TR_QOUT	Trend mean-value Q outgoing	£r.90
237	TR_S	Trend mean-value S	£r.5
238	M_PIN_MIN	Maximum mean-value P incoming	P. inc
239	M_POUT_MIN	Maximum mean-value P outgoing	P.oUŁ
240	M_QIND_MIN	Maximum mean-value Q inductive	9 ind
241	M_QCAP_MIN	Maximum mean-value Q capacitive	Q∈AP
242	M_QIN_MIN	Maximum mean-value Q incoming	q inc
243	M_QOUT_MIN	Maximum mean-value Q outgoing	9.oUE
244	M_S_MIN	Maximum mean-value S	5
245	M_PIN_MAX	Minimum mean-value P incoming	P. inc
246	M_POUT_MAX	Minimum mean-value P outgoing	P.oUE
247	M_QIND_MAX	Minimum mean-value Q inductive	9 ind
248	M_QCAP_MAX	Minimum mean-value Q capacitive	Q ∈ <i>RP</i>
249	M_QIN_MAX	Minimum mean-value Q incoming	q inc
250	M_QOUT_MAX	Minimum mean-value Q outgoing	9.oUE
251	M_S_MAX	Minimum mean-value S	5
252	M1	Mean-value 1	
253	M2	Mean-value 2	ñ 2
254	M3	Mean-value 3	л Э
255	M4	Mean-value 4	<i>4</i>
256	M5	Mean-value 5	ñ 5
257	M6	Mean-value 6	л Б
258	M7	Mean-value 7	.ī. 7
259	M8	Mean-value 8	л B
260	M9	Mean-value 9	.ī. 9
261	M10	Mean-value 10	ñ 10
262	M11	Mean-value 11	
	1	I	t

No.	Name	Description	Name (Display)
263	M12	Mean-value 12	ii 12
264	TR_1	Trend mean-value 1	Er I
265	TR_2	Trend mean-value 2	Er 2
266	TR_3	Trend mean-value 3	Er 3
267	TR_4	Trend mean-value 4	Er 4
268	TR_5	Trend mean-value 5	Er 5
269	TR_6	Trend mean-value 6	Er 5
270	TR_7	Trend mean-value 7	Er 7
271	TR_8	Trend mean-value 8	tr 8
272	TR_9	Trend mean-value 9	Er 9
273	TR_10	Trend mean-value 10	Er 10
274	TR_11	Trend mean-value 11	Erll
275	TR_12	Trend mean-value 12	Er 12
276	M1 MIN	Maximum mean-value 1	- i - i
277	M2 MIN	Maximum mean-value 2	ñ 2
278	M3 MIN	Maximum mean-value 3	ī 3
279	M4 MIN	Maximum mean-value 4	.ī. 4
280	M5 MIN	Maximum mean-value 5	<i>ī</i> . 5
281	M6_MIN	Maximum mean-value 6	л Б
282	M7_MIN	Maximum mean-value 7	7 7
283	M8_MIN	Maximum mean-value 8	ī B
284	M9_MIN	Maximum mean-value 9	7. g
285	M10_MIN	Maximum mean-value 10	7. ID
286	M11_MIN	Maximum mean-value 11	7. LT
287	M12_MIN	Maximum mean-value 12	ii 12
288	M1 MAX	Minimum mean-value 1	
289	M2 MAX	Minimum mean-value 2	
290	M3_MAX	Minimum mean-value 3	
291	M4 MAX	Minimum mean-value 4	<u></u>
292	M5_MAX	Minimum mean-value 5	
293	M6_MAX	Minimum mean-value 6	7. 5
294	M7 MAX	Minimum mean-value 7	7 7
295	M8_MAX	Minimum mean-value 8	
296	M9_MAX	Minimum mean-value 9	g
297	M10_MAX	Minimum mean-value 10	
298	M11_MAX	Minimum mean-value 11	7.13
299	M12_MAX	Minimum mean-value 12	
300	AOUT1	Analog output 1	RD I
301	AOUT2	Analog output 2	RO2
301	AOUT3	Analog output 3	R03
303	AOUT4	Analog output 4	RO4
303	PIN_HT	Meter P incoming high tariff	PI.HE
304	POUT_HT	Meter P incoming high tariff	POHE
305			9L.HE
306	QIND_HT	Meter Q inductive high tariff Meter Q capacitive high tariff	9C.HE
	QCAP_HT	' '	91.HE
308	QIN_HT	Meter Q incoming high tariff	90.HE
309	QOUT_HT	Meter Q outgoing high tariff	
310	PIN_LT	Meter P incoming low tariff	PILE
311	POUT_LT	Meter P outgoing low tariff	POLE 9LLE
312	QIND_LT	Meter Q inductive low tariff	
313	QCAP_LT	Meter Q capacitive low tariff	9CLE
314	QIN_LT	Meter Q outgoing low tariff	91.LE
315	QOUT_LT	Meter Q outgoing low tariff	90LE
316	P1IN_HT	Meter P1 incoming high tariff	P II .H

No.	Name	Description	Name (Display)
317	P2IN_HT	Meter P2 incoming high tariff	P21.H
318	P3IN_HT	Meter P3 incoming high tariff	P3I.H
319	Q1IN_HT	Meter Q1 incoming high tariff	9 II.H
320	Q2IN_HT	Meter Q2 incoming high tariff	921.H
321	Q3IN_HT	Meter Q3 incoming high tariff	H. IEP
322	P1IN_LT	Meter P1 incoming low tariff	P II.L
323	P2IN_LT	Meter P2 incoming low tariff	P21.L
324	P3IN_LT	Meter P3 incoming low tariff	P3I.L
325	Q1IN_LT	Meter Q1 incoming low tariff	9 II.L
326	Q2IN_LT	Meter Q2 incoming low tariff	921.L
327	Q3IN_LT	Meter Q3 incoming low tariff	931.L
328	CNTR_IO2_HT	Meter I/O 2 high tariff	E 2.H
329	CNTR_IO6_HT	Meter I/O 6 high tariff	E 5.H
330	CNTR_IO7_HT	Meter I/O 7 high tariff	E 7H
331	CNTR_IO8_HT	Meter I/O 8 high tariff	E B.H
332	CNTR_IO9_HT	Meter I/O 9 high tariff	E 9.H
333	CNTR_IO10_HT	Meter I/O 10 high tariff	E IOH
334	CNTR_IO11_HT	Meter I/O 11 high tariff	EIUH
335	CNTR_IO2_LT	Meter I/O 2 low tariff	E 2.L
336	CNTR_IO6_LT	Meter I/O 6 low tariff	E 5.L
337	CNTR_IO7_LT	Meter I/O 7 low tariff	E 7L
352	CNTR_IO8_LT	Meter I/O 8 low tariff	E BL
353	CNTR_IO9_LT	Meter I/O 9 low tariff	E 9.L
354	CNTR_IO10_LT	Meter I/O 10 low tariff	E IOL
355	CNTR_IO11_LT	Meter I/O 11 low tariff	EILL
356	RTC_UTC	UTC time in seconds since January 1st 1970	U L C.L
357	EV_TIME	UTC time of last event	EuŁ.Ł
358	OPR_CNTR	Operating hour counter APLUS	DFC
359	OPR_CNTR1	Resettable operating hour counter 1	OFC I
360	OPR_CNTR2	Resettable operating hour counter 2	DF.C.5
361	OPR_CNTR3	Resettable operating hour counter 3	OFC3
362	RTC_LOCAL	Local time in seconds since January 1st 1970	L O C.E
363	H2_U1X :	Voltage phase 1: content of 2nd harmonic :	
424	H63_U1X	Voltage phase 1: content of 63rd harmonic	
425	H2_U2X	Voltage phase 2: content of 2nd harmonic	
486	H63_U2X	Voltage phase 2: content of 63rd harmonic	
487	H2_U3X	Voltage phase 3: content of 2nd harmonic	
548	H63_U3X	Voltage phase 3: content of 63rd harmonic	
549	H2_I1X	Current phase 1: content of 2nd harmonic :	
610	H63_I1X	Current phase 1: content of 63rd harmonic	
611	H2_I2X :	Current phase 2: content of 2nd harmonic :	
672	H63_I2X	Current phase 2: content of 63rd harmonic	
673	H2_I3X :	Current phase 3: content of 2nd harmonic :	
734	H63_I3X	Current phase 3: content of 63rd harmonic	
735	H2_U1X_MAX	Voltage phase 1: max. content of 2nd harmonic	
796	H63_U1X_MAX	Voltage phase 1: max. content of 63rd harmonic	
797	H2_U2X_MAX	Voltage phase 2: max. content of 2nd harmonic	
0.50	:	Voltage phase 2: may content of 22-d because it	
858	H63_U2X_MAX	Voltage phase 2: max. content of 63rd harmonic	

No.	Name	Description	Name (Display)
859	H2_U3X_MAX	Voltage phase 3: max. content of 2nd harmonic	
	:	:	
920	H63_U3X_MAX	Voltage phase 3: max. content of 63rd harmonic	
921	H2_I1X_MAX	Current phase 1: max. content of 2nd harmonic	
	:	:	
982	H63_I1X_MAX	Current phase 1: max. content of 63rd harmonic	
983	H2_I2X_MAX	Current phase 2: max. content of 2nd harmonic	
	:	:	
1044	H63_I2X_MAX	Current phase 2: max. content of 63rd harmonic	
1045	H2_I3X_MAX	Current phase 3: max. content of 2nd harmonic	
	:	:	
1106	H63_I3X_MAX	Current phase 3: max. content of 63rd harmonic	

B1 Display matrix single phase system



B2 Display matrix Split-phase (two-phase) systems

U1N	U1N_MAX	U1N_MIN	UNE			
U2N	U2N_MAX	U2N_MIN	UNE_MAX			
U	U_MAX	U_MIN	OTTE_IVIDAT			
11	I1_MAX	IB1	IB1_MAX			
12	I2_MAX	IB2	IB2_MAX			
P1	P1_MAX		1			
P2	P2_MAX					
Р	P_MAX					
Q1	Q1_MAX					
Q2	Q2_MAX					
Q	Q_MAX					
S1	S1_MAX					
S2	S2_MAX					
S	S_MAX					_
PF	PF	PF	PFG	PFG	PFG	1
PF1	PF_MIN_IN_L	PF_MIN_OUT_L	PFG1	PFG_MIN_IN_L	PFG_MIN_OUT_L	
PF2	PF_MIN_IN_C	PF_MIN_OUT_C	PFG2	PFG_MIN_IN_C	PFG_MIN_OUT_C	
F_MAX				<u> </u>		=
F						
F_MIN					_	
Р	Р	Р	Р	Р		
Q	U_MEAN	Q	S	QG		
S	I_MEAN	PF	F	TG		
P1	P2	U1N	U2N			
Q1	Q2	l1	12			
S1	S2	P1	P2			٦
D1	D1_MAX	D	QG1	QG1_MAX	QG	
D2	D2_MAX	D_MAX	QG2	QG2_MAX	QG_MAX	
dd.mm	OPR_CNTR1	OPR_CNTR				_
hh.mm	OPR_CNTR2					
SS	OPR_CNTR3					
THD_U1N	THD_U2N					
THD_U1N_MAX	THD_U2N_MAX					
TDD_I1 TDD_I1_MAX	TDD_l2 TDD_l2_MAX					
	values of power qu					
H2_U1N	H3_U1N	H4_U1N		H48_U1N	H49_U1N	H50_U1N
H2_U1N_MAX	H3_U1N_MAX	H4_U1N_MAX		H48_U1N_MAX	H49_U1N_MAX	H50_U1N_MAX
H2_U2N	H3_U2N	H4_U2N		H48_U2N	H49_U2N	H50_U2N
H2_U2N_MAX	H3_U2N_MAX	H4_U2N_MAX		H48_U2N_MAX	H49_U2N_MAX	H50_U2N_MAX
H2_I1	H3_I1	H4_I1	T	H48_I1	H49_I1	H50_I1
H2_I1_MAX	H3_I1_MAX	H4_I1_MAX		H48_I1_MAX	H49_I1_MAX	H50_I1_MAX
H2_I2	H3_I2	H4_I2	┪	H48_I2	H49_I2	H50_I2
H2_I2_MAX	H3_I2_MAX	H4_I2_MAX		H48_I2_MAX	H49_I2_MAX	H50_I2_MAX
	1			ĺ	1	

B3 Display matrix 3-wire system, balanced load

U12	U12_MAX	U12_MIN	DEV_UMAX	1		
U23	_	_	_			
	U23_MAX	U23_MIN	DEV_UMAX_MAX			
U31 UR1	U31_MAX	U31_MIN				
UR2	UNB_UR2_UR1					
UO	UNB_UR2_UR1_MAX					
	ID	4				
I I_MAX	IB IB_MAX					
P P_MAX		_				
Q Q_MAX						
S S_MAX						
PF	PF	PFG	PFG	1		
PF_MIN_IN_L	PF_MIN_OUT_L	PFG_MIN_IN_L	PFG_MIN_OUT_L			
PF_MIN_IN_C	PF_MIN_OUT_C	PFG_MIN_IN_C	PFG_MIN_OUT_C			
F_MAX						
F						
F_MIN				-		
P	Р	Р	Р			
Q	Q	S F	QG			
S	PF	F	TG			
D	QG	F	IIG	_		
		F	μG	1		
D D_MAX	QG QG_MAX			J		
D D_MAX dd.mm	QG QG_MAX OPR_CNTR1	OPR_CNTR	lig.	1		
D D_MAX dd.mm hh.mm	QG QG_MAX OPR_CNTR1 OPR_CNTR2		lig.	J		
D D_MAX dd.mm hh.mm ss	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3	OPR_CNTR		J		
D D_MAX dd.mm hh.mm ss THD_U12	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23	OPR_CNTR THD_U31		J		
D D_MAX dd.mm hh.mm ss	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3	OPR_CNTR]IG	J		
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23	OPR_CNTR THD_U31]IG	J		
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23	OPR_CNTR THD_U31]IG	J		
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23	OPR_CNTR THD_U31]IG	J		
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23 THD_U23_MAX	OPR_CNTR THD_U31 THD_U31_MAX]IG	J		
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23	OPR_CNTR THD_U31 THD_U31_MAX		J		
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23 THD_U23_MAX	OPR_CNTR THD_U31 THD_U31_MAX		H48_U12	H49_U12	H50_U12
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23 THD_U23_MAX	OPR_CNTR THD_U31 THD_U31_MAX		H48_U12 H48_U12_MAX	H49_U12 H49_U12_MAX	H50_U12 H50_U12_MAX
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX Block with mean-value.	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23 THD_U23_MAX Alues of power quantities H3_U12	OPR_CNTR THD_U31 THD_U31_MAX				_
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX Block with mean-value.	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23 THD_U23_MAX Alues of power quantities H3_U12	OPR_CNTR THD_U31 THD_U31_MAX				_
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX Block with mean-vi H2_U12_MAX	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23 THD_U23_MAX alues of power quantities H3_U12 H3_U12_MAX	OPR_CNTR THD_U31 THD_U31_MAX H4_U12 H4_U12_MAX]	H48_U12_MAX	H49_U12_MAX	H50_U12_MAX
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX Block with mean-vi H2_U12 H2_U12_MAX H2_U23 H2_U23_MAX	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23 THD_U23_MAX alues of power quantities H3_U12 H3_U12_MAX H3_U23_MAX	OPR_CNTR THD_U31 THD_U31_MAX H4_U12 H4_U12_MAX H4_U23 H4_U23_MAX]	H48_U12_MAX H48_U23 H48_U23_MAX	H49_U12_MAX H49_U23 H49_U23_MAX	H50_U12_MAX H50_U23 H50_U23_MAX
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX Block with mean-vi H2_U12 H2_U12_MAX H2_U23 H2_U23_MAX H2_U31	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23 THD_U23_MAX Alues of power quantities H3_U12 H3_U12_MAX H3_U23_MAX H3_U23_MAX H3_U23_MAX	OPR_CNTR THD_U31 THD_U31_MAX H4_U12 H4_U12_MAX H4_U23 H4_U23_MAX H4_U31]	H48_U12_MAX H48_U23 H48_U23_MAX H48_U23_MAX	H49_U12_MAX H49_U23 H49_U23_MAX H49_U31	H50_U12_MAX H50_U23 H50_U23_MAX H50_U31
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX Block with mean-vi H2_U12 H2_U12_MAX H2_U23 H2_U23_MAX	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23 THD_U23_MAX Alues of power quantities H3_U12 H3_U12_MAX H3_U23 H3_U23_MAX	OPR_CNTR THD_U31 THD_U31_MAX H4_U12 H4_U12_MAX H4_U23 H4_U23_MAX		H48_U12_MAX H48_U23 H48_U23_MAX	H49_U12_MAX H49_U23 H49_U23_MAX	H50_U12_MAX H50_U23 H50_U23_MAX
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX Block with mean-vi H2_U12 H2_U12_MAX H2_U23 H2_U23_MAX H2_U23_MAX H2_U31_MAX	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23 THD_U23_MAX Alues of power quantities H3_U12 H3_U12_MAX H3_U23 H3_U23_MAX H3_U23_MAX H3_U31_MAX	OPR_CNTR THD_U31 THD_U31_MAX H4_U12 H4_U12_MAX H4_U23 H4_U23_MAX H4_U31 H4_U31_MAX		H48_U12_MAX H48_U23 H48_U23_MAX H48_U31_MAX	H49_U12_MAX H49_U23 H49_U23_MAX H49_U31 H49_U31_MAX	H50_U12_MAX H50_U23 H50_U23_MAX H50_U31 H50_U31_MAX
D D_MAX dd.mm hh.mm ss THD_U12 THD_U12_MAX TDD_I TDD_I_MAX Block with mean-vi H2_U12 H2_U12_MAX H2_U23 H2_U23_MAX H2_U31	QG QG_MAX OPR_CNTR1 OPR_CNTR2 OPR_CNTR3 THD_U23 THD_U23_MAX Alues of power quantities H3_U12 H3_U12_MAX H3_U23_MAX H3_U23_MAX H3_U23_MAX	OPR_CNTR THD_U31 THD_U31_MAX H4_U12 H4_U12_MAX H4_U23 H4_U23_MAX H4_U31		H48_U12_MAX H48_U23 H48_U23_MAX H48_U23_MAX	H49_U12_MAX H49_U23 H49_U23_MAX H49_U31	H50_U12_MAX H50_U23 H50_U23_MAX H50_U31

B4 Display matrix 3-wire systems, unbalanced load

- -	L	- ·	T	=		
U12	U12_MAX	U12_MIN	DEV_UMAX			
U23	U23_MAX	U23_MIN	DEV_UMAX_MAX			
U31	U31_MAX	U31_MIN				
UR1 UR2	UNB_UR2_UR1 UNB_UR2_UR1_MA>					
U0	OND_ONZ_ONT_WAX	Ì				
l1	I1_MAX	IB1	IB1_MAX	DEV_IMAX	1	
i: 2	I2_MAX	IB2	IB2_MAX	DEV_IMAX_MAX		
13	I3_MAX	IB3	IB3_MAX	DE V_IND O(_IND O(
IR1	UNB_IR2_IR1		-	· I	4	
IR2	UNB_IR2_IR1_MAX					
10						
Р		_				
P_MAX						
Q Q_MAX]					
6	4					
S S_MAX						
PF	PF	PFG	PFG			
PF_MIN_IN_L	PF_MIN_OUT_L	PFG_MIN_IN_L	PFG_MIN_OUT_L			
PF_MIN_IN_C	PF_MIN_OUT_C	PFG_MIN_IN_C	PFG_MIN_OUT_C			
F_MAX						
F F_MIN						
<u>–</u> Р	Р	Р	Р	Р	1	
Q	U_MEAN	Q	S	QG		
S	I_MEAN	PF	F	TG		
D D_MAX	QG QG_MAX					
dd.mm	OPR_CNTR1	OPR_CNTR	٦			
hh.mm	OPR_CNTR2	oo				
SS	OPR_CNTR3					
THD_U12	THD_U23	THD_U31				
THD_U12_MAX	THD_U23_MAX	THD_U31_MAX				
TDD_I1	TDD_I2	TDD_I3				
TDD_I1_MAX	TDD_I2_MAX	TDD_I3_MAX				
Block with mean-	values of power quan	<u>tities</u>				
H2_U12	H3_U12	H4_U12	•••	H48_U12	H49_U12	H50_U12
H2_U12_MAX	H3_U12_MAX	H4_U12_MAX		H48_U12_MAX	H49_U12_MAX	H50_U12_MAX
H2_U23	H3_U23	H4_U23	┪	H48 U23	H49_U23	H50 U23
H2_U23_MAX	H3_U23_MAX	H4_U23_MAX		H48_U23_MAX	H49_U23_MAX	H50_U23_MAX
H2_U31	H3 U31	H4_U31	┪	H48_U31	H49_U31	H50_U31
H2_U31_MAX	H3_U31_MAX	H4_U31_MAX		H48_U31_MAX	H49_U31_MAX	H50_U31_MAX
H2_I1	H3_I1	H4_I1	╡	H48_I1	H49_I1	H50_I1
H2_I1_MAX	H3_I1_MAX	H4_I1_MAX		H48_I1_MAX	H49_I1_MAX	H50_I1_MAX
H2_I2	H3_I2	H4_I2	┪	H48_I2	H49_l2	H50_I2
H2_I2_MAX	H3_I2_MAX	H4_I2_MAX		H48_I2_MAX	H49_I2_MAX	H50_I2_MAX
		1		-	 	-
H2 I3	H3 I3	H4 I3		H48 I3	H49 I3	H50 I3
H2_I3 H2_I3_MAX	H3_I3 H3_I3_MAX	H4_I3 H4_I3_MAX		H48_I3 H48_I3_MAX	H49_I3 H49_I3_MAX	H50_I3 H50_I3_MAX

B5 Display matrix 3-wire systems, unbalanced load, Aron

1110	III A MAN	LIAO MINI	DEV HAAV	=1		
U12	U12_MAX	U12_MIN	DEV_UMAX			
U23	U23_MAX	U23_MIN	DEV_UMAX_MAX			
U31	U31_MAX	U31_MIN				
UR1	UNB_UR2_UR1					
UR2	UNB_UR2_UR1_MAX					
U0					-1	
l1	I1_MAX	IB1	IB1_MAX	DEV_IMAX		
12	I2_MAX	IB2	IB2_MAX	DEV_IMAX_MAX		
13	I3_MAX	IB3	IB3_MAX			
Р						
P_MAX						
Q						
Q_MAX						
S						
S_MAX						
PF	PF	PFG	PFG			
PF_MIN_IN_L	PF_MIN_OUT_L	PFG_MIN_IN_L	PFG_MIN_OUT_L			
PF_MIN_IN_C	PF_MIN_OUT_C	PFG_MIN_IN_C	PFG_MIN_OUT_C			
F_MAX				—		
F						
F_MIN						
P	Р	Р	Р	Р	1	
Q	U_MEAN	Q	S	QG		
S	I_MEAN	PF	F	TG		
D	QG	<u> </u>	ľ		_	
D_MAX	QG_MAX					
D_1411 U.C	QO_INII UK					
dd.mm	OPR_CNTR1	OPR_CNTR	7			
hh.mm	OPR_CNTR2	or re_orrer				
ss	OPR_CNTR3					
THD_U12	THD_U23	THD_U31	=			
THD_U12_MAX	THD_U23_MAX	THD_U31_MAX				
TTID_012_WAX	111D_023_WAX	TTID_031_WAX				
TDD_I1	TDD_I2	TDD_I3	_			
TDD_I1_MAX	TDD_I2 TDD_I2_MAX	TDD_I3_MAX				
I DD_II_WAX	TDD_IZ_IVIAX	I DD_IS_IVIAX				
Disabasith second		_ <u> </u>			_	•
	values of power quantitie		_	Lance		L
H2_U12	H3_U12	H4_U12		H48_U12	H49_U12	H50_U12
H2_U12_MAX	H3_U12_MAX	H4_U12_MAX		H48_U12_MAX	H49_U12_MAX	H50_U12_MAX
			_			
H2_U23	H3_U23	H4_U23		H48_U23	H49_U23	H50_U23
H2_U23_MAX	H3_U23_MAX	H4_U23_MAX		H48_U23_MAX	H49_U23_MAX	H50_U23_MAX
		1	╛			
H2_U31	H3_U31	H4_U31		H48_U31	H49_U31	H50_U31
H2_U31_MAX	H3_U31_MAX	H4_U31_MAX	1	H48_U31_MAX	H49_U31_MAX	H50_U31_MAX
I						
H2_I1	H3_I1	H4_I1		H48_I1	H49_I1	H50_I1
H2_I1 H2_I1_MAX		H4_I1 H4_I1_MAX		H48_I1 H48_I1_MAX	H49_I1 H49_I1_MAX	H50_I1 H50_I1_MAX
	H3_I1		Ī			
	H3_I1					
H2_I1_MAX	H3_I1 H3_I1_MAX	H4_I1_MAX	_	H48_I1_MAX	H49_I1_MAX	H50_I1_MAX
H2_I1_MAX H2_I2	H3_I1 H3_I1_MAX H3_I2	H4_I1_MAX H4_I2	_	H48_I1_MAX H48_I2	H49_I1_MAX H49_I2	H50_I1_MAX H50_I2
H2_I1_MAX H2_I2 H2_I2_MAX	H3_I1 H3_I1_MAX H3_I2	H4_I1_MAX H4_I2	_	H48_I1_MAX H48_I2	H49_I1_MAX H49_I2 H49_I2_MAX	H50_I1_MAX H50_I2 H50_I2_MAX
H2_I1_MAX H2_I2	H3_I1 H3_I1_MAX H3_I2 H3_I2_MAX	H4_I1_MAX H4_I2 H4_I2_MAX		H48_I1_MAX H48_I2 H48_I2_MAX	H49_I1_MAX H49_I2	H50_I1_MAX H50_I2

B6 Display matrix 4-wire system, balanced load

U_MAX						
U						
U_MIN						
<u>- </u>	IB	1				
I_MAX	IB_MAX					
I_IVIAX	ID_IVIAX					
P						
Ī						
P_MAX						
Q						
Q_MAX						
S						
S_MAX						
				_		
PF	PF	PFG	PFG	1		
PF_MIN_IN_L	PF_MIN_OUT_L	PFG_MIN_IN_L	PFG_MIN_OUT_L	1		
PF_MIN_IN_C	PF_MIN_OUT_C	PFG_MIN_IN_C	PFG_MIN_OUT_C	:		
F_MAX		-	-	_		
F						
F_MIN						
P	Р	Р	Р	Р		
Q	U	Q	s	QG		
S		PF	F	TG		
D	QG		r	1		
D_MAX	QG_MAX					
D_1411 OX	QO_IVII UK					
dd.mm	OPR_CNTR1	OPR_CNTR	7			
hh.mm	OPR_CNTR2	OFK_CNTK				
SS	OPR_CNTR3					
THD_U						
THD_U_MAX						
TDD_I						
TDD_I_MAX						
Block with mear	n-values of power qu	<u>uantities</u>				
H2_U	H3_U	H4_U		H48_U	H49_U	H50_U
H2_U_MAX	H3_U_MAX	H4_U_MAX		H48_U_MAX	H49_U_MAX	H50_U_MAX
H2_I	H3_I	H4_I	₹	H48_I	H49_I	H50_I
H2_I_MAX	H3_I_MAX	H4_I_MAX		H48_I_MAX	H49_I_MAX	H50_I_MAX
1 12_1_IVIAA	I IS_I_IVIAA	I IT_I_IVIAA		I I+O_I_IVIAA	I I49_I_IVIAA	I IOU_I_IVIAA

B7 Display matrix 4-wire systems, unbalanced load

	1		1	1	1	1	1
U1N	U1N_MAX	U1N_MIN	U12	U12_MAX	U12_MIN	UNE	DEV_UMAX
U2N	U2N_MAX	U2N_MIN	U23	U23_MAX	U23_MIN	UNE_MAX	DEV_UMAX_MAX
U3N	U3N_MAX	U3N_MIN	U31	U31_MAX	U31_MIN		
UR1	UNB_UR2_UR1			•	•	•	•
UR2	UNB_UR2_UR1_MAX	•					
UO							
I1	I1_MAX	IB1	IB1_MAX	IN	DEV_IMAX	1	
	_				_		
12	I2_MAX	IB2	IB2_MAX	IN_MAX	DEV_IMAX_MAX		
13	I3_MAX	IB3	IB3_MAX]	
IR1	UNB_IR2_IR1						
IR2	UNB_IR2_IR1_MAX						
10			_				
P1	P1_MAX	Р					
P2	P2_MAX	P_MAX					
P3	P3_MAX	_					
Q1	Q1_MAX	Q					
Q2	Q2_MAX	Q_MAX					
	Q2_MAX Q3_MAX	Q_IVIAX					
Q3			4				
S1	S1_MAX	S					
S2	S2_MAX	S_MAX					
S3	S3_MAX					-	
PF1	PF	PF	PFG1	PFG	PFG		
PF2	PF_MIN_IN_L	PF_MIN_OUT_L	PFG2	PFG_MIN_IN_L	PFG_MIN_OUT_L		
PF3	PF_MIN_IN_C	PF_MIN_OUT_C	PFG3		PFG_MIN_OUT_C		
F_MAX						4	
F							
F_MIN							
	_	L .	L	L	7		
P -	P	P -	P	P			
Q	U_MEAN	Q	S	QG			
S	I_MEAN	PF	F	TG		_	
P1	P2	P3	U1N	U2N	U3N		
Q1	Q2	Q3	l1	12	13		
S1	S2	S3	P1	P2	P3		
D1	D1_MAX	D	QG1	QG1_MAX	QG	1	
	D2_MAX	D_MAX	QG2	QG2_MAX	QG_MAX		
D3		D_IVI/ ()X	QG3	QG3_MAX	QO_IVI/ (X		
	D3_MAX	ODD ONTD	QG3	QG3_IVIAA		1	
dd.mm	OPR_CNTR1	OPR_CNTR					
hh.mm	OPR_CNTR2						
ss	OPR_CNTR3						
THD_U1N	THD_U2N	THD_U3N					
THD_U1N_MAX	THD_U2N_MAX	THD_U3N_MAX					
TDD_I1	TDD_I2	TDD_I3					
TDD_I1_MAX	TDD_I2_MAX	TDD_I3_MAX					
. 55							
511		l	L			•	1
Block with mear	n-values of power qua	antities					
H2_U1N	H3_U1N	H4_U1N		H48_U1N	H49_U1N	H50_U1N	
H2_U1N_MAX	H3_U1N_MAX	H4_U1N_MAX		H48_U1N_MAX	H49_U1N_MAX	H50_U1N_MAX	
		I				I	
H2_U2N	H3 U2N	H4_U2N	1	H48_U2N	H49_U2N	H50_U2N	1
H2_U2N_MAX	_					_	
I IZ_UZIN_IVIAX	H3_U2N_MAX	H4_U2N_MAX		H48_U2N_MAX	H49_U2N_MAX	H50_U2N_MAX	1
L			4			L	4
H2_U3N	H3_U3N	H4_U3N		H48_U3N	H49_U3N	H50_U3N	
H2_U3N_MAX	H3_U3N_MAX	H4_U3N_MAX		H48_U3N_MAX	H49_U3N_MAX	H50_U3N_MAX	
	<u> </u>	<u> </u>			<u> </u>	<u> </u>]
H2_I1	H3_I1	H4_I1		H48_I1	H49_I1	H50_I1	
H2_I1_MAX	H3_I1_MAX	H4_I1_MAX		H48_I1_MAX	H49_I1_MAX	H50_I1_MAX	
	[[[[
⊔a la	⊔o 10	LI4 12	-	LI40 I2	U40 I2	HEO IO	1
	H3_I2	H4_I2	•••	H48_I2	H49_I2	H50_I2	
H2_I2_MAX	H3_I2_MAX	H4_I2_MAX		H48_I2_MAX	H49_I2_MAX	H50_I2_MAX	
			1]
H2_I3	H3_I3	H4_I3		H48_I3	H49_I3	H50_I3	
H2_I3_MAX	H3_I3_MAX	H4_I3_MAX		H48_I3_MAX	H49_I3_MAX	H50_I3_MAX	
l		ĺ					

B8 Display matrix 4-wire system, unbalanced load, Open-Y

		_				•
U1N	U1N_MAX	U1N_MIN	U12	U12_MAX	U12_MIN	
U2N	U2N_MAX	U2N_MIN	U23	U23_MAX	U23_MIN	
U3N	U3N_MAX	U3N_MIN	U31	U31_MAX	U31_MIN	
I 1	I1_MAX	IB1	IB1_MAX	IN	DEV_IMAX	
12	I2_MAX	IB2	IB2_MAX	IN_MAX	DEV_IMAX_MAX	
13	I3_MAX	IB3	IB3_MAX			
IR1	UNB_IR2_IR1					
IR2	UNB_IR2_IR1_MAX					
10						
P1	P1_MAX	Р				
P2	P2_MAX	P_MAX				
P3	P3_MAX					
Q1	Q1_MAX	Q				
Q2	Q2_MAX	Q_MAX				
Q3	Q3_MAX					
S1	S1_MAX	S				
S2	S2_MAX	S_MAX				
S3	S3_MAX				•	1
PF1	PF	PF	PFG1	PFG	PFG	
PF2	PF_MIN_IN_L	PF_MIN_OUT_L	PFG2	PFG_MIN_IN_L	PFG_MIN_OUT_L	
PF3	PF_MIN_IN_C	PF_MIN_OUT_C	PFG3	PFG_MIN_IN_C	PFG_MIN_OUT_C	
F_MAX						
F						
F_MIN		_	1		- 1	
P	Р	P	P	P		
Q	U_MEAN	Q	S	QG		
S	I_MEAN	PF	F	TG		1
P1	P2	P3	U1N	U2N	U3N	
Q1	Q2	Q3	l1 	12	13	
S1	S2	S3	P1	P2	P3	
D1	D1_MAX	D	QG1	QG1_MAX	QG	
D2	D2_MAX	D_MAX	QG2	QG2_MAX	QG_MAX	
D3	D3_MAX	ODD OUTD	QG3	QG3_MAX]
dd.mm	OPR_CNTR1	OPR_CNTR				
hh.mm	OPR_CNTR2					
SS	OPR_CNTR3	TUD HON				
THD_U1N	THD_U2N	THD_U3N				
THD_U1N_MAX	THD_U2N_MAX	THD_U3N_MAX				
TDD_I1	TDD 12	TDD 13				
_	TDD_I2	TDD_I3 TDD_I3_MAX				
TDD_I1_MAX	TDD_I2_MAX	I DD_IS_IVIAX				
	values of power qua		1	T		L
H2_U1N	H3_U1N	H4_U1N		H48_U1N	H49_U1N	H50_U1N
H2_U1N_MAX	H3_U1N_MAX	H4_U1N_MAX		H48_U1N_MAX	H49_U1N_MAX	H50_U1N_MAX
					1140 11011	
		114 11011			H49_U2N	H50_U2N
H2_U2N	H3_U2N	H4_U2N		H48_U2N	_	_
H2_U2N H2_U2N_MAX	H3_U2N H3_U2N_MAX	H4_U2N H4_U2N_MAX		H48_U2N H48_U2N_MAX	H49_U2N_MAX	H50_U2N_MAX
H2_U2N_MAX	H3_U2N_MAX	H4_U2N_MAX		H48_U2N_MAX	H49_U2N_MAX	H50_U2N_MAX
H2_U2N_MAX H2_U3N	H3_U2N_MAX H3_U3N	H4_U2N_MAX H4_U3N		H48_U2N_MAX H48_U3N	H49_U2N_MAX H49_U3N	H50_U2N_MAX H50_U3N
H2_U2N_MAX	H3_U2N_MAX	H4_U2N_MAX		H48_U2N_MAX	H49_U2N_MAX	H50_U2N_MAX
H2_U2N_MAX H2_U3N H2_U3N_MAX	H3_U2N_MAX H3_U3N H3_U3N_MAX	H4_U2N_MAX H4_U3N H4_U3N_MAX		H48_U2N_MAX H48_U3N H48_U3N_MAX	H49_U2N_MAX H49_U3N H49_U3N_MAX	H50_U2N_MAX H50_U3N H50_U3N_MAX
H2_U2N_MAX H2_U3N H2_U3N_MAX H2_U3N_MAX	H3_U2N_MAX H3_U3N H3_U3N_MAX H3_I1	H4_U2N_MAX H4_U3N H4_U3N_MAX H4_I1		H48_U2N_MAX H48_U3N H48_U3N_MAX H48_U1	H49_U2N_MAX H49_U3N H49_U3N_MAX H49_I1	H50_U2N_MAX H50_U3N H50_U3N_MAX H50_I1
H2_U2N_MAX H2_U3N H2_U3N_MAX H2_U3N_MAX	H3_U2N_MAX H3_U3N H3_U3N_MAX	H4_U2N_MAX H4_U3N H4_U3N_MAX		H48_U2N_MAX H48_U3N H48_U3N_MAX	H49_U2N_MAX H49_U3N H49_U3N_MAX	H50_U2N_MAX H50_U3N H50_U3N_MAX
H2_U2N_MAX H2_U3N H2_U3N_MAX H2_I1 H2_I1_MAX	H3_U2N_MAX H3_U3N H3_U3N_MAX H3_I1 H3_I1_MAX	H4_U2N_MAX H4_U3N H4_U3N_MAX H4_I1 H4_I1_MAX		H48_U2N_MAX H48_U3N H48_U3N_MAX H48_I1 H48_I1_MAX	H49_U2N_MAX H49_U3N H49_U3N_MAX H49_I1 H49_I1_MAX	H50_U2N_MAX H50_U3N H50_U3N_MAX H50_U1 H50_I1
H2_U2N_MAX H2_U3N H2_U3N_MAX H2_I1 H2_I1_MAX H2_I2	H3_U2N_MAX H3_U3N H3_U3N_MAX H3_I1 H3_I1_MAX H3_I2	H4_U2N_MAX H4_U3N H4_U3N_MAX H4_I1 H4_I1_MAX H4_I2		H48_U2N_MAX H48_U3N H48_U3N_MAX H48_I1 H48_I1_MAX H48_I2	H49_U2N_MAX H49_U3N H49_U3N_MAX H49_I1 H49_I1_MAX H49_I2	H50_U2N_MAX H50_U3N H50_U3N_MAX H50_I1 H50_I1_MAX H50_I2
H2_U2N_MAX H2_U3N H2_U3N_MAX H2_I1 H2_I1_MAX	H3_U2N_MAX H3_U3N H3_U3N_MAX H3_I1 H3_I1_MAX	H4_U2N_MAX H4_U3N H4_U3N_MAX H4_I1 H4_I1_MAX		H48_U2N_MAX H48_U3N H48_U3N_MAX H48_I1 H48_I1_MAX	H49_U2N_MAX H49_U3N H49_U3N_MAX H49_I1 H49_I1_MAX	H50_U2N_MAX H50_U3N H50_U3N_MAX H50_U1 H50_I1
H2_U2N_MAX H2_U3N H2_U3N_MAX H2_I1 H2_I1_MAX H2_I2 H2_I2_MAX	H3_U2N_MAX H3_U3N H3_U3N_MAX H3_I1 H3_I1_MAX H3_I2 H3_I2_MAX	H4_U2N_MAX H4_U3N H4_U3N_MAX H4_I1 H4_I1_MAX H4_I2 H4_I2_MAX		H48_U2N_MAX H48_U3N H48_U3N_MAX H48_I1 H48_I1_MAX H48_I2 H48_I2_MAX	H49_U2N_MAX H49_U3N H49_U3N_MAX H49_I1 H49_I1_MAX H49_I2 H49_I2_MAX	H50_U2N_MAX H50_U3N H50_U3N_MAX H50_I1 H50_I1_MAX H50_I2 H50_I2_MAX
H2_U2N_MAX H2_U3N H2_U3N_MAX H2_I1 H2_I1_MAX H2_I2 H2_I2_MAX H2_I3	H3_U2N_MAX H3_U3N H3_U3N_MAX H3_I1 H3_I1_MAX H3_I2 H3_I2_MAX H3_I3	H4_U2N_MAX H4_U3N H4_U3N_MAX H4_I1 H4_I1_MAX H4_I2 H4_I2_MAX H4_I3		H48_U2N_MAX H48_U3N H48_U3N_MAX H48_I1 H48_I1_MAX H48_I2 H48_I2_MAX H48_I3	H49_U2N_MAX H49_U3N H49_U3N_MAX H49_I1 H49_I1_MAX H49_I2 H49_I2_MAX H49_I3	H50_U2N_MAX H50_U3N H50_U3N_MAX H50_I1 H50_I1_MAX H50_I2 H50_I2_MAX H50_I3
H2_U2N_MAX H2_U3N H2_U3N_MAX H2_I1 H2_I1_MAX H2_I2 H2_I2_MAX	H3_U2N_MAX H3_U3N H3_U3N_MAX H3_I1 H3_I1_MAX H3_I2 H3_I2_MAX	H4_U2N_MAX H4_U3N H4_U3N_MAX H4_I1 H4_I1_MAX H4_I2 H4_I2_MAX	 	H48_U2N_MAX H48_U3N H48_U3N_MAX H48_I1 H48_I1_MAX H48_I2 H48_I2_MAX	H49_U2N_MAX H49_U3N H49_U3N_MAX H49_I1 H49_I1_MAX H49_I2 H49_I2_MAX	H50_U2N_MAX H50_U3N H50_U3N_MAX H50_l1 H50_l1_MAX H50_l2 H50_l2_MAX

B9 Display matrix of mean-values of power quantities

TREND	MIN / MAX	Present	Present - 1	Present - 2	Present - 3	Present - 4
TR_PIN - -	M_PIN_MAX M_PIN_MIN	M1_PIN	M2_PIN	M3_PIN	M4_PIN	M5_PIN
TR_POUT -	M_POUT_MAX M_POUT_MIN	M1_POUT	M2_POUT	M3_POUT	M4_POUT	M5_POUT
TR_QIN -	M_QIN_MAX M_QIN_MIN	M1_QIN	M2_QIN	M3_QIN	M4_QIN	M5_QIN
TR_QOUT - -	M_QOUT_MAX M_QOUT_MIN	M1_QOUT	M2_QOUT	M3_QOUT	M4_QOUT	M5_QOUT
TR_QIND -	M_QIND_MAX M_QIND_MIN	M1_QIND	M2_QIND	M3_QIND	M4_QIND	M5_QIND
TR_QCAP - -	M_QCAP_MAX M_QCAP_MIN	M1_QCAP	M2_QCAP	M3_QCAP	M4_QCAP	M5_QCAP
TR_S -	M_S_MAX M_S_MIN	M1_S	M2_S	M3_S	M4_S	M5_S

C FCC statement

The following statement applies to the products covered in this manual, unless otherwise specified herein. The statement for other products will appear in the accompanying documentation.

NOTE: This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules and meets all requirements of the Canadian Interference-Causing Equipment Standard ICES-003 for digital apparatus. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/T.V. technician for help.

Camille Bauer AG is not responsible for any radio television interference caused by unauthorized modifications of this equipment or the substitution or attachment of connecting cables and equipment other than those specified by Camille Bauer AG. The correction of interference caused by such unauthorized modification, substitution or attachment will be the responsibility of the user.

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