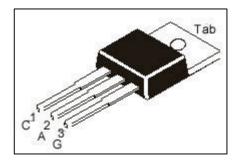


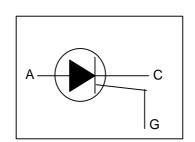
TÜV MANAGEMENT SERVICE



An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company

THYRISTORS BT151





TO-220 Plastic Package

For use in Applications Requiring high Bidirectional Blocking Voltage Capability and high Thermal Cycling Performance. Typical Applications include Motor Control, Industrial and Domestic Lighting, Heating and Static Switching

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITION	VALUE	UNIT	
		BT151-	500	650	
Repetitive Peak Off State Voltage	V_{DRM}, V_{RRM}		*500 *	650	V
Average On State Current	I _{T (AV)}	half sine wave, T _{mb} ≤ 109°C	7.5		Α
RMS On State Current	I _{T (RMS)}	all conduction angles	12		А
Non Repetitive Peak On State Current	I _{TSM}	half sine wave, T _J =25°C prior to surge			
		t=10ms	100		Α
		t=8.3ms	110		А
I ² t for Fusing	l ² t	t=10ms	50		A ² s
Repetitive Rate of Rise of On State Current After Triggering	dl _T /dt	I_{TM} =20A, I_{G} =50mA, dI_{G} /dt=50mA/ μ s	50		A/μs
Peak Gate Current	I _{GM}		2.0		Α
Peak Gate Voltage	V_{GM}		5.0		V
Peak Reverse Gate Voltage	V_{RGM}		5.0		V
Peak Gate Power	P_{GM}		5.0		W
Average Gate Power	P _{G (AV)}	Over any 20ms period	0.5		W
Storage Temperature	T_{stg}		- 40 to +150		°C
Operating Junction Temperature	T _j		125		°C

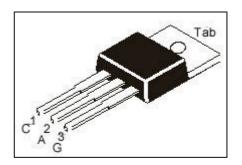
THERMAL RESISTANCE

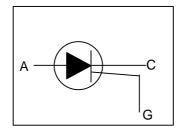
Junction to Mounting Base	R _{th (j-mb)}		1.3 max	K/W	
Junction to Ambient	R _{th (i-a)}	in free air	60 typ	K/W	

^{*}Although not recommended, off state voltage upto 800V may be applied without damage, but the thyristor may switch to the on state. The rate of rise of current should not exceed 15A/ms

BT151Rev020103E

THYRISTORS BT151





TO-220 Plastic Package

ELECTRICAL CHARACTERISTICS (T_J=25°C unless specified otherwise)

PARAMETER	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Gate Trigger Current	I _{GT}	V _D =12V, I _T =0.1A		15	mA
Latching Current	ار	V _D =12V, I _{GT} =0.1A		40	mA
Holding Current	I _H	V _D =12V, I _{GT} =0.1A		20	mA
On State Voltage	V_{T}	I _T =23A		1.75	V
Gate Trigger Voltage	V_{GT}	V _D =12V, I _T =0.1A		1.5	V
		$V_D=V_{DRM}$ (max), $I_T=0.1A,T_J=125$ °C	0.25		V
Off State Leakage Current	I _{D,} I _R	$V_D = V_{DRM}$ (max), $V_R = V_{RRM}$ (max) $T_J = 125$ °C		0.5	mA

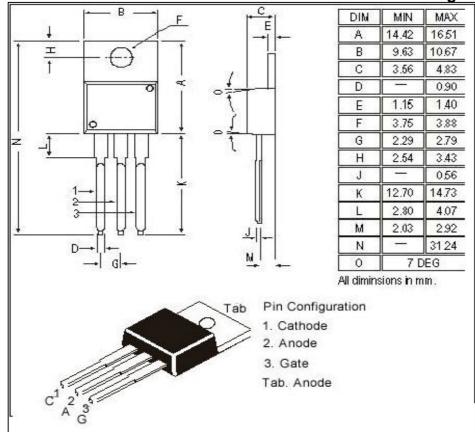
DYNAMIC CHARACTERISTICS

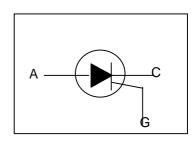
PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Critical Rate of Rise of Off State Voltage	dV _D /dt	V _{DM} =67% V _{DRM} =(max), T _J =125°C, exponential waveform gate open circuit	50			V/μs
		$R_{GK} = 100\Omega$	200			V/μs
Gate Controlled Turn On time	t _{gt}	I_{TM} =40A, V_D = V_{DRM} (max), I_G =0.1A, dI_G / dt =5A/ μ s		2.0		μs
Circuit Commutated Turn Off time	t _q	$\begin{split} &V_{\text{D}}\text{=}67\% \ V_{\text{DRM}}(\text{max}), \\ &T_{\text{J}}\text{=}125^{\circ}\text{C}, \ I_{\text{TM}}\text{=}20\text{A}, \ V_{\text{R}}\text{=}25\text{V}, \\ &\text{d}I_{\text{TM}}/\text{d}t\text{=}30\text{A}/\mu\text{s}, \\ &\text{d}V_{\text{D}}/\text{d}t\text{=}50\text{V}/\mu\text{s}, \ R_{\text{GK}}\text{=}100\Omega \end{split}$		70		μs

Marking	BT151-500	BT151-650	
	CDXX	CDXX	
	BT151	BT151	
	- 500	- 650	
XX=Date Code			

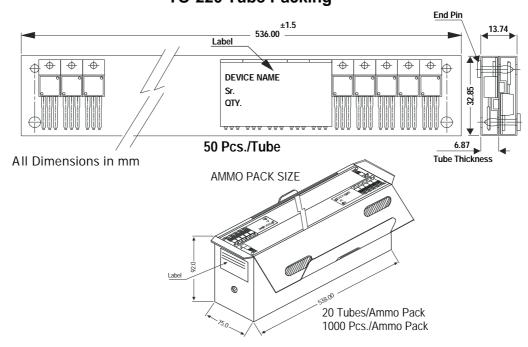
BT151Rev020103E







TO-220 Tube Packing



Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Oty	GrWt
TO-220 /FP	200 pcs/polybag	396 gm/200 pcs	3"×7.5"×7.5"	1.0K	17" x 15" x 13.5"	16.0K	36 kgs
	50 pcs/tube	120 gm/50 pcs	3.5" x 3.7" x 21.5"	1.0K	19" x 19" x 19"	10.0K	29 kgs

Notes BT151

TO-220

Plastic Package

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of
Continental Device India Limited
C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119
email@cdil.com www.cdilsemi.com