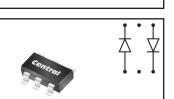
SURFACE MOUNT SILICON
DUAL OPPOSING
HIGH VOLTAGE
SCHOTTKY DIODE



SOT-363 CASE



www.centralsemi.com

## **DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMKD6263DO contains two (2) galvanically isolated, high voltage, low V<sub>F</sub> silicon Schottky diodes with an opposing Anode/ Cathode configuration, epoxy molded in a SOT-363 surface mount package. This device has been designed for fast switching applications requiring a low forward voltage drop.

**MARKING CODE: 63D** 

### **FEATURES:**

- Dual Opposing (DO) Schottky Diodes
- High Voltage (70V)

- Low Forward Voltage
- · Galvanically Isolated

MAXIMUM RATINGS: (T <sub>A</sub> =25°C)	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	$V_{RRM}$	70	V
Continuous Forward Current	l <sub>F</sub>	15	mA
Peak Forward Surge Current, tp=1.0s	I <sub>FSM</sub>	50	mA
Power Dissipation	$P_{D}$	250	mW
Operating and Storage Junction Temperature	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C
Thermal Resistance	$\Theta_{\sf JA}$	500	°C/W

ELECTRICA	ELECTRICAL CHARACTERISTICS PER DIODE: (T <sub>A</sub> =25°C unless otherwise noted)			
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX
$I_{R}$	V <sub>R</sub> =50V		10	200
D\/_	I10A	70		

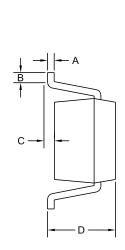
$I_R$	V <sub>R</sub> =50V		10	200	nA
$BV_R$	I <sub>R</sub> =10μA	70			V
$V_{F}$	I <sub>F</sub> =1.0mA		395	410	mV
CJ	V <sub>R</sub> =0, f=1.0MHz			2.0	pF
t <sub>rr</sub>	$I_R = I_F = 10$ mA, $I_{rr} = 1.0$ mA, $R_L = 100$ $\Omega$			5.0	ns

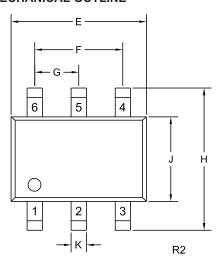
**UNITS** 

# SURFACE MOUNT SILICON DUAL OPPOSING HIGH VOLTAGE SCHOTTKY DIODE

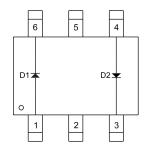


## **SOT-363 CASE - MECHANICAL OUTLINE**





## **PIN CONFIGURATION**



DIMENSIONS				
	INCHES		MILLIMETERS	
SYMBOL	MIN	MAX	MIN	MAX
Α	0.004	0.010	0.10	0.25
В	0.005	-	0.12	-
С	0.000	0.004	0.00	0.10
D	0.031	0.043	0.80	1.10
E	0.071	0.087	1.80	2.20
F	0.051		1.30	
G	0.026		0.65	
Н	0.075	0.091	1.90	2.30
J	0.043	0.055	1.10	1.40
K	0.006	0.012	0.15	0.30
SOT-363 (REV: R2)				

# LEAD CODE:

- 1) Anode D1
- 2) NC
- 3) Cathode D2
- 4) Anode D2
- 5) NC
- 6) Cathode D1

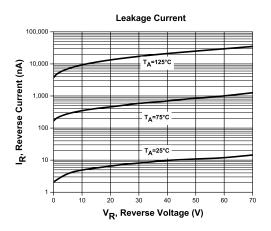
**MARKING CODE: 63D** 

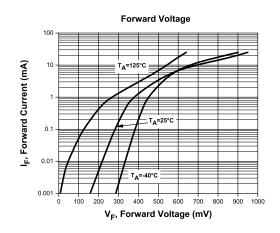
R4 (11-December 2015)

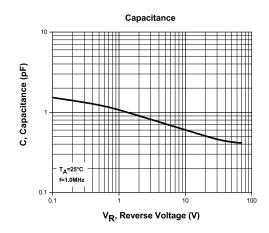
# SURFACE MOUNT SILICON DUAL OPPOSING HIGH VOLTAGE SCHOTTKY DIODE

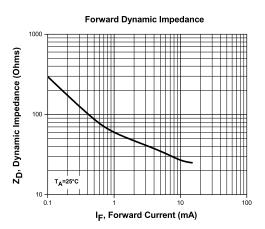


## TYPICAL ELECTRICAL CHARACTERISTICS







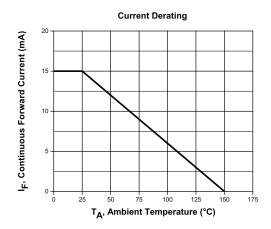


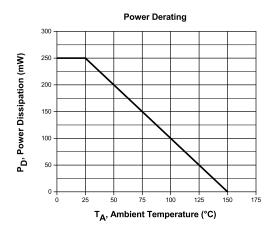
R4 (11-December 2015)

SURFACE MOUNT SILICON DUAL OPPOSING HIGH VOLTAGE SCHOTTKY DIODE



## TYPICAL ELECTRICAL CHARACTERISTICS





## **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**



#### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

#### **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- · Special wafer diffusions
- · PbSn plating options
- Package details
- · Application notes
- · Application and design sample kits
- · Custom product and package development

#### **CONTACT US**

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