

2400-03 FRONT END PROCESSOR

MAINTENANCE & SUPPORT GUIDE



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Cyclone P/N 800-2400-03

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Release History for 2400 Series Front End Processors January 15, 2003

This document contains changes applicable to all 2400 Series FEP Maintenance & Support Guides.

- 1.1 Cyclone Microsystems Inc. has released a new firmware revision, Revision 2.3, to support new revisions of the 2400 FEP host and datalink boards. The new firmware is designated in the ordering information with the suffix “**T3**”. All shipments of 2400 Series FEPs and individual boards ordered as spares as of December 12, 2002, will be loaded with the T3 firmware. Replacement board part numbers and corresponding CLEI codes can be found in **section 5.1**, under Serviceable Components.

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1.1 INTRODUCTION

The 2400-03 Front End Processor is a system featuring two ATM SARs for dual DS1 line interface and two Ethernet ports. The system uses two PowerPC processors. The 2400-03 is available in AC or DC versions and features two redundant, hot swappable removable power supplies. The 2400-03 system also monitors power supply, fan status and temperature in an effort to detect and provide early alert to component failures.

The part numbering system for the 600-2400 series of FEPs is as shown in Figure 1-1.

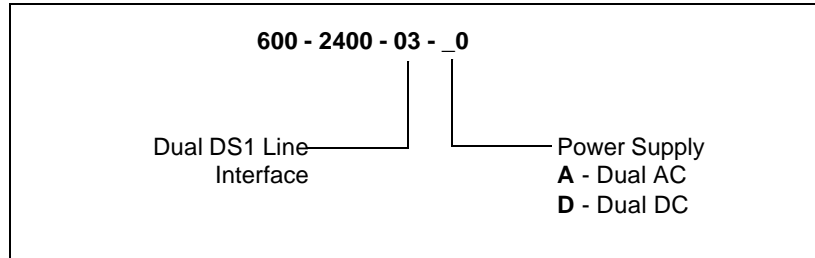


Figure 1-1. FEP Part Numbering

1.2 SPECIFICATIONS

Table 1-1 lists the specifications for the 2400-03 series Front End Processor. Cooling air input is on the left side of the unit and the air exhaust is on the right side. Airflow should not be restricted; a minimum of one inch clearance on each side is required.

Table 1-1. 600-2400-03 Specifications

Physical	Height	5.25 inches
	Width	17.5 inches
	Depth	12 inches
	Other	19 inch rack mountable
	Weight	15 lbs
Electrical - AC version	(600-2400-03-A0)	
	Voltage:	90 - 264 Vac
	Current (Max):	3A
	Power Dissipation:	205 BTU/hr.
	Frequency:	47 - 63 Hz
	Connector:	
	Ethernet ports:	10/100 BaseTx RJ45
	Console Serial ports:	RJ11
	DS1 ports:	RJ45
Electrical - DC version	(600-2400-03-D0)	
	Voltage:	36 - 72 Vdc
	Current (Max):	6.5A
	Power Dissipation:	205 BTU/hr.
	Connector:	
	Ethernet ports:	10/100 BaseTx RJ45
	Console Serial ports:	RJ11
	DS1 ports:	RJ45
Environmental	Operating Temperature	0 to 55 Degrees Celsius
	Relative Humidity	0% to 95% (non-condensing)
	Storage Temperature	-55 to 125 Degrees Celsius
Regulatory Compliance	UL1950	
		<p>CUL C22.2 No. 950, CE Marking, FCC Class A</p> <p><u>Note:</u> This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.</p> <p><u>Caution:</u> Changes or modifications to this equipment not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.</p> <p>This digital apparatus does not exceed the Class A limits for radio noise emissions from digital apparatus set out in the Radio Interference Regulations of the Canadian Department of Communication.</p> <p>Le present appareil numerique n'emmet pas de bruits radioelectriques depassant les limites applicables aux appareils numeriques de las classe A prescrites dans le Reglement sur le brouillage radioelectrique edicte par le ministere des Communications du Canada.</p>

2.1 PHYSICAL CONFIGURATION

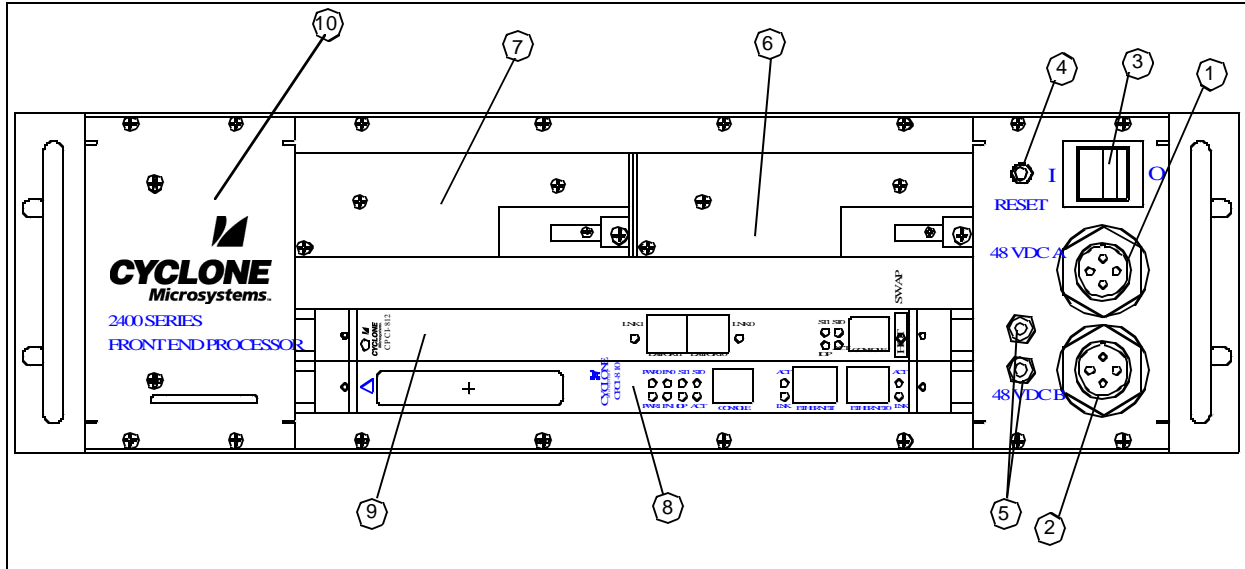


Figure 2-1. 600-2400-03-D0 FEP Front Panel

Figure 2-1 shows a front view of the FEP unit with DC power supplies. Table 2-1 identifies each item.

Table 2-1. 600-2400-03-D0 FEP Front Panel Description

No.	Name
1	Power Connector A
2	Power Connector B
3	FEP Power supply ON/OFF Switch
4	FEP Reset Switch
5	FEP Ground Lugs
6	Power Supply A
7	Power Supply B
8	CPCI-810
9	CPCI-812
10	Fan/Filter Assembly

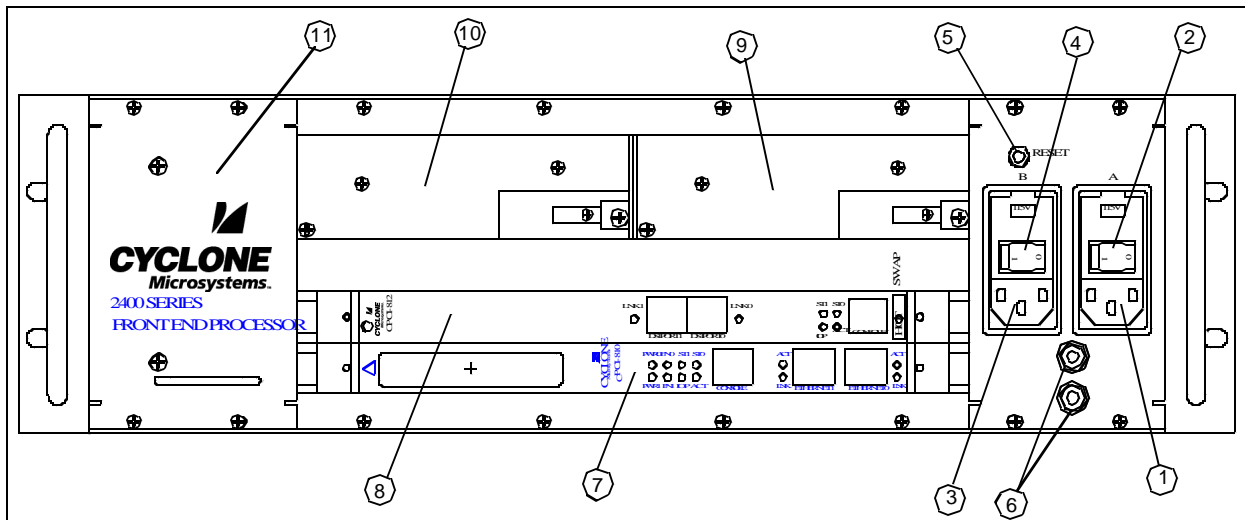


Figure 2-2. 600-2400-03-A0 FEP Front Panel

Figure 2-2 shows a front view of the FEP unit with AC power supplies. Table 2-2 identifies each item.

Table 2-2. 600-2400-03-A0 FEP Front Panel Description

No.	Name
1	Power Connector A
2	FEP Power supply ON/OFF Switch A
3	Power Connector B
4	FEP Power supply ON/OFF Switch B
5	FEP Reset Switch
6	FEP Ground Lugs
7	CPCI-810
8	CPCI-812
9	Power Supply A
10	Power Supply B
11	Fan/Filter Assembly

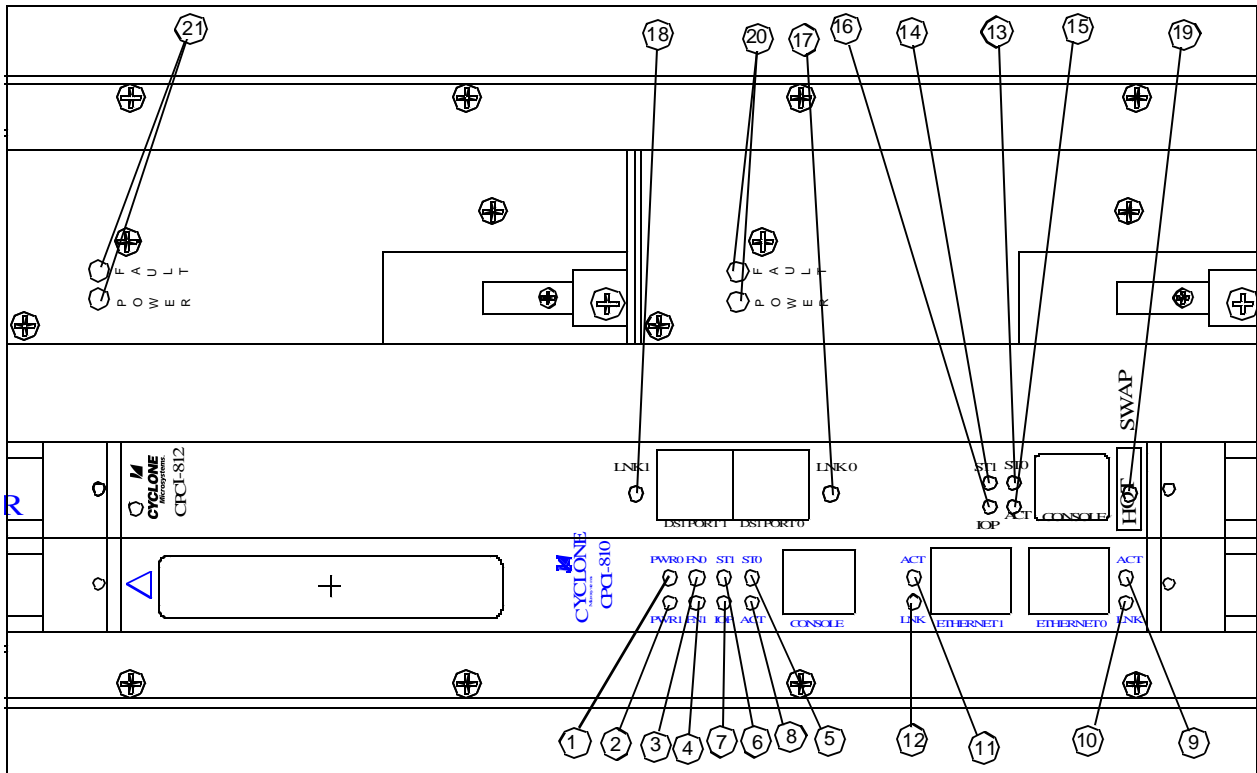


Figure 2-3. 600-2400-03 FEP Front Panel LEDs

Figure 2-3 shows the FEP front panel LEDs. Table 2-3 briefly describes each item.

Table 2-3. 600-2400-03 FEP Front Panel LED Description

No.	Name	Description
1	CPCI-810 PWR0	Turns on and remains on for power good condition of power supply A and turns off for power failure.
2	CPCI-810 PWR1	Turns on and remains on for power good condition of power supply B.
3	CPCI-810 FN0	Turns on and remains on for FAN0 good condition and turns off for FAN0 failure.
4	CPCI-810 FN1	Turns on and remains on for FAN1 good condition and turns off for FAN1 failure.
5	CPCI-810 ST0	Indicates status 0 for CPCI-810 and will light And remain on for some time after a reset or power-on and turn off.
6	CPCI-810 ST1	Indicates status 1 for CPCI-810 and will light and remain on for some time after a reset or power-on and turn off.
7	CPCI-810 IOP	Indicates IOP for CPCI-810 and will light for a short time after a reset or power on and then turn off.

8	CPCI-810 ACT	Indicates CPCI-810 activity and will blink and/or vary in intensity as CPCI-810 performs different operations.
9	CPCI-810 Ethernet 0 ACT	Indicates network activity for port 0 and will blink during transmit and receive activity.
10	CPCI-810 Ethernet 0 LNK	Indicates link integrity for Ethernet port 0 and will remain on continuously if port 0 is connected to a functional network or Ethernet port.
11	CPCI-810 Ethernet 1 ACT	Indicates network activity for port 1 and will blink during transmit and receive activity.
12	CPCI-810 Ethernet 1 LNK	Indicates link integrity for Ethernet port 1 and will remain on continuously if port 1 is connected to a functional network or Ethernet port.
13	CPCI-812 ST0	Indicates status 0 for CPCI-812 and will light for a short time after reset or power-on, then turn off.
14	CPCI-812 ST1	Indicates status 1 for CPCI-812 and will light for a short time after reset or power-on and then turn off.
15	CPCI-812 ACT	Indicates CPCI-812 activity and will remain on for a short time then turn off after reset or power-on.
16	CPCI-812 IOP	Indicates IOP for CPCI-812 and will light for a short time after reset or power-on and then turn off.
17	DS1 Port 0 LNK	Indicates valid DS1 port 0 link.
18	DS1 Port 1 LNK	Indicates valid DS1 port 1 link.
19	Hot Swap	This will illuminate when extraction or insertion of CPCI-812 board takes place when power-on.
20	Power Supply A	The FEP Power Supply green 'POWER' LED is located on the power supply. It illuminates when the power supply is ON. The yellow 'FAULT' LED will light when the power supply is removed or the power supply is faulty, or there is no power supplied to the power entry module.
21	Power Supply B	The FEP Power Supply green 'POWER' LED is located on the power supply. It illuminates when the power supply is ON. The yellow 'FAULT' LED will light when the power supply is removed or the power supply is faulty, or there is no power supplied to the power entry module.

2.2 SWITCHES

2.2.1 FEP Power Supply Switches

The 600-2400-03-D0 DC unit has a single power switch. This switch enables and disables power from both power entries to both power supplies. The '1' position indicates power supply ON and '0' position indicates power supply OFF.

The 600-2400-03-A0 AC unit has two power switches, each switch associated with one of the power entry modules. AC power switch A enables and disables power entry to power supply A and AC power switch B enables and disables power entry to power supply B.

2.2.2 FEP Reset Switch

The FEP Reset Switch is a momentary push-button switch that resets the FEP.

2.3 CONNECTORS

2.3.1 Power Connector

There are two power supply connectors on the right hand side of the FEP.

2.3.1.1 DC Power Connectors (600-2400-03-D0)

The DC power connector is a 4-pin connector. See Figure 2-4.

Mating Connector p/n -

ITT Cannon P/N MS 3476 W14 4S

or

ITT Cannon P/N MS 3475 W14 4Sor

Deutsch P/N MS 3475 W14 4S

The cable clamp and Backshell for Deutsch P/N MS 3475 W14 4S used is -

Deutsch Backshell P/N 94002-14-3014

Deutsch Cable Clamp P/N 9400314-3014

The Backshell for ITT Cannon P/N MS 3475 W14 4S used is -

ITT Cannon Backshell (shell size 14, type S) P/N 057-0685-002

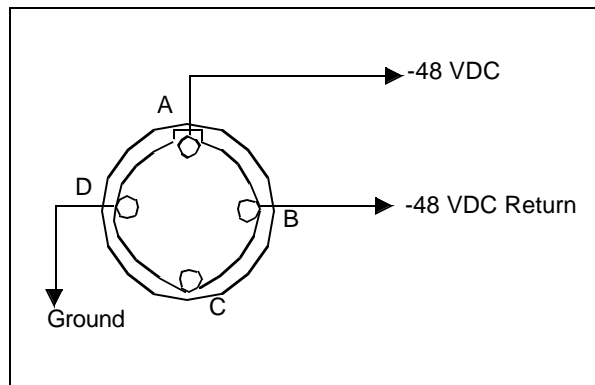


Figure 2-4. DC Power Connector

The color code for wire gauge for 600-2400-01-D0 for DC power mating connector is as follows:

Red	+48VDC
Black	-48VDC Return
Green & White	Ground

2.3.1.2 AC Power Connectors (600-2400-03-A0)

The AC power connector is a 3-pin connector. See Figure 2-5.

The power Connector is a standard IEC type recessed power receptacle.

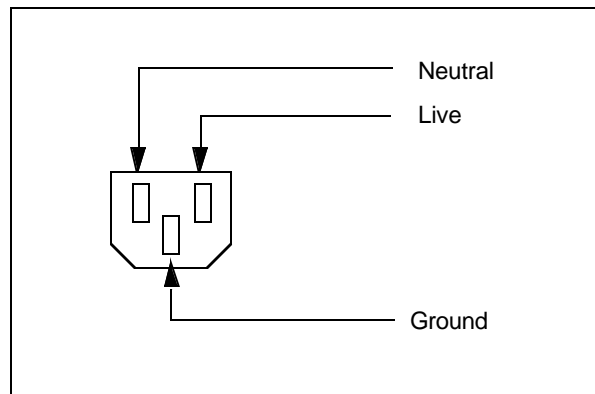


Figure 2-5. AC Power Connector

2.3.1.3 Ground Stud for 600-2400-01-A0 and 600-2400-01-D0

The ground stud used for 600-2400-01-A0 and 600-2400-01-D0 is Hybricon’s # 10, and either # 10 or # 16 awg wire for earth ground can be used. Following are the AMP part numbers:

Table 2-4. Ground Stud

# 10 Stud	# 10-12 awg	amp 2-36161-2
#10 Stud	# 14-16 awg	amp 36161

2.3.2 CPCI-810 Console Connector

The CPCI-810 Console Connector is a narrow RJ11 (6 positions) phone jack. The console port is an RS232 serial port using a simple three-wire connection (transmit data, receive data & ground). The connector assignment is as shown in Table 2-4. Normal system operation does not require a connection to the CPCI-810 console connector.

Diagnostic or debug activity may require connecting to the CPCI-810 console connector. A cable connecting the console port to a standard DB25 connector is available from Cyclone Microsystems (P/N 530-2006). A terminal (or PC running a terminal emulation program) should be set up for any band up to 115K band, 8 bits, no parity and 1 stop bit.

Table 2-5. Console Port Connector

Pin	Signal	Description
1		N/C
2	GND	Ground
3	TXD	Transmit Data
4	RXD	Receive Data
5		N/C
6		N/C

2.3.3 Ethernet Port Connector

The Ethernet Ports Connector 10/100 Base TX is a shielded RJ45 phone jack. The pin assignments are as shown in Table 2-5.

Table 2-6. Ethernet Port Connector

Pin	Signal	Description
1	TX+	Transmit + (output)
2	TX-	Transmit - (output)
3	RX+	Receive + (input)
4	TXC	Transmit center tap
5	-	N/C
6	RX-	Receive - (input)
7	RXC	Receive center tap
8	-	N/C

2.3.4 DS1 Connectors

The CPCI-812 uses two shielded RJ45 connectors for DS1 port 0 and DS1 port 1 line interfaces. Tables 2-6 and 2-7 list the pin connections and the signal description for the same.

Table 2-7. DS1 Port 0 Connector Pin Assignments

Pin	CPCI-812 Signal	Description
1	Rx - R0	Receive Ring 0
2	Rx - T0	Receive Tip 0
4	Tx - R0	Transmit Ring 0
5	Tx - T0	Transmit Tip 0

NOTE: Pin 3, 6, 7, and 8 are not connected.

Table 2-8. DS1 Port 1 Connector Pin Assignments

Pin	CPCI-812 Signal	Description
1	Rx - R1	Receive Ring 1
2	Rx - T1	Receive Tip 1
4	Tx - R1	Transmit Ring 1
5	Tx - T1	Transmit Tip 1

NOTE: Pin 3, 6, 7, and 8 are not connected.

CHAPTER 3

FEP INITIALIZATION & BUILT-IN SELF TEST

3.1 FEP INITIALIZATION AND BUILT-IN SELF TEST

The CPCI-810 and CPCI-812 boards display on the IOP, ACT, ST0 and ST1 LEDs the status of the boards initialization and diagnostic self test.

FEP initialization firmware illuminates various user LED patterns after completing early stages of hardware initialization. Should the board fail to boot properly, the LED pattern will indicate the last successfully completed stage of the board initialization sequence. The following table describes the startup LEDs.

Table 3-1. Initialization and Self Test LEDs

User LED Pattern	Initialization Stage Passed
ACT	Memory Initialized
ST0	Floating Point Unit Enabled
ST1	Floating Point Unit Initialized
IOP	Instruction Cache Initialized
ACT & ST0	Exception Vectors Initialized
ACT & ST1	BSP Clocks Initialized
ACT & IOP	MPC8240 PCI Bridge Initialized
ST0 & ST1	MPC8240 Internal Registers Initialized
ST0 & IOP	PCI Device Initialization Complete
ACT & ST0 & ST1	FE Diagnostics Starting
ACT	All FE Diagnostics Passed
All LEDs OFF	User application downloaded, LEDs cleared.

CHAPTER 4 PERIODIC MAINTENANCE

4.1 PERIODIC MAINTENANCE

The only element of the FEP unit requiring periodic maintenance is the air filter, located on the left hand side of the unit. The filter should be replaced or cleaned every 12 months in normal operating environments. It should be replaced more often in dusty environments.

The replacement of air filter or fan can be done without turning off the power supplies. The fan and air filter assembly is located on the left hand side of the FEP unit. Replacement filters can be purchased from Cyclone Microsystems, Cyclone part number 370-1303.

Table 4-1. Air Filter Replacement

1	Loosen the four screws retaining the Fan & Air Filter access panel.
2	Remove the Fan & Air Filter access panel.
3	Pry out the air filter by pinching the filter and using a medium Phillips screwdriver to pry it outwards. (see Figure 4-1)
4	Insert the new filter and align it into the panel using a medium Phillips screwdriver.
5	Install the Fan & Air Filter access panel.
6	Snug the four thumbscrews using a Phillips screwdriver. DO NOT OVERTIGHTEN, AS THIS WILL CRUSH THE GASKETING MATERIAL AROUND THE ACCESS PANEL.

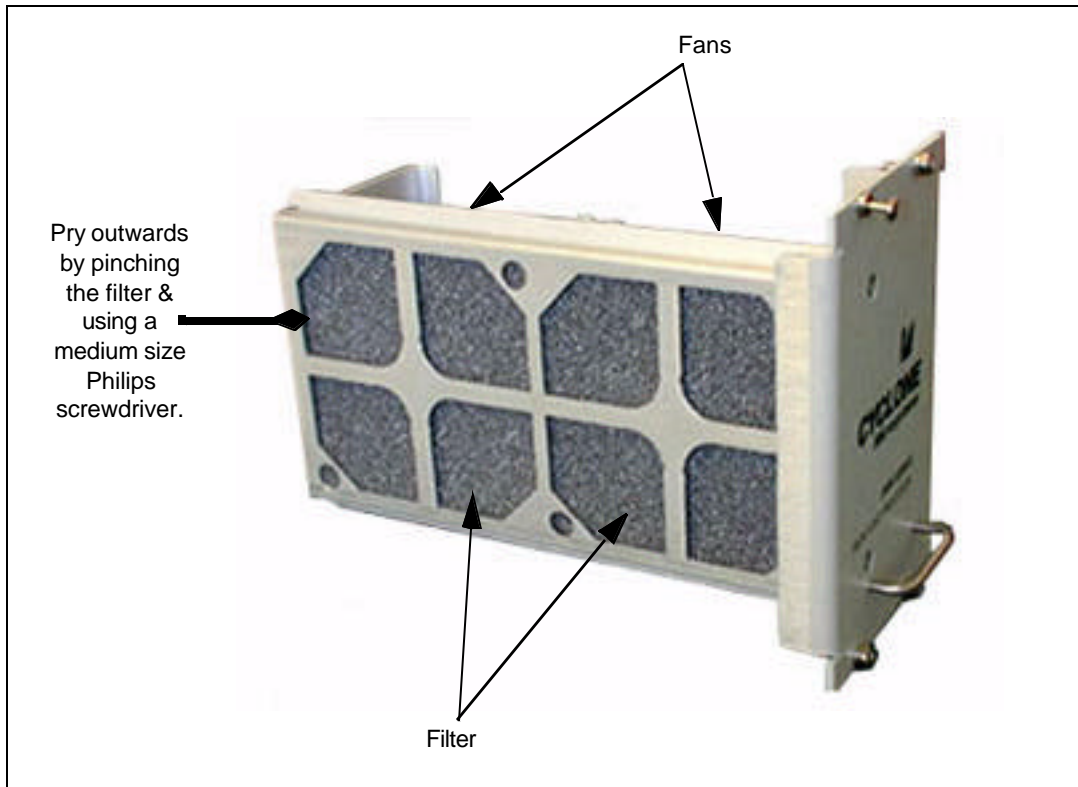


Figure 4-1. Air Filter Replacement

CHAPTER 5 SERVICEABLE COMPONENTS

5.1 SERVICEABLE COMPONENTS

All of the Field Replaceable Units (FRU) of an FEP are serviceable from the front of the unit without the need to dismount an installed FEP from its rack. Table 5-1 lists all the field replaceable units and the associated CLEI codes and Cyclone part numbers.

Table 5-1. CLEI Number and Barcode

Cyclone Part Number	Qty	Description	CLEI Number	ECI Code Barcode
600-2400-03-A0				
600-2011	1	5 slot CPCI-AC: 2400 Chassis	GCM2DC0CRA	436787
CM-810-064-00-T3	1	CPCI-810 Host Processor Board	GCP8UTMFAA	113135
CM-812-064-T3	1	Data Link Board: 2 Port ATM-T1	GCP7W2WJAA	113139
370-1302	1	AC Power supply: 2400 Chassis	GCPUHT0JAA	272077
370-1300	1	Fan / Filter Assembly: 2400 Chassis	GCNYAANFAA	436784
370-1303		Filter (only): 2400 Chassis		
600-2400-03-D0				
600-2010	1	5 slot CPCI-DC: 2400 Chassis	GCM2ED0CRA	436788
CM-810-064-00-T3	1	CPCI-810 Host Processor Board	GCP8UTMFAA	113135
CM-812-064-T3	1	Data Link Board: 2 Port ATM-T1	GCP7W2WJAA	113139
370-1301	1	DC Power supply: 2400 Chassis	GCPUCS0JAA	272075
370-1300	1	Fan / Filter Assembly: 2400 Chassis	GCNYAANFAA	436784
370-1303		Filter (only): 2400 Chassis		

5.2 BOARD REMOVAL AND INSTALLATION

The CPCI-810 is not hot-swappable and should only be removed or installed with power OFF. All cables should be disconnected from the boards during installation and removal. CPCI 812 board is hot swappable and can be removed from the FEP when the power is ON.

5.2.1 Board Removal

FEP Boards can be damaged with electrostatic discharge. Consequently, all boards should be handled in an environment with preventive Electro Static Discharge (ESD) measures. Specifically, boards should only be actively handled with a ground strap and transported in an ESD shielded bag.

Turn power off. Disconnect cables from the board. Loosen the two retaining screws on the boards front panel. The retaining screws are located on each edge of the front panel, between the ejector handles and the end of the front panel. Eject the board by prying outward on both the ejector handles.

5.2.2 Board Installation

Insert the board into the FEP unit opening and align the printed circuit board card edge with the card guides inside the unit. Care should be taken to insert the board horizontally (as flat as possible) so as not to interfere with components on the board below or interfere with the underside of the board above. Fully seat the board by pressing firmly on both ejector handles or pressing at the center of the board's front panel.

5.3 FAN TRAY REMOVAL AND INSTALLATION

The cooling fans of the FEP are serviceable from the front of the unit. The FEP does NOT have to be removed from its rack to service the fans.

5.3.1 Fan / Filter Access Panel Removal

Follow steps 1 through 3 in Table 4-1 to remove the Fan/Filter access panel. The Fan & Filter tray is located on the left side of the unit as shown in Figure 4-1. The oval opening in the fan tray panel is a finger pull. Pull the fan tray from the unit.

5.3.2 Fan/Filter Access Panel Installation

Align the flanges in the Fan /Filter access panel with its guides after replacing the fan and slide the Fan/Filter access panel into the unit.

5.4 POWER SUPPLY REMOVAL AND INSTALLATION

The AC power supply has 2 switches for 2 power supplies and the DC power supply has a single switch for 2 power supplies. The power supplies of the FEP are serviceable from the front of the unit. The FEP does NOT have to be removed from its rack to service the power supplies. The FEP will operate with one power supply installed. Since the power supplies are hot swappable, they can be removed or installed without switching off the power.

5.4.1 Power Supply Removal

The two power supplies of the FEP are located above the boards, at the top of the unit. To remove failed power supply, loosen the two retaining screws, at either end of the power supply. Eject the power supply from the FEP by prying outward on the ejector handle. Pull the power supply from the unit.

5.4.2 Power Supply Installation

With the printed circuit board of the power supply aligned with the card guides of the FEP unit, slide the power supply into the FEP. Fully seat the power supply and tighten the retaining screws.

CHAPTER 6 RELIABILITY PREDICTIONS

6.1 METHODOLOGY

Reliability predictions were completed using the Bellcore 5 reliability standard developed by AT&T Bell Laboratories. Component failure rates are calculated based on technology, stress levels, gate or transistor density (ICs), package style, and quality level. Failure rates provided by the component manufacturer have been used in some cases, where available. Failure rates are expressed in FITs, which is failures per billion hours. Mean Time Between Failures (MTBF) is the inverse of the failure rate and is the average time between failures.

6.2 ASSUMPTIONS

- Ambient input air temperature does not exceed 30 deg. C.
- Ground, Fixed, Controlled Environment
- Components are Quality Level I.
- Component stress levels do not exceed rated limits (i.e. power dissipation, junction temperature, etc.). In other words, components are used within specification limits. The parts count prediction assumes parts are used at a maximum of 50% of rated electrical stress.
- Component failure could result in FEP failure (except for the power supplies that are configured in parallel redundancy). However, Bellcore's architecture uses FEPs in parallel redundancy.

6.3 CONFIGURATION

Item	Part Number	Quantity	Failure Rate (FIT)	MTBF (hrs.)
	600-2400-03-D0	1	4,935.845326	202,600
CPCI-810	CM810-064-00-T3	1	1,140.091729	877,122
CPCI-812	CM812-064-T3	1	1,514.676066	660,207
DC Power Supply: 2400 Chassis	370-1301	1	2,331.389001	428,929
Fan/Filter Assembly: 2400 Chassis	370-1300	1	100.000000	
	600-2400-03-A0	1	5,086.156797	196,612
CPCI-810	CM810-064-00-T3	1	1,140.091729	877,122
CPCI-812	CM812-064-T3	1	1,514.676066	660,207
AC Power Supply: 2400 Chassis	370-1302	1	2,181.077531	458,489
Fan/Filter Assembly: 2400 Chassis	370-1300	1	100.000000	

6.4 RESULTS

ITEM	MTBF at 30 Deg. C.	Failure Rate
600-2400-03-D0	196,612 hrs.	5,086.156797
600-2400-03-A0	202,600 hrs.	4,935.845326

7.1 OVERVIEW

Cyclone Microsystems is a commercial manufacturer of Single Board Computers, Intelligent Communication Controllers and communications systems. Our standard repair cycle for in-warranty or out-of-warranty repair is two weeks. Most of our FEP customers require 24 by 7 support that is far in excess of Cyclone Microsystems' current or anticipated capabilities. Consequently, we highly recommend that FEP customers pursue high availability support from a support organization or pursue an on-site sparing policy in conjunction with a Cyclone Microsystems Expedited Repair Program. Please contact a Cyclone sales representative for a program proposal.

7.2 HARDWARE

Cyclone Microsystems, Inc. (Cyclone) for the period set out below, warrants that its standard products will be free from defects in workmanship or material under normal use and service. Cyclone's obligation under this warranty shall not arise until the Buyer returns the defective product, freight prepaid, to Cyclone. The only responsibilities of Cyclone under this warranty are at its option to replace or repair, without charge, any defective component of such products.

7.3 SOFTWARE AND FIRMWARE

Cyclone warrants that Software and Firmware supplied shall conform to the then current published documentation applicable to such programs. Cyclone, for the effective period of the warranty set out below, will upon written notice from the Buyer documenting the symptoms or the defect, expend its best efforts to resolve software bugs and/or fault. This service shall be without extra charge, and at Cyclone's option may include on-site visit(s) if in its opinion the conditions justify such visit(s).

7.4 EFFECTIVE PERIOD OF WARRANTY

One year from date of delivery.

7.5 REPAIRED OR REPLACEMENT PRODUCT (OUT-OF-WARRANTY)

Cyclone Microsystems, for a period of 30 days, warrants that its out-of-warranty products that are repaired or replaced shall be free from defects in workmanship or material under normal use and service.

Any repair or replacement shall not extend the period within which the warranty can be asserted.

The above warranties do not extend to and shall not apply to:

- Products which have been repaired or altered by other than Cyclone, unless the Buyer has properly altered or repaired the products in accordance with procedures previously approved, in writing, by Cyclone; or
- Products which have been subject to misuse, neglect, accident or improper installation; or

WARRANTY



- Products not manufactured by Cyclone.

The foregoing warranty and remedies are exclusive and are made in lieu of all other warranties express or implied, either in fact or by operation of law, statutory or otherwise, including warranties of merchantability and fitness for use. Cyclone neither assumes nor authorizes any other person to assume for it any other liability in connection with the sale, installation or use of its products, and Cyclone makes no warranty whatsoever for products not manufactured by Cyclone or with respect to any non-standard products which have been subject to misuse, neglect, accident, or have been modified by the Buyer. Cyclone shall have no liability for incidental or consequential damages of any kind arising out of the sale, installation, or use of its products.

7.6 SERVICE POLICY

Out of repair warranty will be accomplished expeditiously at a charge published on the current price schedule plus shipping. A full description of the failure must be enclosed with the product.

Shipments arriving at Cyclone without a Return Material Authorization (RMA) number will not be accepted and will be returned to the customer at his cost regardless of warranty status.

Return Procedures

Upon determining that repair is required, the customer must:

- Call Cyclone Customer Support at (203) 786-5536 for a RMA number. Please have ready:
 - The serial number of the board (s)
 - The reason for return
- Enclose a detailed description of the failure with the failed unit in a static-shielded protective container.
- Ship unit to: Cyclone Microsystems, 25 Science Park, New Haven, CT 06511, Attn: RMA number
- The RMA is valid for 30 days after issue.



APPENDIX A MANUALS

A.1 ADDITIONAL INFORMATION

Additional information for each of the boards used in the FEP can be found in their respective User's Manual. These manuals are available from Cyclone Microsystems.

Part Number	Description
800-0810	CPCI-810 User's Manual
800-0812	CPCI-812 User's Manual