

# Test Floor Management Software

# Raise the bar for overall test effectiveness (OTE).

- See the variables that are impacting your test operation.
- Make informed corrective action decisions using customizable analytical tools.
- Connect equipment and test floor management tools to server.
- Link maps and equipment events to generate reports.
- Continuously optimize throughput, yield, overall equipment effectiveness.
- Customize your applications no programming experience necessary.

# Control and share your test operations locally and globally.

- Web-based "Best-in-Class" SPC graphical reporting engine to visualize, analyze, and control your test operations.
- Enable enterprise-wide access to data and reports.
- Enhance collaboration by all team members.
- Harness your company's most valuable asset – intellectual property.
- Connect your process and products experts around the world.

#### Fits seamlessly into your existing infrastructure.

- Flexibility to integrate with and enhance your existing test process and software solutions.
- Consolidate information from a wide range of database and file types into a single application.

## Expand your capabilities with SORTware

 Suite of value-added, higher-level test floor management applications and application modules.

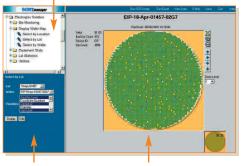
# **SORT**manager

•••• Electroglas' Test Floor Management Software provides unique, networked solutions that connect test equipment to the broader testing infrastructure. It integrates smoothly with existing test processes and software applications enabling semiconductor manufacturers to better manage overall test effectiveness by monitoring, analyzing, and improving important processes locally and globally.

#### **Automated Map Management**

- High-performance automated map repository system for verifying, analyzing, moving, converting, and managing maps.
- Map Manager provides the back-end map management and data integrity for automated, off-line inking, inkless, and map delivery to and from other systems.
- Semi E142 compliant

#### Navigation Frame

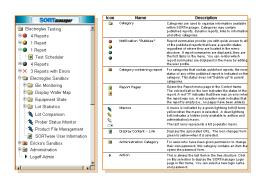


# Control Frame

Result Frame

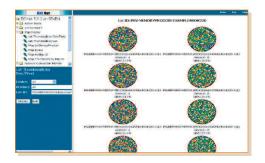
#### **Web browser-based Interface**

- Enables full and easy access to test operation globally
- Internet and LAN / WAN connections
- Login/password protection and Internet security protocols
- Users can launch various applications through a Web browser to easily analyze issues on the test floor



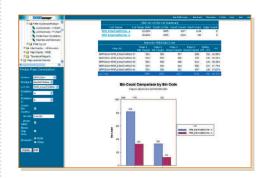
## **Executive Dashboard**

 Canned reports to create multiple web pages with automatic table of contents



#### **Map Analyzer**

- Analyzes maps from multiple test passes with multiple maps displays and composites
- Speeds the identification of yield shortfalls
- · Lot comparator and summary analysis reports



## **Lot Summary**

- Detailed yield reports with bin results, charts and graphs out-of-control points
- Clickable data points open maps and key underlying data



#### **Product File Management (PFM)**

- Central repository for recipes
- Ensures consistency and accuracy in manufacturing operation
- Historical revision control with comment display
- Product file changes report

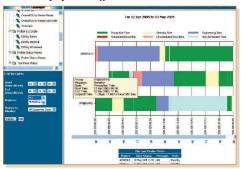
## Statistical Bin Monitoring and Statistical Bin Limits



## **Map Level Bin Monitor (MLBM)**

- Statistical Process Control
- Allows process experts to implement process rules
- Triggers built-in messaging to notify process owner when rule is violated.
- Reduces test processed-induced yield loss and retest throughput loss
- Raises confidence in first pass test results

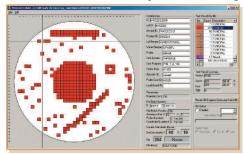
# Overall Equipment Effectiveness



# **Overall Equipment Effectiveness (OEE)**

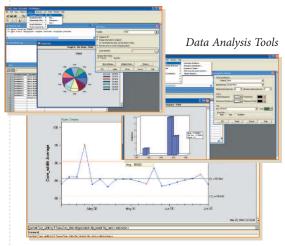
- Quickly detect and correct problems that impact productivity and profitability with real-time test floor status viewer
- Semi E10 reports & graphs
- Records and measure test floor equipment reliability and availability
- Optimizes processes, throughput and utilization

## Map Viewer & Editing



#### **Map Editor**

- Die, map, and lot-level viewing and editing capabilities
- Easily integrated with other TFMS applications



## **EG** Desktop

- Desktop analysis station
- Complete, robust SPC and graphical reporting engine
- Scripting language for easy custom report generation

# **Process Step Verification (PSV)**

- Ensures wafers and lots will not be tested or processed until they have completed the previous step in the process
- Designed to be flexible such that it can be adapted to accommodate virtually
- Based on the use of maps at each process step
- Maps are generated at the first test or visual inspection step
- Subsequent process steps utilize the updated result map to process only good die
- If a process step is missed the equipment and process owner are notified
- Subsequent process step will not follow or process a map that does not have the correct information

# electroglas

# ELECTROGLAS, INNOVATIVE TOOLS FOR TEST

#### Electroglas is Focused on Advancing Innovative Technologies to Meet Evolving Challenges in Semiconductor Test.

Test is all about ensuring device quality and manufacturing performance. In the high-volume manufacturing environment of our customers, our innovative products provide substantial value and help lower the overall cost of test.

Electroglas delivers high-speed tools for wafer probing and package test that are reliable, accurate and production proven. Today, we are focused on overcoming our customers' evolving test challenges, partnering with them to develop solid solutions for wafer probing, prober-based test handling, and test management that will drive greater efficiencies in their wafer and device testing processes. Our customers have rapid, direct access to our worldwide team of experts for service and advice.

## Wafer Probers for Any Test Environment; With Shipments of Over 15,000 Systems

Electroglas' probers have been meeting a variety of probing needs for more than 40 years. These automated systems consistently deliver accurate, reliable wafer probing for high volume, low cost manufacturing, as well as leading edge, multi-die, bumped wafer, in-line parametric test and fine-pitch probing applications.

# Prober-Based Test Handlers for Today's Latest Packaging Technologies

Electroglas' test handlers are built upon proven prober technology to give chip-makers a fast, flexible handling solution for today's final test challenges. Strip test handlers deliver unprecedented throughput for testing a wide variety of popular package types in panel or leadframe format. Filmframe handlers have unique capabilities for testing Wafer Level Packages (WLP), Known Good Die (KGD) on diced wafers, Microelectro-mechanical Systems (MEMS), and ultra-thin wafers.

# Test Floor Management Software for Web-Based Process Analysis and Control

Electroglas test floor management software provides a unique, networked solution to connect wafer probers and test handlers to the broader testing infrastructure, allowing the chipmaker to better manage overall test effectiveness with accurate and efficient tools for monitoring, analyzing and improving important processes.

# All Products Backed by Global Service for Fastest Response

Electroglas' customer service centers are located worldwide for rapid-response field service and local spare parts support. Electroglas demonstrates its commitment to total customer satisfaction through service excellence backed by factory-based technical support, applications development and training.