

Technology

1. Patent list & Certification.

1. Patent list (PL_01)

No.	Patent item	Patent number	Remark
1	A PCB working panel structure	ZL 2009 2 0193316.1	Certified
2	A lay-up structure	ZL 2009 2 0193314.2	Certified
3	A structure of preventing burs for half side electroplated PCB	ZL 2009 2 0193303.4	Certified
4	An asymmetric lay-up structure of PCB	ZL 2009 2 0193311.9	Certified
5	A Lay-up structure of PCB	ZL 2009 2 0062621.7	Certified
6	A resisting resin flow structure of inner layer PCB	ZL 2009 2 0193313.8	Certified
7	A working panel structure of PCB	ZL 2009 2 0193315.7	Certified
8	A working panel structure of VGA card PCB	ZL 2010 2 0027208.X	Certified
9	A stamp holes structure of PCB	ZL 2010 2 0027210.7	Certified
10	A new orientation structure of PCB	ZL 2009 2 0193302.X	Certified
11	A working panel of PCB	ZL 2009 2 0062622.1	Certified
12	A PCB with different solder mask color	ZL 2009 2 0193312.3	Certified
13	An impedance module structure	ZL 2010 2 0027209.4	Certified

2. Patent Certification (PL_02)

A PCB working panel structure



A lay-up structure



2. Patent Certification (PL_03)

A structure of preventing burrs for half side electroplated PCB



An asymmetric lay-up structure of PCB



2. Patent Certification (PL_04)

A Lay-up structure of PCB

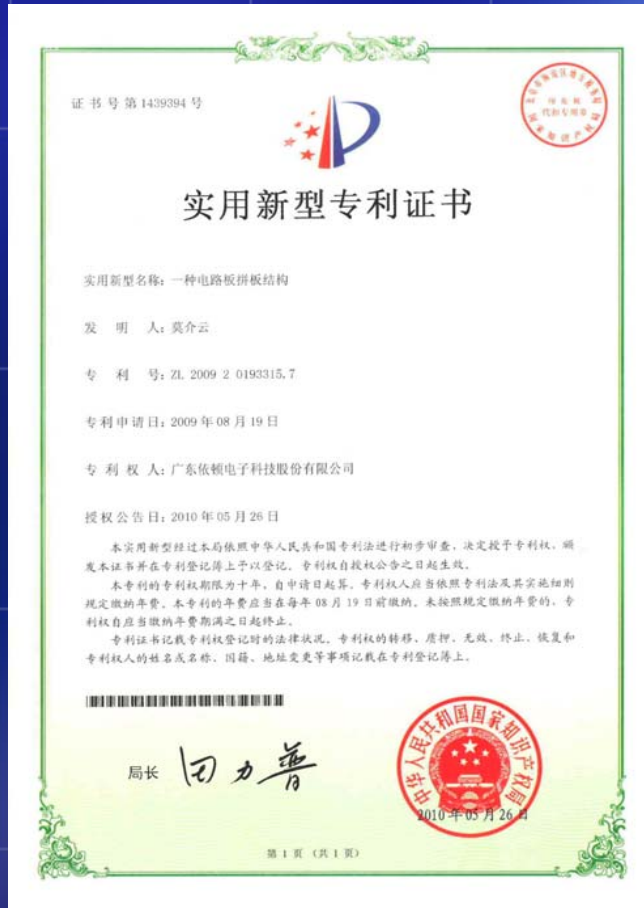


A resisting resin flow structure of inner layer PCB



2. Patent Certification (PL_05)

A working panel structure of PCB



A working panel structure of VGA card PCB



2. Patent Certification (PL_06)

A stamp holes structure of PCB



A new orientation structure of PCB

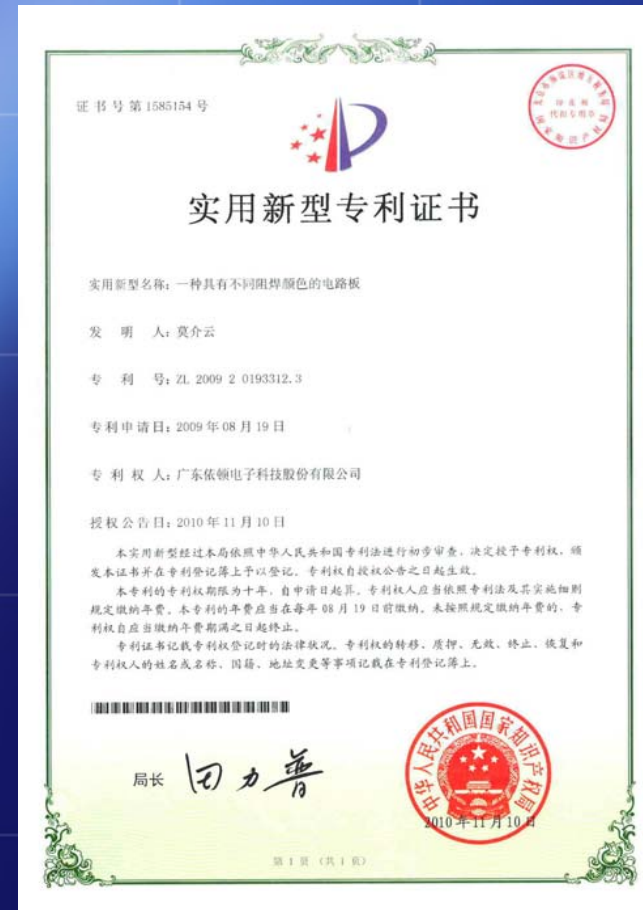


2. Patent Certification (PL_07)

A working panel of PCB



A PCB with different solder mask color



2. Patent Certification (PL_08)

An impedance module structure

