

General Standards Corporation
High Performance Bus Interface Solutions

PNL-LVDS CLOCK DRIVER-32



FEATURES:

- ◆ 32 Output Channels (2 groups of 16 outputs)
- ◆ Hardware solution for Multi-board Synchronization
- ◆ LVDS Clock or Sync inputs
- ◆ +5VDC required externally
- ◆ Board designed for 19" Rack Panel(1U)¹

¹Rack panel hardware sold separately.

General Standards Corporation

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Overview:

The 32-channel PNL-LVDS Clock Driver Board provides a means of distributing LVDS signals among multiple boards. Optimized for flexibility and performance, the board is ideal for multi-board synchronization.

Functional Description:

The board provides two channel groups, A or B, consisting of sixteen LVDS output channels per group.

System input/output connections are made at the front panel through a high-density DB9 I/O connector. Power requirements consist of +5 VDC.

ELECTRICAL SPECIFICATIONS

Power Requirements:

+5.0 VDC \pm 0.25 VDC at 1.0 Watt typical, 2.2 Watts maximum.

Mechanical Characteristics:

(HxWxD): 1.75 in x 16.50 in x 0.50 in

Environmental Specifications:

Ambient Temperature Range: Operating: Standard: -40 to +85 degrees Celsius*
Storage: -65 to +150 degrees Celsius
* Air temperature at board surface

Relative Humidity: Operating: 0 to 80%, non-condensing
Storage: 0 to 95%, non-condensing

Altitude: Operation to 10,000 ft.

Cooling: Conventional convection cooling; 150 LFPM

Ordering Information:

Basic Model Number	Form Factor
PNL-LVDS Clock Driver-32	19" rack panel (1U)

Contact factory for availability in form factors.

Board designed for 19" Rack Panel(1U) - Rack panel hardware sold separately.

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SYSTEM I/O CONNECTIONS

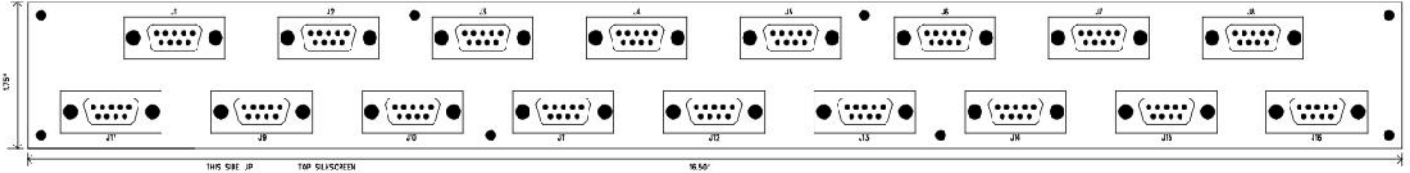
Table 1. System Input/Output Connector

J17 (Power)		J1 (Master)		J2 (Slave)		J3-J16 (Slave)	
PIN	SIGNAL	PIN	SIGNAL	PIN	SIGNAL	PIN	SIGNAL
1	+5V	1	DGND	1	DGND	1	DGND
2	+5V	2	OUT_B CHAN 00 HI	2	OUT_B CHAN 01 HI	2	OUT_B CHAN xx HI
3	N/C	3	OUT_B CHAN 00 LO	3	OUT_B CHAN 01 LO	3	OUT_B CHAN xx LO
4	N/C	4	OUT_A CHAN 00 HI	4	OUT_A CHAN 01 HI	4	OUT_A CHAN xx HI
5	N/C	5	OUT_A CHAN 00 LO	5	OUT_A CHAN 01 LO	5	OUT_A CHAN xx LO
6	N/C	6	LVDS_INP_B_HI	6	N/C	6	N/C
7	N/C	7	LVDS_INP_B_LO	7	N/C	7	N/C
8	GND	8	LVDS_INP_A_HI	8	N/C	8	N/C
9	GND	9	LVDS_INP_A_LO	9	N/C	9	N/C

Board Connectors:

J17 - DB9 Male, **FCI D09P24A4GV00LF** or equivalent.

J1-J16 - DB9 Female, **FCI D09S24A4GV00LF** or equivalent.



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