High Temperature Silicon Carbide Power Schottky Diode

V_{RRM}	=	1200 V
I_F @ 25 °C	=	8 A
Q_C	=	17 nC

Features

- 1200 V Schottky rectifier
- 210 °C maximum operating temperature
- Zero reverse recovery charge
- Superior surge current capability
- Positive temperature coefficient of V_F
- Temperature independent switching behavior
- Lowest figure of merit Q_C/I_F
- Available screened to Mil-PRF-19500

Die Size = 1.6 mm x 1.6 mm

Advantages

- High temperature operation
- Improved circuit efficiency (Lower overall cost)
- Low switching losses
- Ease of paralleling devices without thermal runaway
- Smaller heat sink requirements
- Industry's lowest reverse recovery charge
- Industry's lowest device capacitance
- Ideal for output switching of power supplies
- Best in class reverse leakage current at operating temperature

Applications

- Down Hole Oil Drilling
- Geothermal Instrumentation
- Solenoid Actuators
- General Purpose High-Temperature Switching
- Amplifiers
- Solar Inverters
- Switched-Mode Power Supply (SMPS)
- Power Factor Correction (PFC)

Maximum Ratings at T_j = 210 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	V_{RRM}		1200	V
Continuous forward current	I_F	T_C = 25 °C, R_{thJC} = 3.4	8	A
Continuous forward current	I_F	T_C ≤ 190 °C, R_{thJC} = 3.4	2.5	A
RMS forward current	$I_{F(RMS)}$	T_C ≤ 190 °C, R_{thJC} = 3.4	4.3	A
Surge non-repetitive forward current, Half Sine Wave	$I_{F,SM}$	T_C = 25 °C, t_P = 10 ms	30	A
Non-repetitive peak forward current	$I_{F,max}$	T_C = 25 °C, t_P = 10 µs	120	A
I^2t value	$\int i^2\,dt$	T_C = 25 °C, t_P = 10 ms	5	A^2S
Power dissipation	P_{tot}	T_C = 25 °C, R_{thJC} = 3.4	66	W
Operating and storage temperature	T_j , T_{stg}		-55 to 210	°C

Electrical Characteristics at T_j = 210 °C, unless otherwise specified

Parameter	Symbol	Conditions		Values min.	Values typ.	Values max.	Unit
Diode forward voltage	V_F	I_F = 2.5 A, T_j = 25 °C I_F = 2.5 A, T_j = 210 °C			1.6 2.8		V
Reverse current	I_R	V_R = 1200 V, T_j = 25 °C V_R = 1200 V, T_j = 210 °C			1 25	10 200	µA
Total capacitive charge	Q_C	$I_F \le I_{F,MAX}$ dI_F/dt = 200 A/µs T_j = 210 °C	V_R = 400 V V_R = 960 V		17 29		nC
Switching time	t_s		V_R = 400 V V_R = 960 V		< 25		ns
Total capacitance	C	V_R = 1 V, f = 1 MHz, T_j = 25 °C V_R = 400 V, f = 1 MHz, T_j = 25 °C V_R = 1000 V, f = 1 MHz, T_j = 25 °C			237 25 20		pF

Figures:

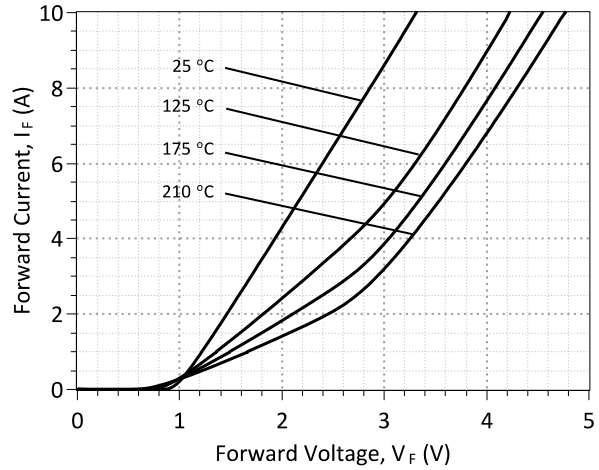


Figure 1: Typical Forward Characteristics

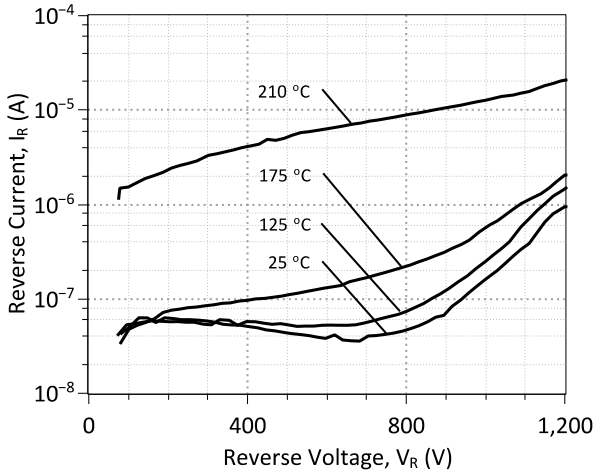


Figure 2: Typical Reverse Characteristics

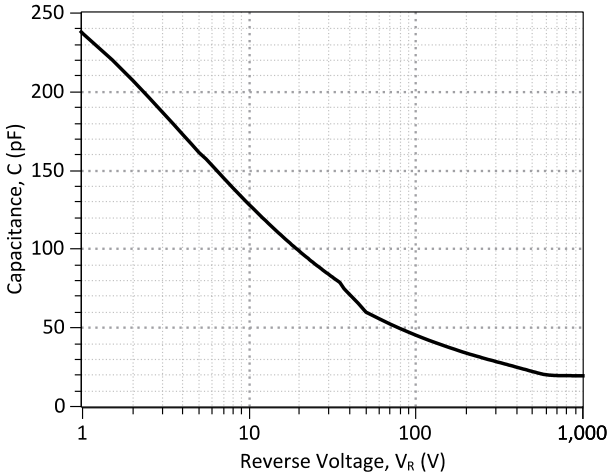


Figure 3: Typical Junction Capacitance vs Reverse Voltage Characteristics

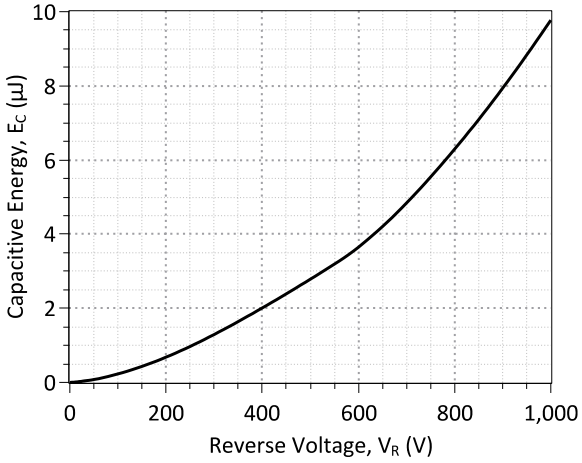
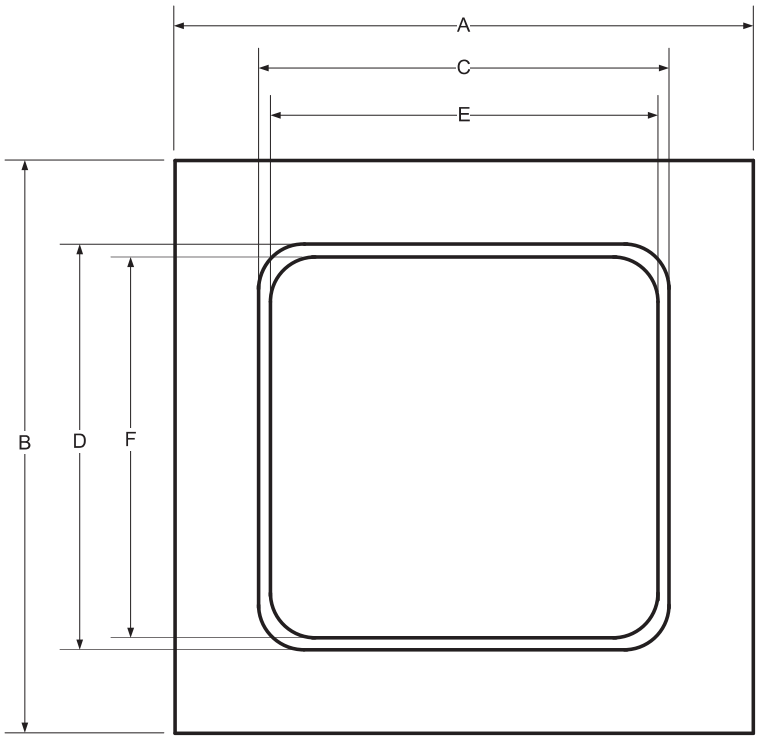


Figure 4: Typical Capacitive Energy vs Reverse Voltage Characteristics

Mechanical Parameters

Die Dimensions	1.6 x 1.6	mm^2
Anode pad size	1.34 x 1.34	
Die Area total / active	2.56/1.69	
Die Thickness	360	µm
Wafer Size	100	mm
Flat Position	0	deg
Die Frontside Passivation	Polyimide	
Anode Pad Metallization	400 nm Ni + 200 nm Au	
Backside Cathode Metallization	400 nm Ni + 200 nm Au	
Die Attach	Electrically conductive glue or solder	
Wire Bond	Au ≤ 76 µm	
Reject ink dot size	Φ ≥ 0.3 mm	
Recommended storage environment	Store in original container, in dry nitrogen, < 6 months at an ambient temperature of 23 °C	

Chip Dimensions:



DIE	A [mm]	1.6
	B [mm]	1.6
METAL	C [mm]	1.34
	D [mm]	1.34
WIRE BONDABLE	E [mm]	1.3
	F [mm]	1.3

Revision History			
Date	Revision	Comments	Supersedes
2015/02/09	1	Inserted Mechanical Parameters	
2012/04/03	0	Initial release	

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Dulles, VA 20166

SPICE Model Parameters

This is a secure document. Please copy this code from the SPICE model PDF file on our website (http://www.genesicsemi.com/images/hit_sic/baredie/schottky/GB05SHT12-CAU_SPICE.pdf) into LTSPICE (version 4) software for simulation of the GB05SHT12-CAU.

```
*     MODEL OF GeneSiC Semiconductor Inc.
*
*     $Revision:   1.0           $
*     $Date:   05-SEP-2013       $
*
*     GeneSiC Semiconductor Inc.
*     43670 Trade Center Place Ste. 155
*     Dulles, VA 20166
*
*     COPYRIGHT (C) 2013 GeneSiC Semiconductor Inc.
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*
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
*
* Start of GB05SHT12-CAU SPICE Model
*
.SUBCKT GB05SHT12 ANODE KATHODE
R1 ANODE INT R=((TEMP-24)*0.0021); Temperature Dependant Resistor
D1 INT KATHODE GB05SHT12_25C; Call the 25C Diode Model
D2 ANODE KATHODE GB05SHT12_PIN; Call the PiN Diode Model
.MODEL GB05SHT12_25C D
+ IS       4.45E-15        RS         0.206
+ N        1.18144         IKF        112.92
+ EG       1.2             XTI        3
+ CJO      3.00E-10        VJ         0.419
+ M        1.6             FC         0.5
+ TT       1.00E-10        BV         1200
+ IBV      1.00E-03        VPK        1200
+ IAVE     5               TYPE       SiC_Schottky
+ MFG      GeneSiC_Semiconductor
.MODEL GB05SHT12_PIN D
+ IS       2.93E-12        RS         0.35326
+ N        4.6113          IKF        0.0043236
+ EG       3.23            XTI        60
+ FC       0.5             TT         0
+ BV       1200            IBV        1.00E-03
+ VPK      1200            IAVE       2.5
+ TYPE     SiC_PiN
.ENDS
*
* End of GB05SHT12-CAU SPICE Model
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