

## SPICE Model Parameters

This is a secure document. Please copy this code from the SPICE model PDF file on our website ([http://www.genesicsemi.com/images/hit\\_sic/baredie/sjt/GA10JT06-CAL\\_SPICE.pdf](http://www.genesicsemi.com/images/hit_sic/baredie/sjt/GA10JT06-CAL_SPICE.pdf)) into LTSPICE (version 4) software for simulation of the GA10JT06-CAL.

```
*      MODEL OF GeneSiC Semiconductor Inc.
*
*      $Revision:   1.1           $
*      $Date:      27-FEB-2014   $
*
*      GeneSiC Semiconductor Inc.
*      43670 Trade Center Place Ste. 155
*      Dulles, VA 20166
*
*      COPYRIGHT (C) 2013 GeneSiC Semiconductor Inc.
*      ALL RIGHTS RESERVED
*
*      These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
*      OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
*      TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
*      PARTICULAR PURPOSE."
*      Models accurate up to 2 times rated drain current.
*
.model GA10JT06 NPN
+ IS      5.08E-47
+ ISE     1.26E-28
+ EG      3.23
+ BF      125
+ BR      0.55
+ IKF     900
+ NF      1
+ NE      2.021
+ RB      0.26
+ RE      0.1039
+ RC      0.06188
+ CJC     2.73E-10
+ VJC     3.04
+ MJC     0.448
+ CJE     6.86E-10
+ VJE     2.89
+ MJE     0.466
+ XTI     3
+ XTB     -0.35
+ TRC1    1.90E-2
+ VCEO    600
+ ICRATING 10
+ MFG     GeneSiC_Semiconductor
*
* End of GA10JT06-CAL SPICE Model
```