SPICE Model Parameters

Please copy this code from the SPICE model into LTSPICE (version 4) software for simulation of the GA01PNS150-CAU device.

```
*      MODEL OF GeneSiC Semiconductor Inc.
*
*      $Revision:   1.1            $
*      $Date:   30-APR-2015        $
*
*      GeneSiC Semiconductor Inc.
*      43670 Trade Center Place Ste. 155
*      Dulles, VA 20166
*
*      COPYRIGHT (C) 2014 GeneSiC Semiconductor Inc.
*      ALL RIGHTS RESERVED
*
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
*
* Start of GA01PNS150-CAU SPICE Model
*
 .MODEL GA01PNS150 D
+ IS        9.71E-12
+ RS        2.24770
+ N         5.7869
+ IKF       0.039646
+ EG        3.23
+ XTI       58
+ TRS1      -0.0034
+ CJO       2.28E-11
+ VJ        2.304
+ M         0.376
+ FC        0.5
+ BV        16000
+ IBV       1.00E-03
+ VPK       15000
+ IAVE      1
+ TYPE      SiC_PiN
+ MFG       GeneSiC_Semi
*
* End of GA01PNS150-CAU SPICE Model
```