

Section VII: SPICE Model Parameters

Please copy this code from the SPICE model PDF file on our website (http://www.genesicsemi.com/images/hit_sic/sjt/GA05JT03-46_SPICE.pdf) into LTSPICE (version 4) software for simulation of the GA05JT03-46.

```
* MODEL OF GeneSiC Semiconductor Inc.  
*  
* $Revision: 1.0 $  
* $Date: 12-DEC-2014 $  
*  
* GeneSiC Semiconductor Inc.  
* 43670 Trade Center Place Ste. 155  
* Dulles, VA 20166  
*  
* COPYRIGHT (C) 2014 GeneSiC Semiconductor Inc.  
* ALL RIGHTS RESERVED  
*  
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY  
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED  
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A  
* PARTICULAR PURPOSE."  
* Models accurate up to 2 times rated drain current.  
*  
.model GA05JT03 NPN  
+ IS 9.8338E-48  
+ ISE 1.0733E-26  
+ EG 3.23  
+ BF 135  
+ BR 0.55  
+ IKF 200  
+ NF 1  
+ NE 2.  
+ RB 14.5  
+ IRB 0.002  
+ RBM 0.37  
+ RE 0.01  
+ RC 0.23  
+ CJC 2.16E-10  
+ VJC 3.656  
+ MJC 0.4717  
+ CJE 5.021E-10  
+ VJE 2.95  
+ MJE 0.4867  
+ XTI 3  
+ XTB -1.0  
+ TRC1 1.050E-2  
+ VCEO 300  
+ ICRATING 9  
+ MFG GeneSiC_Semiconductor  
*  
* End of GA05JT03 SPICE Model
```