

## SPICE Model Parameters

Please copy this code from the SPICE model into LTSPICE (version 4) software for simulation of the GA50SLT06-CAL.

```
*      MODEL OF GeneSiC Semiconductor Inc.
*
*      $Revision:   1.0           $
*      $Date:      20-SEP-2013   $
*
*      GeneSiC Semiconductor Inc.
*      43670 Trade Center Place Ste. 155
*      Dulles, VA 20166
*
*      COPYRIGHT (C) 2013 GeneSiC Semiconductor Inc.
*      ALL RIGHTS RESERVED
*
*      These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY OF ANY
*      KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO ANY IMPLIED
*      WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE."
*      Models accurate up to 2 times rated drain current.
*
*      Start of GB50SLT06-CAL SPICE Model
*
.SUBCKT GB50SLT06 ANODE KATHODE
D1 ANODE KATHODE GB50SLT06_SCHOTTKY
D2 ANODE KATHODE GB50SLT06_SURGE
.MODEL GB50SLT06_SCHOTTKY D
+ IS      1.99E-16      RS      0.015652965
+ N       1            IKF     1000
+ EG      1.2          XTI     3
+ TRS1    0.0042       TRS2    1.3E-05
+ CJO     3.86E-09     VJ      1.362328465
+ M       0.48198551   FC      0.5
+ TT      1.00E-10     BV      650
+ IBV     1.00E-03     VPK     650
+ IAVE    50           TYPE    SiC_Schottky
+ MFG     GeneSiC_Semi
.MODEL GB50SLT06_SURGE D
+ IS      1.54E-19     RS      0.1
+ TRS1    -0.004      N       3.941
+ EG      3.23        IKF     19
+ XTI     0           FC      0.5
+ TT      0           BV      650
+ IBV     1.00E-03     VPK     650
+ IAVE    50           TYPE    SiC_PiN
.ENDS
*
*      End of GB50SLT06-CAL SPICE Model
```