Section VII: SPICE Model Parameters

Please copy this code from the SPICE model PDF file on our website (http://www.genesicsemi.com/images/hit_sic/sjt/GA50JT06-258_SPICE.pdf) into LTSPICE (version 4) software for simulation of the GA50JT06-258.

```
*       MODEL OF GeneSiC Semiconductor Inc.
*
*       $Revision:   1.3             $
*       $Date:    12-DEC-2014         $
*
*       GeneSiC Semiconductor Inc.
*       43670 Trade Center Place Ste. 155
*       Dulles, VA 20166
*
*       COPYRIGHT (C) 2014 GeneSiC Semiconductor Inc.
*       ALL RIGHTS RESERVED
*
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
*
.model GA50JT06 NPN
*
+ IS          5.00E-47
+ ISE         1.26E-26
+ EG          3.23
+ BF          106
+ BR          0.55
+ IKF         9000
+ NF          1
+ NE          2
+ RB          0.9
+ IRB         0.002
+ RBM         0.09
+ RE          0.01
+ RC          0.013
+ CJC         2.3989E-9
+ VJC         2.8346223
+ MJC         0.4846
+ CJE         6.026E-09
+ VJE         3.17915435
+ MJE         0.52951635
+ XTI         3
+ XTB         -1.2
+ TRC1        7.00E-3
+ VCEO        600
+ ICRATING    100
+ MFG         GeneSiC_Semiconductor
*
* End of GA50JT06 SPICE Model
```