

Evaluation Seminar “Share Your Knowledge”

13 april 2005, Boxmeer

1. What did you think of the location?

Excellent	23,08%
Good	61,54%
Reasonable	
Poorly	7,69%
Bad	

2. If you have answered question 1 with reasonable, poorly or bad, could you please indicate why you think so.

Quite a distance, although the location itself was perfect.

3. What did you think of the quality of the speakers in general?

Excellent	38,46%
Good	53,85%
Reasonable	
Poorly	
Bad	

4. What did you think of the presentation material?

Excellent	15,38%
Good	53,85%
Reasonable	7,69%
Poorly	15,38%
Bad	

5. Could you please motivate your answer?

Excellent/Good: presentations were well prepared; Motivated and knowledgeable speakers.
Reasonable/Poorly: No information sheets; A large number of topics presented and discussed at a high level. Prefer more profound presentations on less topics; Too small and out of focus information on the projection screen.

6. Which part of the seminar interested you the most (more answers possible):

• Model & Library Creation	30,77%
• System Design Flow Demonstration	46,15%
• Circuit/System Concurrent Design of Digitally Predistorted Power Amplifiers - Part I	23,08%
• WLAN Design Example with VSS	46,15%
• PCB/Module Layout Co-Simulation with MWO	46,15%
• Circuit/System Concurrent Design of Digitally Predistorted Power Amplifiers - Part I	23,08%

7. Could you please motivate your answer?

Nice overview with real-time demo's; Very practical, after all we're engineers. Everything was of interest to me. VSS appears a very interesting, time-saving tool; Special interest in system level design tools and flows.

8. Which part of the seminar interested you the least:

• Model & Library Creation	23,08%
• System Design Flow Demonstration	7,69%
• Circuit/System Concurrent Design of Digitally Predistorted Power Amplifiers - Part I	7,69%
• WLAN Design Example with VSS	7,69%
• PCB/Module Layout Co-Simulation with MWO	7,69%
• Circuit/System Concurrent Design of Digitally Predistorted Power Amplifiers - Part I	23,08%

9. Could you please motivate this answer too?

Some presentations were too superficial

10. Of which parts of the seminar would you like to receive more information?

• Model & Library Creation	30,77%
• System Design Flow Demonstration	53,85%
• Circuit/System Concurrent Design of Digitally Predistorted Power Amplifiers - Part I	23,08%
• WLAN Design Example with VSS	38,46%
• PCB/Module Layout Co-Simulation with MWO	61,54%
• Circuit/System Concurrent Design of Digitally Predistorted Power Amplifiers - Part I	30,77%

11. Are there topics you really missed in this seminar?

More extensive demo with Rhode & Schwarz measurement tools in combination with AWR software; As a non-user I'd like to obtain a more complete and systematic view on potential of products; As a user I'd like to know more about (the cost of) the Gerber Tester.

12. Should you have any more remarks that did not occur in the evaluation, please put them below.

A very well spent day; Very interesting, of importance for my work; Looking forward to the next seminar.