CLOSED LOOP HALL EFFECT CURRENT SENSOR JP-200



FEATURES

- Closed loop (compensated) current transducer using the Hall effect
- Printed circuit board mounting
- Insulated plastic case recognized according to UL 94-V0.

ELECTRICAL PROPERTIES

| Model | | JP-200 |
|-------------------------------|--------------------------|---|
| Primary norminal current | lf | 200A |
| Measuring resistance | R | $ \begin{array}{c} Vcc{=}{\pm}12V @ {\pm}200A: 0\Omega \sim 26\Omega \\ @ {\pm}250A: 0\Omega \sim 4\Omega \\ Vcc{=}{\pm}15V @ {\pm}200A: 0\Omega \sim 56\Omega \\ @ {\pm}300A: 0\Omega \sim 8\Omega \end{array} $ |
| Rated output current | I _o | 100mA (Turn ratio 1:2000) |
| Output current accuracy | V | ±0.4 (±15V, +25°C) |
| Offset current | I _{of} | $\leq \pm 0.2$ mA (at lf=0A) |
| Output linearity | ٤ | $\leq \pm 0.15\%$ (at lf) |
| Power supply voltage | V _{cc} | $\pm 12V \pm 5\% \sim \pm 15V \pm 5\%$ (Ratad output current is restricted by Vcc) |
| Response time | tr | $\leq 1\mu S(at di/dt=lf/\mu s)$ |
| Frequency characteristics | f | DC100kHz (-1 dB) |
| Thermal drift of gain | TCI _o | $\leq \pm 0.01\%$ °C(Without Tclof) |
| Thermal drift of offset | TCI _{of} | ≤ ± 0.5mA |
| Hysteresis error | I _{oH} | \leq 0.3mA (at If=0A \rightarrow If \rightarrow If=0A) |
| Insulation voltage | V _D | AC3000V for 1 minute (Sensing current 0.5mA) inside of through hole ⇔ terminal |
| Insulation resistance | R _{is} | \geq 500M Ω (at DC500V) inside of through hole \Leftrightarrow terminal |
| Ambient Operating temperature | T _A | -40°C ~ +85°C |
| Ambient storage temperature | T _s | -40°C ~ +90°C |
| Secondary coil resistance | R _s | 76Ω(@Ta=70°C) 80Ω(@Ta=85°C) |

DIMENSION



CONNECTION



Unless otherwise specified, tolerances shall be ±0.5mm