

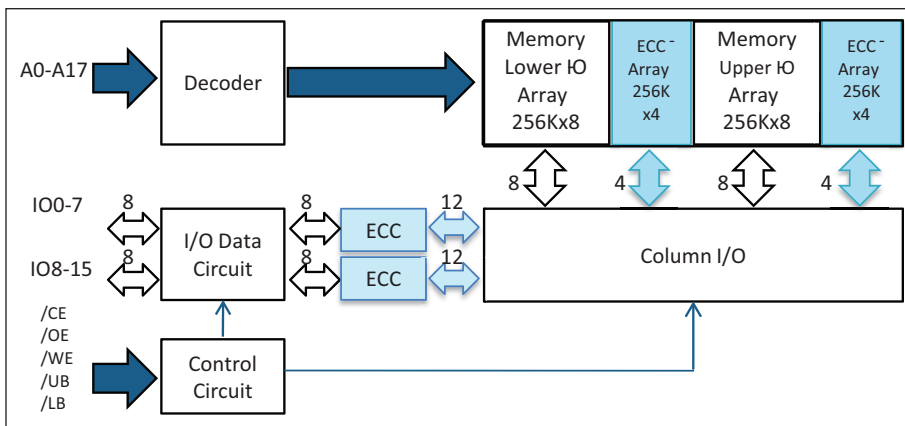


# Error Correction Code (ECC) Based New High Speed Low Power 4Mb Asynchronous SRAM

ISSI's latest Error Correction based 4Mb High Speed Low Power Asynchronous SRAM is currently sampling. This innovative design reinforces ISSI's long-term commitment to SRAMs with the highest quality and performance. This industry's first Error Correction Code (ECC) based Asynchronous SRAM meets high quality requirements in automotive, industrial, military-aerospace, and other applications.

## Error Detection and Error Correction

- Independent ECC with hamming code for each byte
- Detect and correct one bit error per each byte
- Better reliability than parity code schemes which can only detect an error but not correct an error
- Backward Compatible: Drop in replacement to current in industry standard devices (without ECC)



## ► Applications

- Automotive
- Military-Aerospace/Medical
- Industrial
- Telecom/Networking

► [Click here for datasheet](#)

## ► Additional ECC Async SRAMs

- 2Mb, 8Mb

## Key Features

	IS64WV25616EDBLL (A1)	IS64WV25616EDBLL (A3)	Comments
<b>Temperature Support</b>	Industrial (-40°C to +85°C)	Automotive (-40°C to +125°C)	Contact ISSI for military temperature
<b>Technology</b>	65nm	65nm	
<b>Standby Current</b>	9mA	20mA	Typical value 2mA
<b>Operating Current</b>	45mA	65mA	Typical value 25 mA
<b>Data Retention Current</b>	9mA	15mA	Typical value 2mA
<b>Packaging</b>	TSOP-II (44 pins) BGA (48 pins)	TSOP-II (44 pins) BGA (48 pins)	Pin compatible with industry standard 4Mb Async. SRAM
<b>Speed</b>	10ns	10ns	
<b>Copper Leadframe</b>	Yes	Yes	Improved thermal performance
<b>Lead-free and Leaded</b>	Yes	Yes	RoHS Compliant
<b>Availability</b>	Sampling	Sampling	