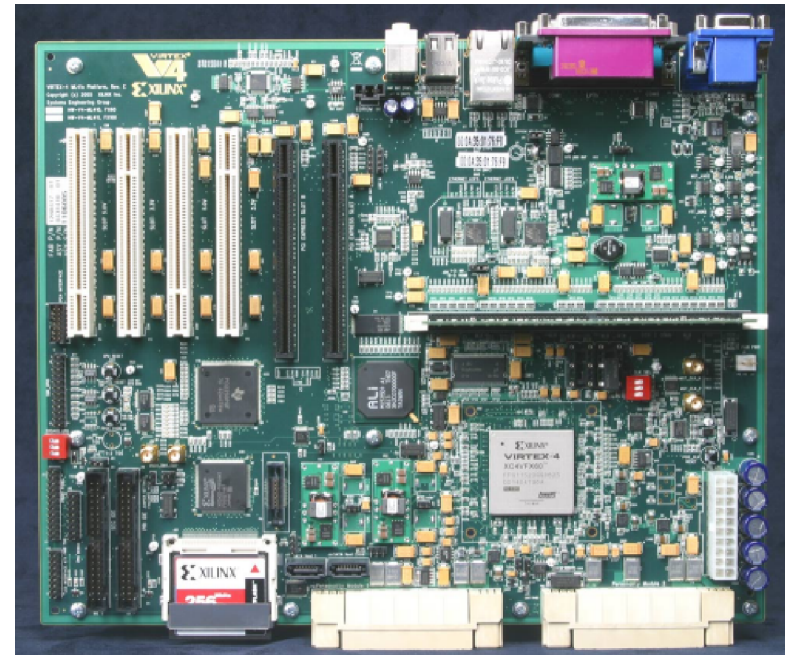


Impulse C PowerPC Demonstration



Dual PowerPC™ Acceleration

- Embedded web server, camera interface and accelerated filtering
- Two PowerPC processors, both with Impulse C acceleration
- Featuring the Xilinx ML410 development board and Virtex-4 FX60 FPGA



Demonstration Setup Notes



ML410 Connectors(facing):

- Left DB9 = MPEG decoding CPU (standalone)
- Right DB9 = Webserver, display CPU (vxWorks)
- Bottom RJ-45 = Webserver CPU

ML410 setup:

- IP is fixed at 192.168.2.100
- "CF_Files" need to be copied to CF6
- DIP switch settings for SystemAce to boot from CF6
- VGA video display needs to be connected
- RJ-45 for Webserver needs to be connected directly or via Ethernet switch to access the host PC
- PC's FTP server must be already running before starting the ML410, success will blackout screen and load bitmaps.
- Video will only appear when vxWorks has booted and started the necessary threads correctly

Host PC:

- IP must be 192.168.2.1
- Must have an FTP server running allowing read access for the login=impulse with password=impulse
- "FTP_Files" need to be copied into the root of the FTP serving directory
- NOTE: Running "run_FTP_Server.bat" in directory will do both of the above
- Point Web browser to: <http://192.168.2.100/>

Misc Notes:

- Bitmaps loaded onto screen must have a width that is divisible by 4 to appear correctly
- Bitmaps for Rev. D and Rev. E of the ML410 are not interchangeable.

Multiprocessing MPEG2 Demonstration

