

Impulse C-to-FPGA Solutions Highlighted at Reconfigurable Systems Summer Institute

Impulse Founder and CTO David Pellerin to Present C-to-FPGA Workshop at the National Center for Supercomputing Applications

What: RSSI 2008
Where: NSCA in Urbana, IL
When: July 7 - 9, 2008

Kirkland, WA – June 18, 2008 – Impulse Accelerated Technologies, Inc. today announced its participation at the 2008 Reconfigurable Systems Summer Institute, being held July 7 through 9 at the National Center for Supercomputing Applications (NCSA) in Urbana, Illinois.

Impulse will conduct live demonstrations of its C-to-FPGA solutions, highlighting both embedded and high performance computing applications.

C-to-FPGA Workshop

On Monday, July 7, Impulse CTO David Pellerin will present a 4-hour workshop on software-to-hardware programming methods. This workshop will focus on FPGA compilation tools and techniques, including pipeline optimization and instruction parallelism, using the Impulse tools. The session will include descriptions of various parallel programming methods, including communicating parallel processes, data streaming, and shared memories. Examples of C-language code optimization and cycle-accurate debugging, as well as platform-specific hardware generation, will be presented. Applications will include image processing, DSP and stochastic (Monte Carlo) simulations. Multiple FPGA-based hardware platforms will be discussed and demonstrated.

About RSSI

RSSI brings together scientists and technology developers from industry and academia to present and discuss new research on high-performance reconfigurable computing. For more information or to register, go to <http://www.rssi2008.org/index.html>.

About Impulse

Impulse provides software-to-FPGA solutions for embedded and high performance computing. For more information, visit <http://www.ImpulseC.com>.