



PRESS RELEASE



For Immediate Release

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GiDEL™ DEMONSTRATES TIGHTLY INTEGRATED TOOLSET FOR ACCELERATING C ALGORITHMS IN HIGH POWERED FPGAS

Impulse C™ Compiler Enables Fast Development of FPGA-Accelerated Software

November 7, 2007 – Santa Clara, California and Kirkland, Washington. GiDEL, a leading supplier of FPGA-based compute accelerator systems, and Impulse Accelerated Technologies, a leading supplier of optimizing C to FPGA software, today announced the availability of C-language to FPGA compilation using the Impulse C tools from Impulse Accelerated Technologies.

The new capabilities include an Impulse C Platform Support Package that tightly integrates the Impulse C-to-FPGA compiler to GiDEL's PROCStarIII™, PROCStarII™, PROCSpark II™ and PROCe™ systems using the GiDEL PROCWizard™ Software.

GiDEL PROC FPGA computing systems feature advanced Altera Stratix™ FPGAs that can be configured to provide application specific acceleration for computationally intensive processing. The PROC reconfigurable systems are used to accelerate complex algorithms that include DSP, image processing, national security and other performance-critical domains.

"The integration of Impulse C and GiDEL PROCWizard tools allows algorithm developers to more rapidly prototype, accelerate and optimize complex applications," said Reuven Weintraub, GiDEL President and CTO. "This integration reduces or eliminates the need to write low-level HDL code, increasing access to FPGAs by software programmers."

"GiDEL's FPGA acceleration platforms are an ideal target for C-to-FPGA acceleration," said David Pellerin, Impulse CTO. "Our users are finding that higher-level design methods save them 50% or more of their overall project design time, and HPC programmers in particular will appreciate the ease-of-use provided by this combined solution."

GiDEL and Impulse will be demonstrating the new integration between the Impulse C design software and the GiDEL PROCWizard tools at the International Conference for High Performance Computing being held November 10 to 16, 2007 in Reno, Nevada.

“The continuing evolution of software-to-hardware tools is critical to the widespread acceptance of FPGAs as hardware coprocessors,” said Mike Strickland, director of strategic and technical marketing for Altera's computer and storage business unit. “It is encouraging to see a combined hardware and software development system that makes FPGA acceleration available to developers of high performance software.”

About GiDEL

GiDEL Ltd. is a successful, profitable and innovative company founded in 1993. GiDEL has become one of the market leaders as a company that continuously provides cutting-edge reconfigurable technology utilizing FPGAs. GiDEL sees its customers as partners and uses its vast experience at the project-level and FPGA design to focus on its customers' projects' success. Customers in semiconductor, consumer product, communications, machine vision, medical imaging, and military/aerospace markets purchase the PROC family of reconfigurable PROCessors for SoC and ASIC verification, as COTS acquisition and accelerator boards, and to validate complex algorithms. For more information, contact GiDEL in North America at 408-969-0389, or world wide at +972 4 610 2505, or on the web at www.gidel.com

About Impulse

Impulse C allows HPC software developers to rapidly and cost-effectively move applications originating in ANSI C to FPGA coprocessors. The Impulse C tools enable partitioning, optimization, and FPGA hardware generation. Interconnections between FPGA coprocessors and host processors are generated automatically for selected platforms. A wide range of FPGA based platforms are supported. For more information about advanced C-to-FPGA design methods and tools, visit www.ImpulseC.com

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