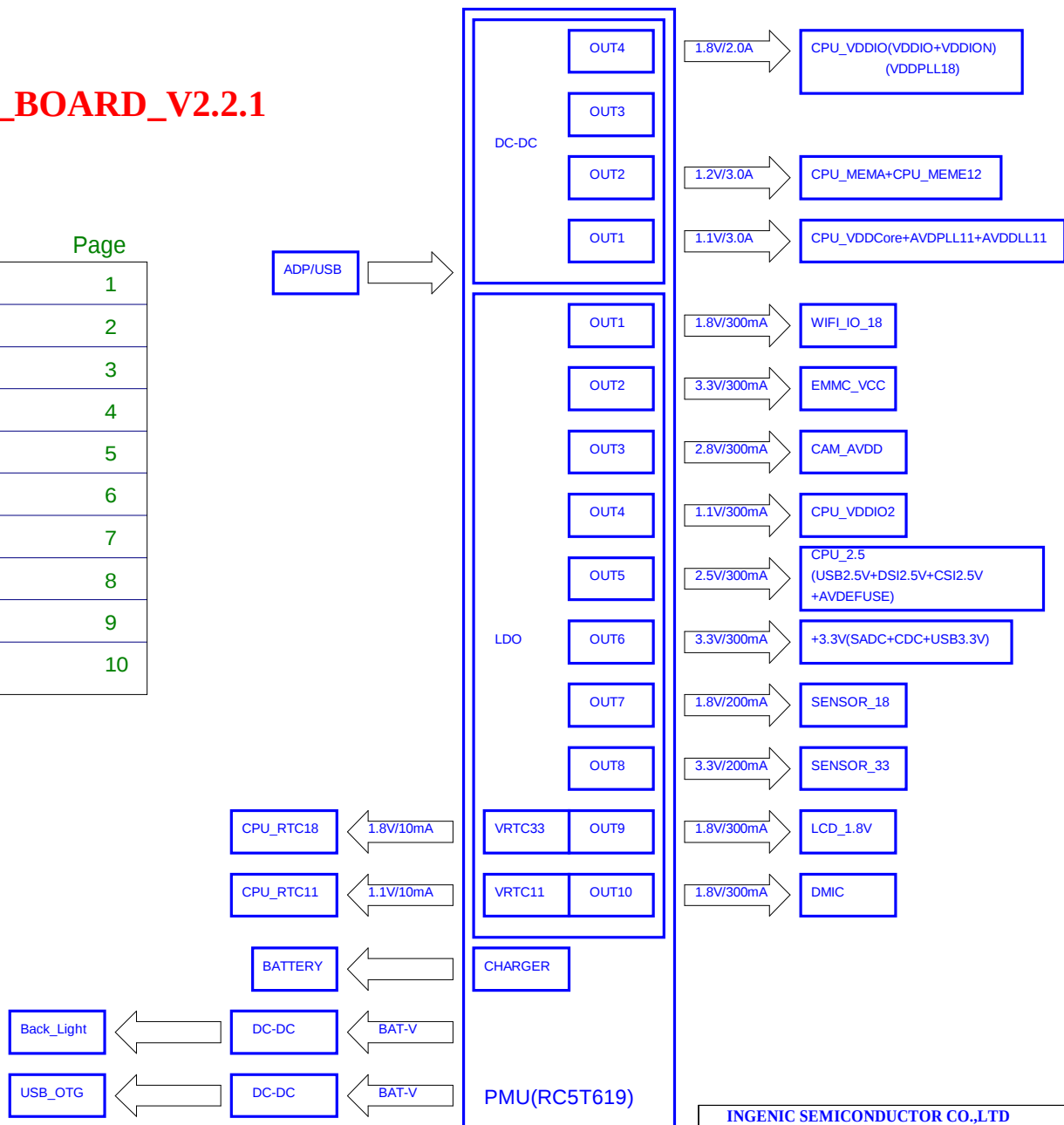
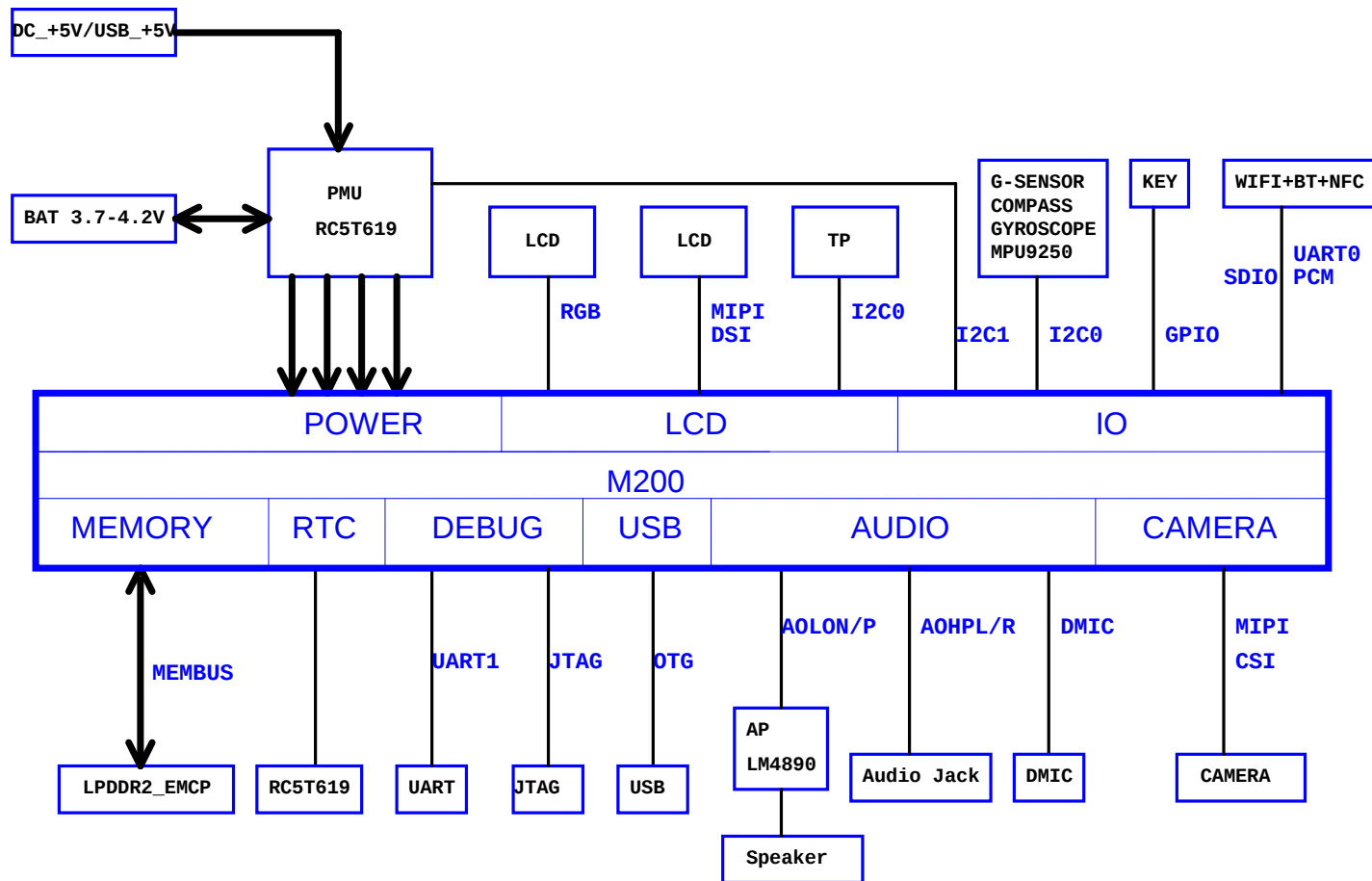


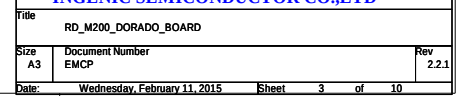
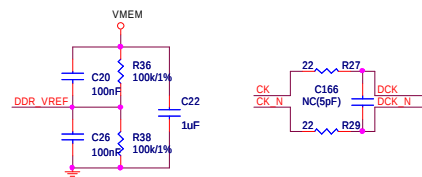


RD_M200_DORADO_BOARD_V2.2.1

Title	Page
COVER SHEET/POWER ARCHITECTURE	1
SYSTEM ARCHITECTURE	2
EMCP	3
IO	4
CPU_PWR/JTAG/UART/KEY	5
PMU_RN5T619	6
USB_OTG/AUDIO	7
SENSOR/LCD/CAMERA	8
WIFI_BT_NFC	9
REVISION HISTORY	10







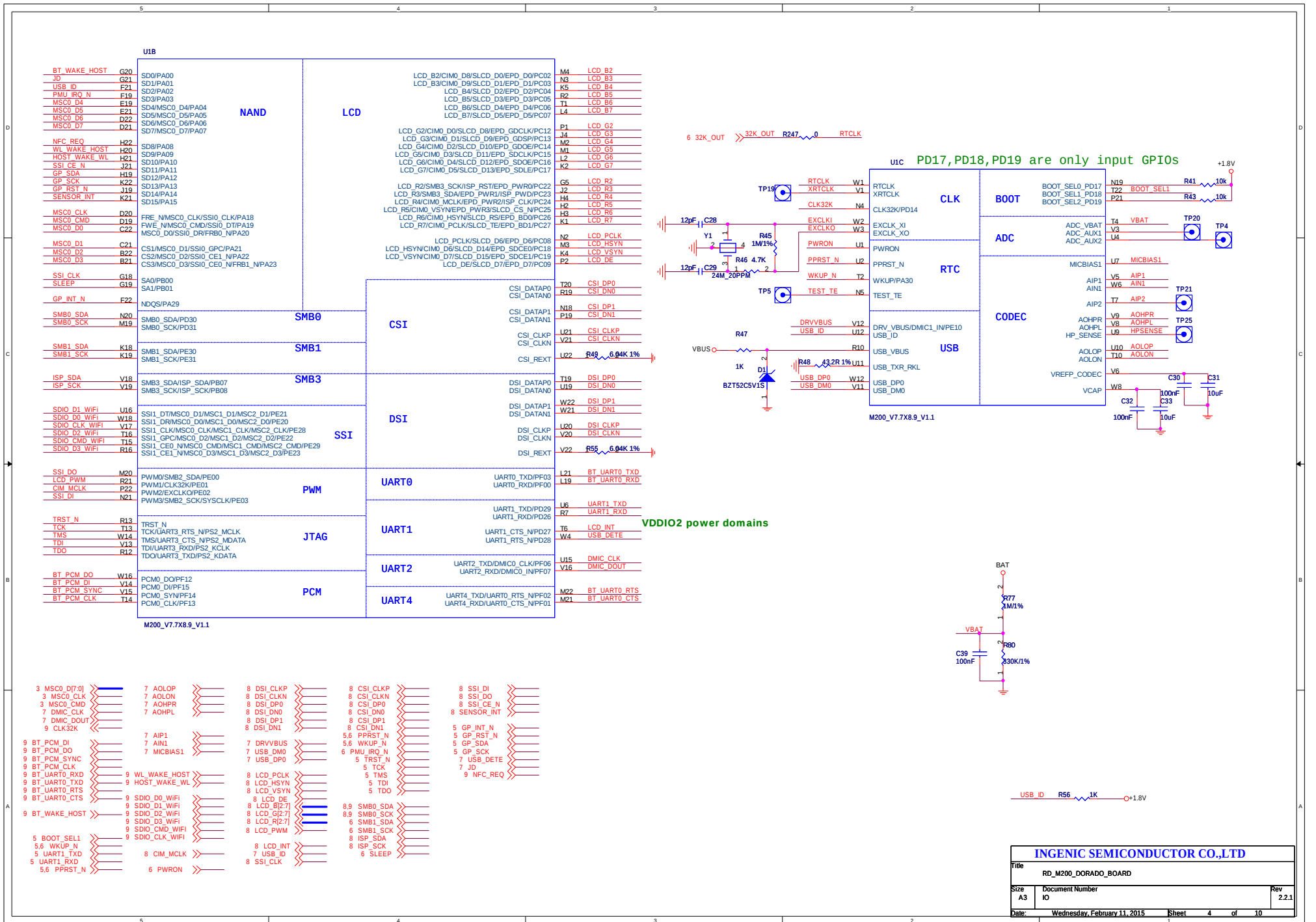
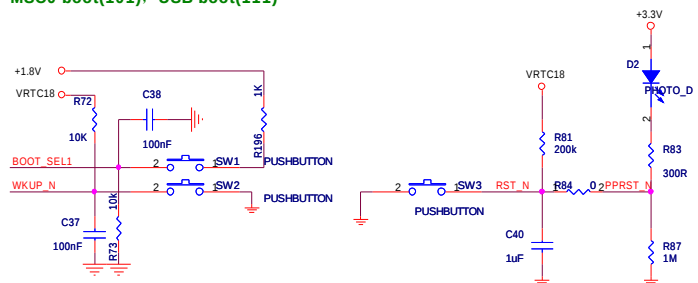


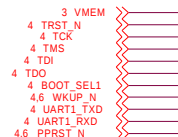
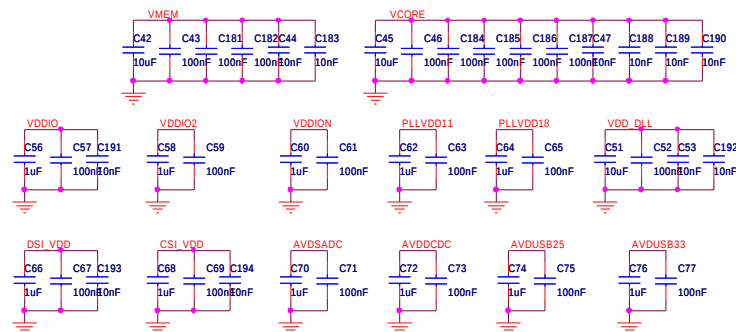
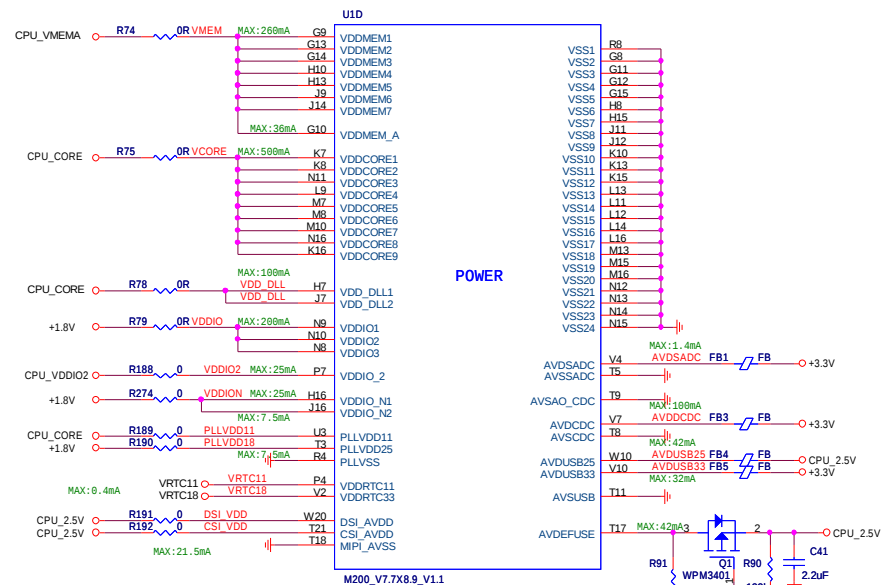
Diagram illustrating the JTAG interface connections:

- TP13 is connected to TRST_N.
- TP15 is connected to TDI.
- TP16 is connected to TDO.
- TP14 is connected to TMS.
- TP17 is connected to TCK.
- TP18 is connected to RST_N.

MSC0 boot(101), USB boot(111)

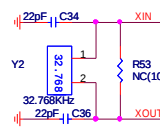
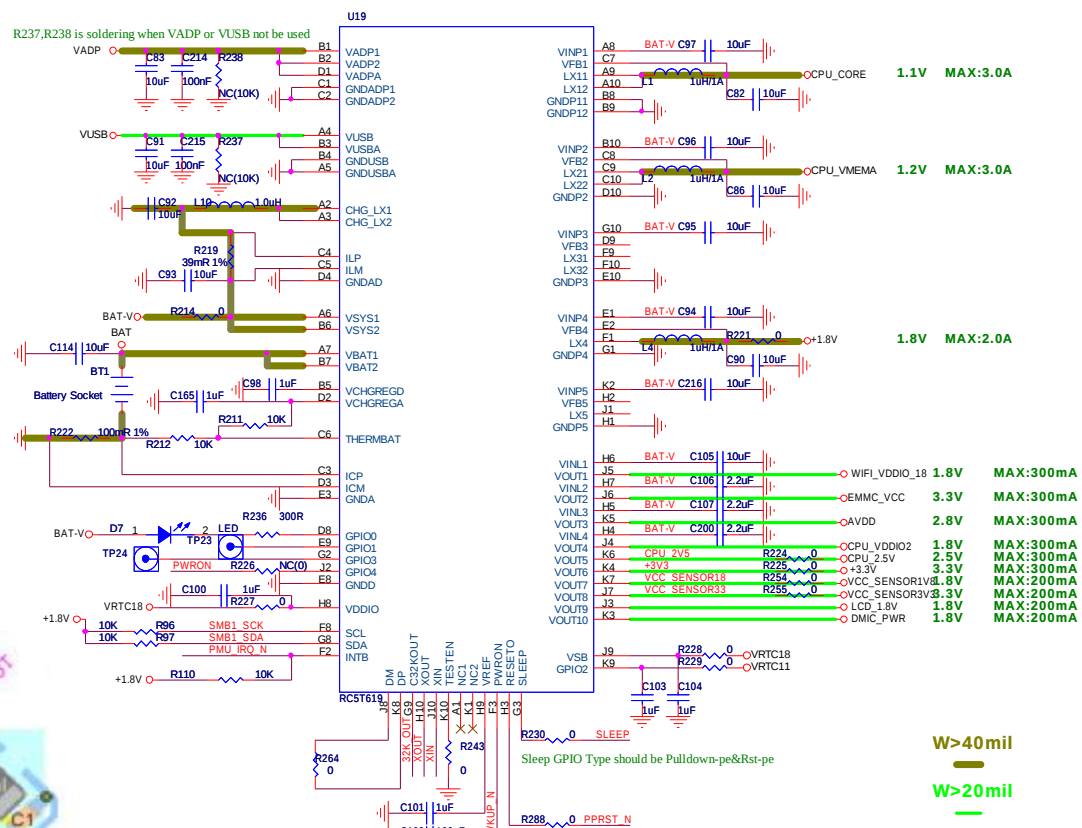
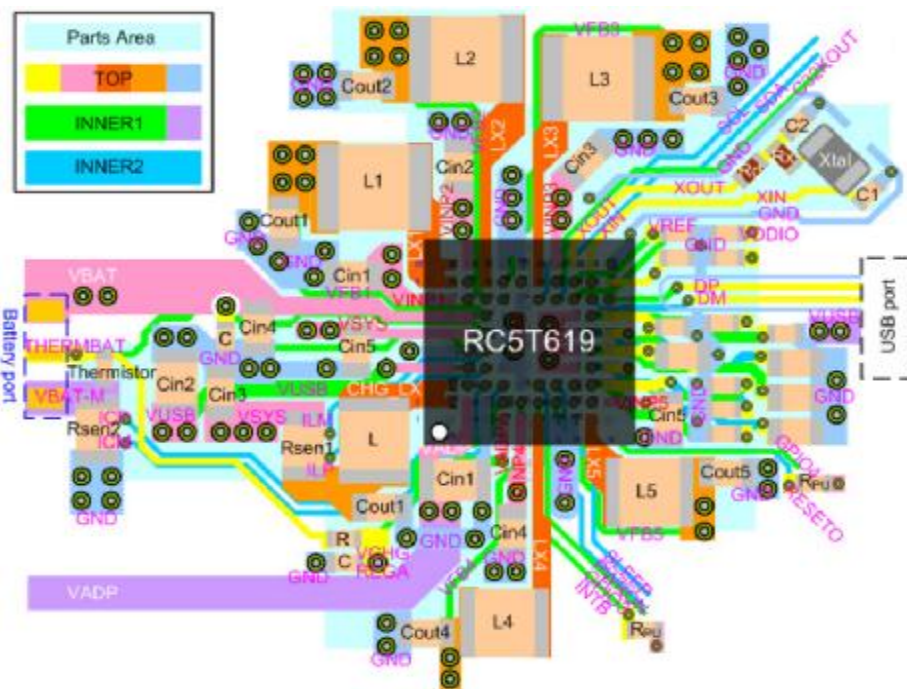
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GPIO

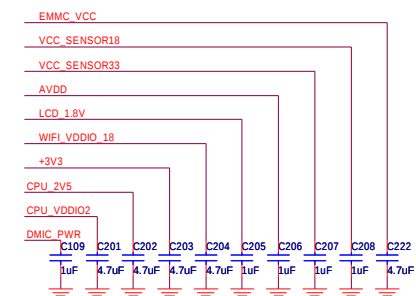


INGENIC SEMICONDUCTOR CO.,LTD			
Title RD_M200_DORADO_BOARD			
Size A3	Document Number CPU_PWRRTAGUART/KEY		Rev 2.2.1
Date:	Wednesday, February 11, 2015	Sheet	5 of 10

Layout Guide

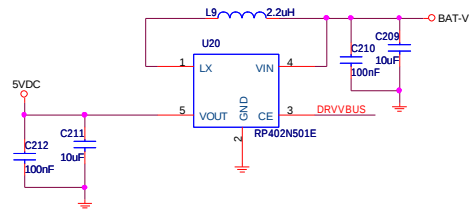
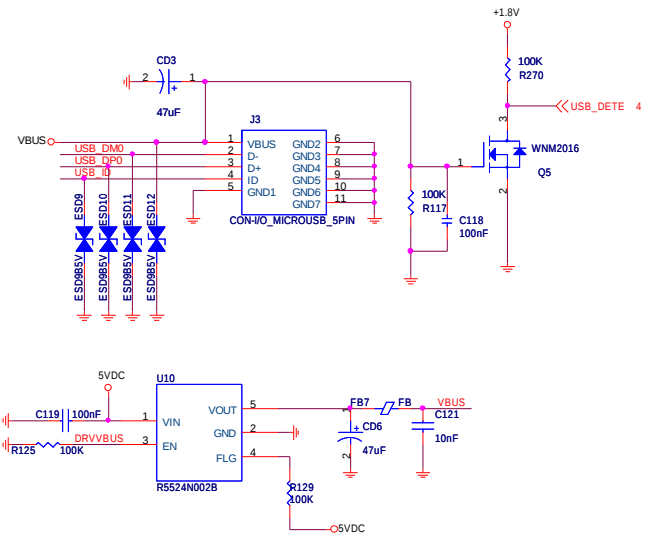


BAT capacity(mAh)	FG accuracy(mA)	Sense Resistance_R222(mohm)
300	1	100
600-1000	1	100
>2000	3	20

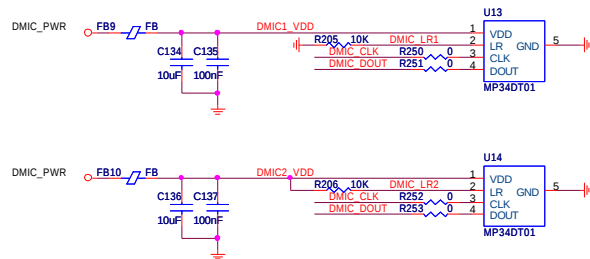


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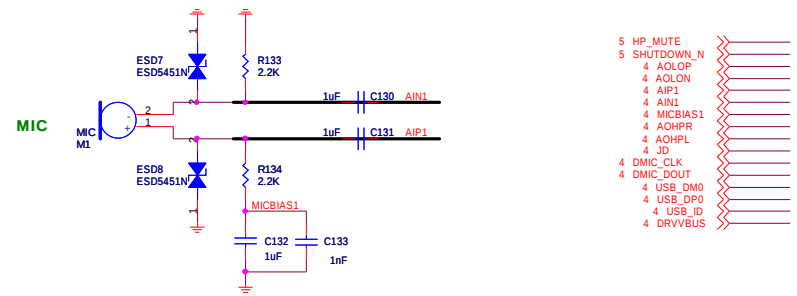
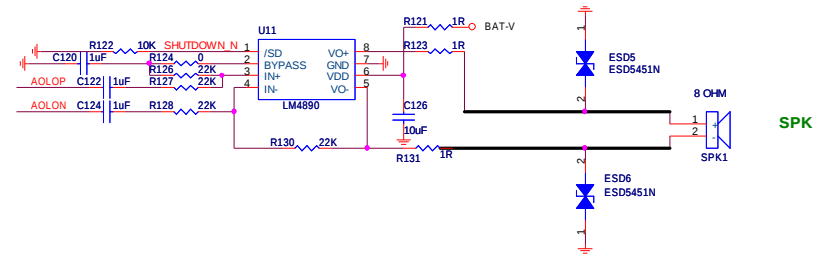
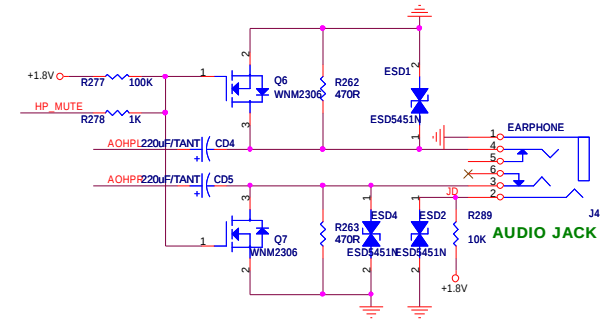
Title			
RD_M200_DORADO_BOARD			
Size	Document Number	Rev	
A3	PMU_RN5T619	2.2.1	
Date:	Wednesday, February 11, 2015	Sheet	6 of 10



USB2.0_OTG



Digital MIC

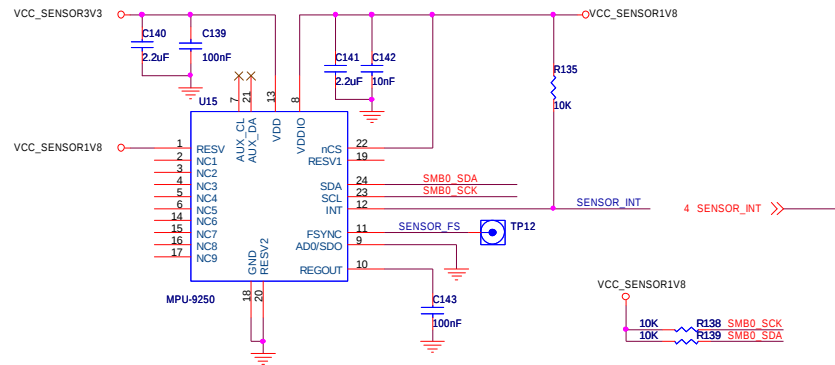


AUDIO

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Title		
RD_M200_DORADO_BOARD		
Size	Document Number	Rev
A3	USB_OTG/AUDIO	2.2.1
Date:	Wednesday, February 11, 2015	Sheet 7 of 10

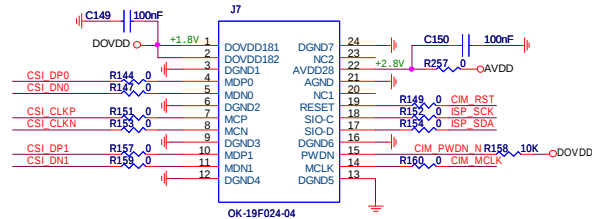
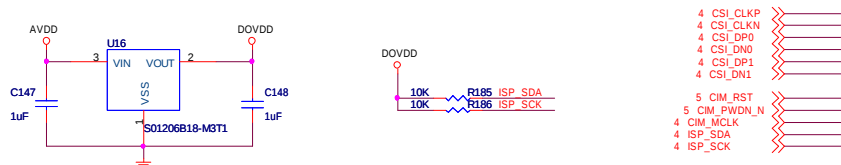
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Gyroscope      : 3.2mA / 8uA
Accelerometer  : 450uA / 8uA
Magnetometer   : 280uA / NO Parameter
```



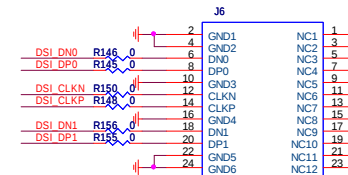
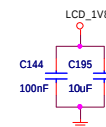
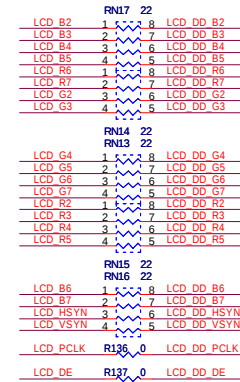
GYROSCOPE+GSENSOR+COMPASS

addr : 1101000

Gsensor

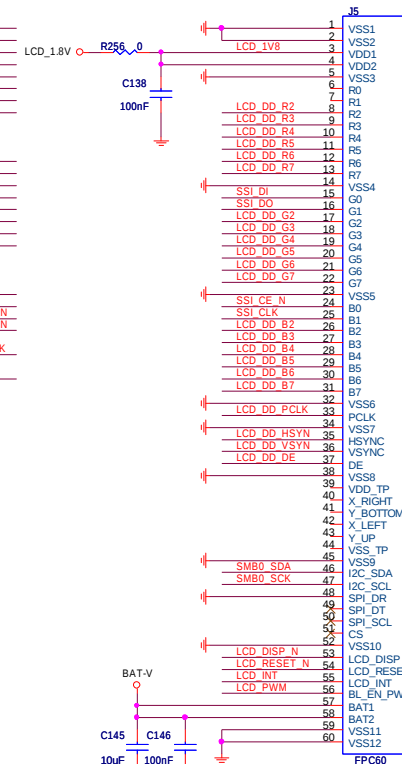


MIPI CAMERA

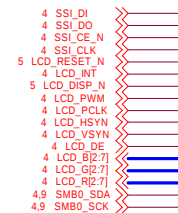


OK-19F024-04

MIPI LCD



RGB & SMART & EPD



Data	Revision	Change
Apr 02 2014	Rev2.0	1. First Revision
JUL 01 2014	Rev2.1	1. PAGE3 ADJUST VDDNAND_3.3V TO EMMC_VCC Adjust DDR_VREF circurst and the resistance to 49.9K VDDMEM_+1.8V power domain is LDO and R199 NC,add four 01005 resistances R258-R261. 2. PAGE4 Adjust RTC circurst to oscillator U21 and a RTC clock drive from PMU. Delete resistances R197,R198,R50,R51,R58,R59,R60,R61,R62,R63, R64,R65,R66,R67,R68,R69,R70,R71 Add a GPIO expander TCA9539 and adjust some GPIO. 3. PAGE5 Change JTAG port to test point and UART port Combine power domain VDDCORE with VDDPLL and VDDDLL, Combine power domain CPU_VDDMEMA with CPU_MEM12. CPU_VDDIO2 power supply is a independent LDO Q1 is soldering and adjust R91 to 47ohm 4. PAGE6 Delete a power domain DCDC3 and adjust LDO,add RTC circuit. 5. PAGE7 Adjust USB boost circuit to RICOH RP402K501B, adjust DMIC dataline 6. PAGE8 Change packaging of J6,J7 and add R257 to AVDD. 7. PAGE9 Add load switch U27 and adjust power source of WiFi_BAT to BAT-V Add two 470ohm resistances R262,R263 to reduce POP noise and change packaging of J4.
AUG 05 2014	Rev2.1.1	1. PAGE5 Adjust CPU package (VDDRRTC and PLLVDD changed to 1.8V,powered by CPU_VDDIO). 2. PAGE3 Add two resistances R272 and R273.
AUG 29 2014	Rev2.1.2	1. PAGE3 Adjust MSC pullup resistor to VDDNAND_VCCQ and adjust package type of U3. Adjust U3 to NC and R199 soldering. 2. PAGE4 U21 is oscillator only and adjust some IO SSI_CLK,GP_INT_N,LCD_DISP_N,LCD_RST_N,LCD_INT_N,JD. Delete U21,R246,C217 3. PAGE5 Add LCD_DISP_N,MSC_RST_N,LCD_RESET_N to U22 and adjust R91 to 240ohm. Add a R276. Adjust R196 to 1K and R265,R266 pullup net is PCA_VCCAdjust VDDPLL25 power to +1.8V. Add R279 for uart_rx circuit. 4. PAGE6 Connect PMU VDDIO(H8) to VRTC18 and delete R231,R233. Add a power domain DMIC_PWR. Adjust R219 to 43mR and R222 to 100mR. Adjust CPU_VDDIO to +1.8V. Add USB_ID pull up resistor and adjust crystal circuit Add OVP protection circuit on VADP and VUSB. Delete R241 and add TP23 and TP24 5. PAGE7 Add R275 and adjust DMIC power DMIC_PWR. Delete EAR_MIC circuit and add plug in detection. Adjust USB detection circuitAdd HP MUTE circuit. 6. PAGE8 Adjust R138,R139 pullup net to VCC_SENSOR1V8 and R185,R186 pullup net to DOVDD. 7. PAGE9 Delete R161 and R163 is NC. Adjust R165,R166 to 10K. Adjust D8 to WSB5501
Nov 12 2014	Rev2.2	1. PAGE4 Exchange two nets SMB1_SCK/SDA and GP_SCK/SDA.Change three nets JD,USB_ID,NFC_REQ. 2. PAGE5 Change nets BT_REG_EN,HOST_WAKE_BT,WL_REG_EN and delete R269 on U22. 3. PAGE6 Add R288 and the net of PPRST_N. Change the footprint of Y2. Add layout guide of PMU. Adjust R219 to 39mR and PCB footprint to 0603. 4. PAGE9 Change D8 to R290.
Feb 11 2015	Rev2.2.1	1. PAGE7&8 Adjust 1st part description and value of U20,J6 and J7.

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RD_M200_DORADO_BOARD			
File			
Size A3	Document Number REVISION HISTORY	Rev 2.2.1	
Date:	Wednesday, February 11, 2015	Sheet 10	of 10