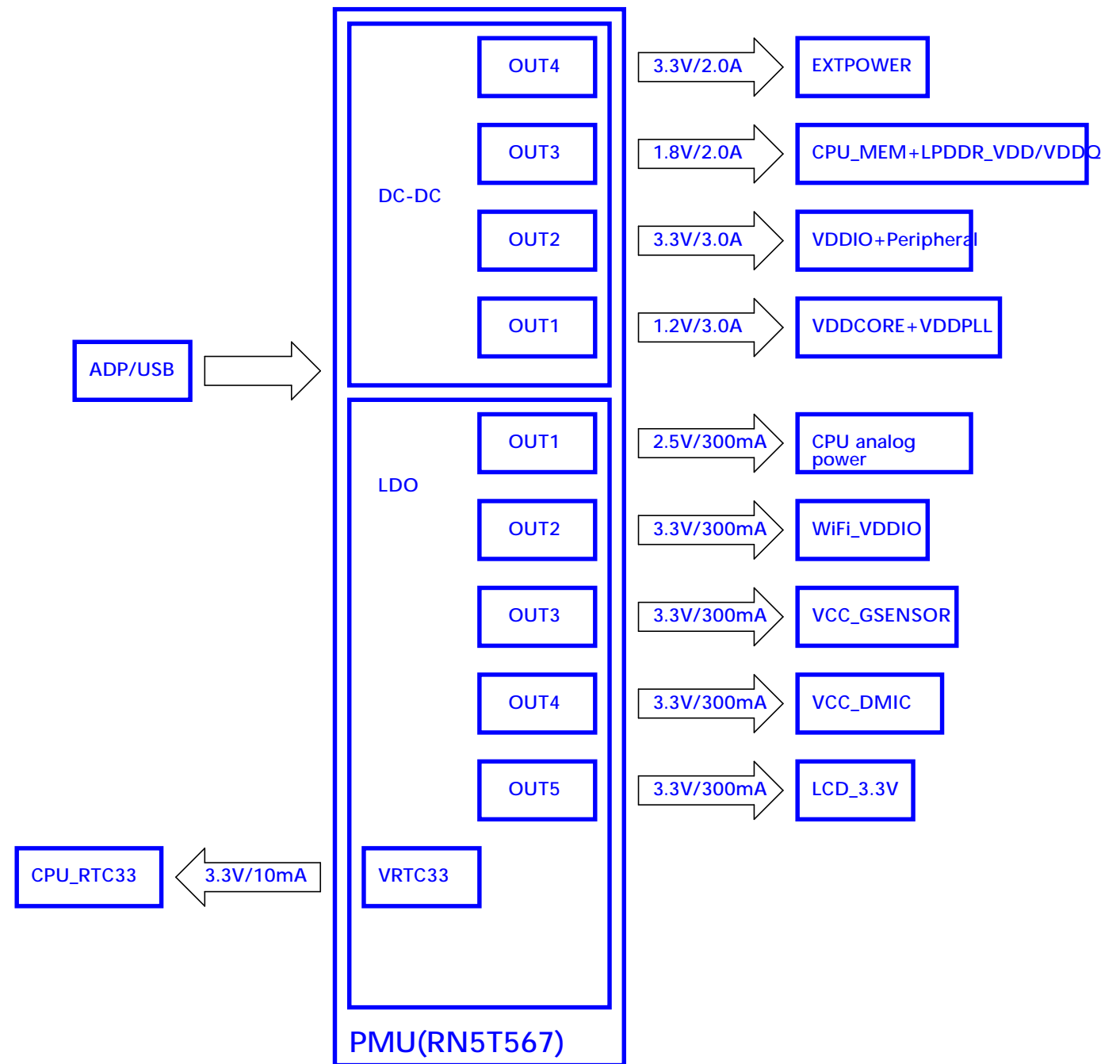
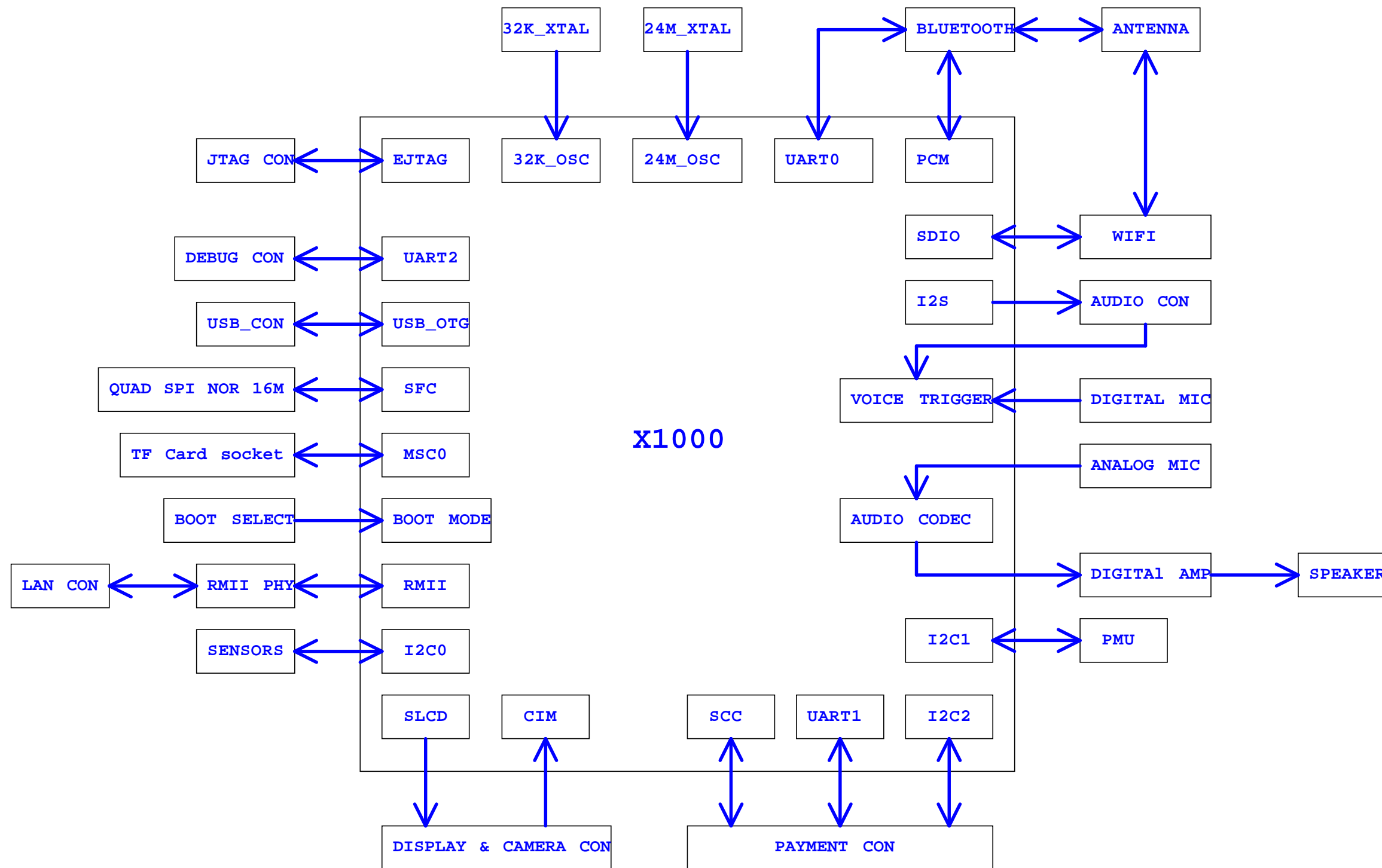




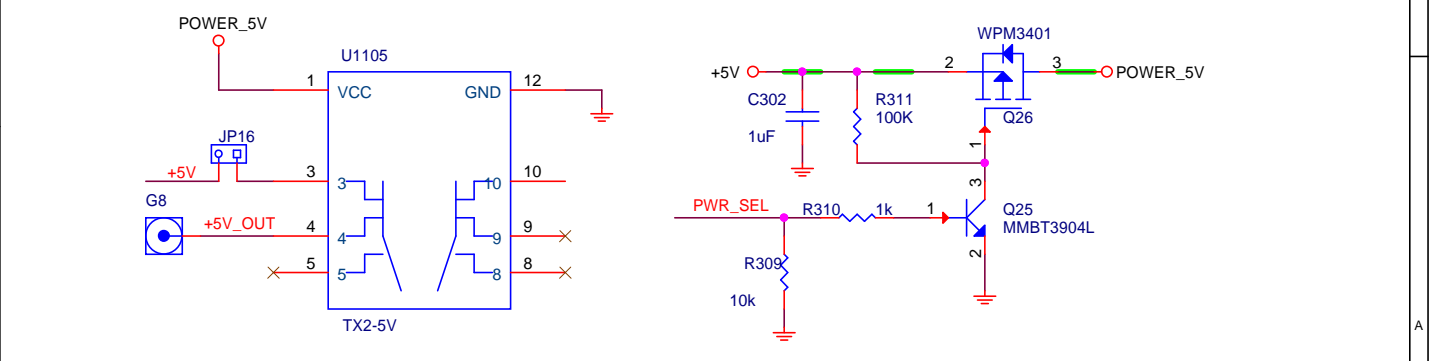
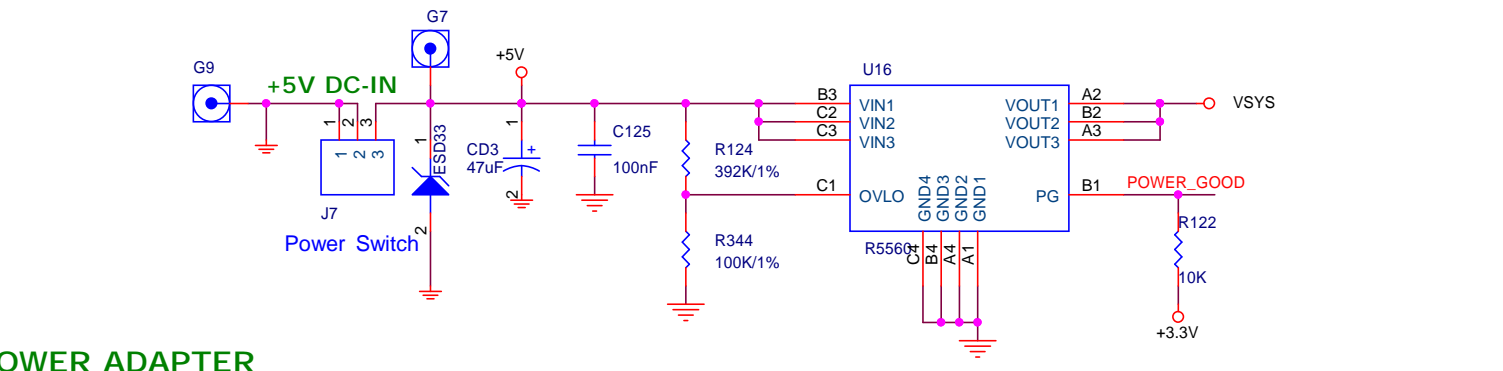
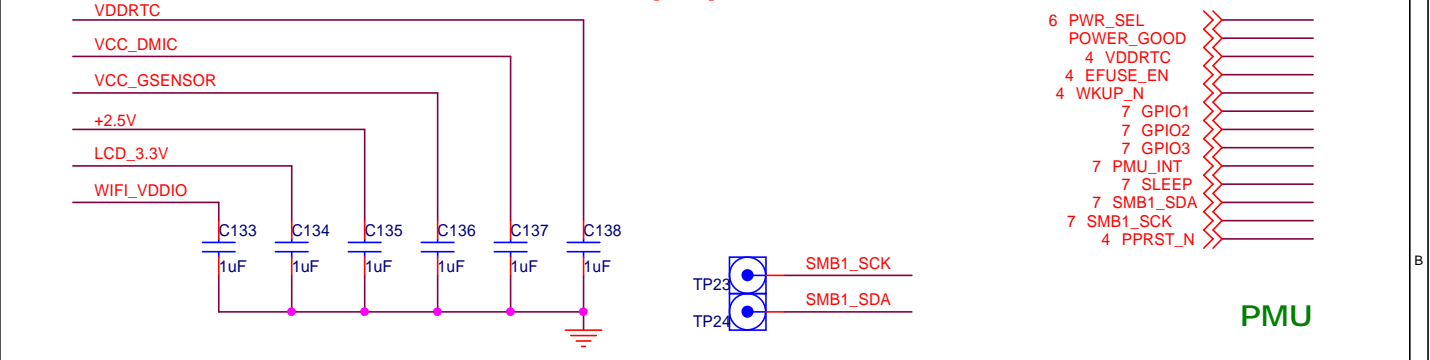
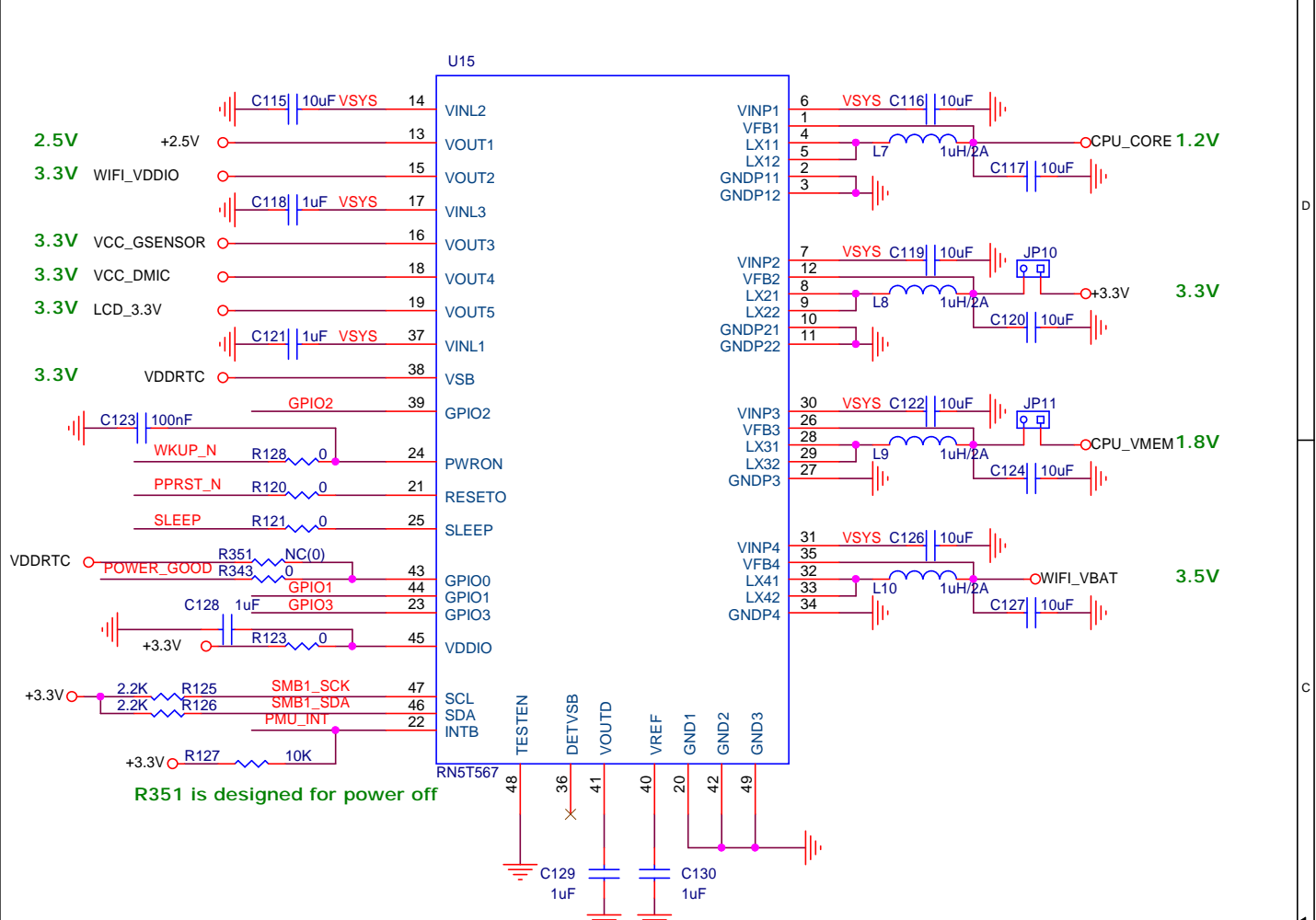
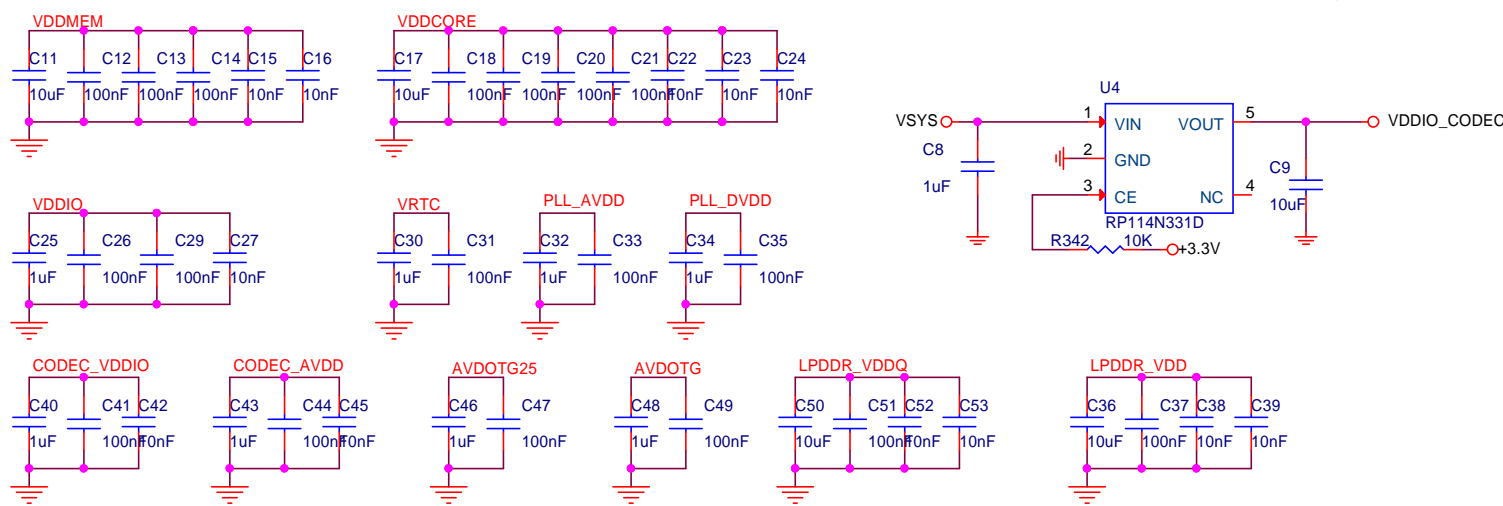
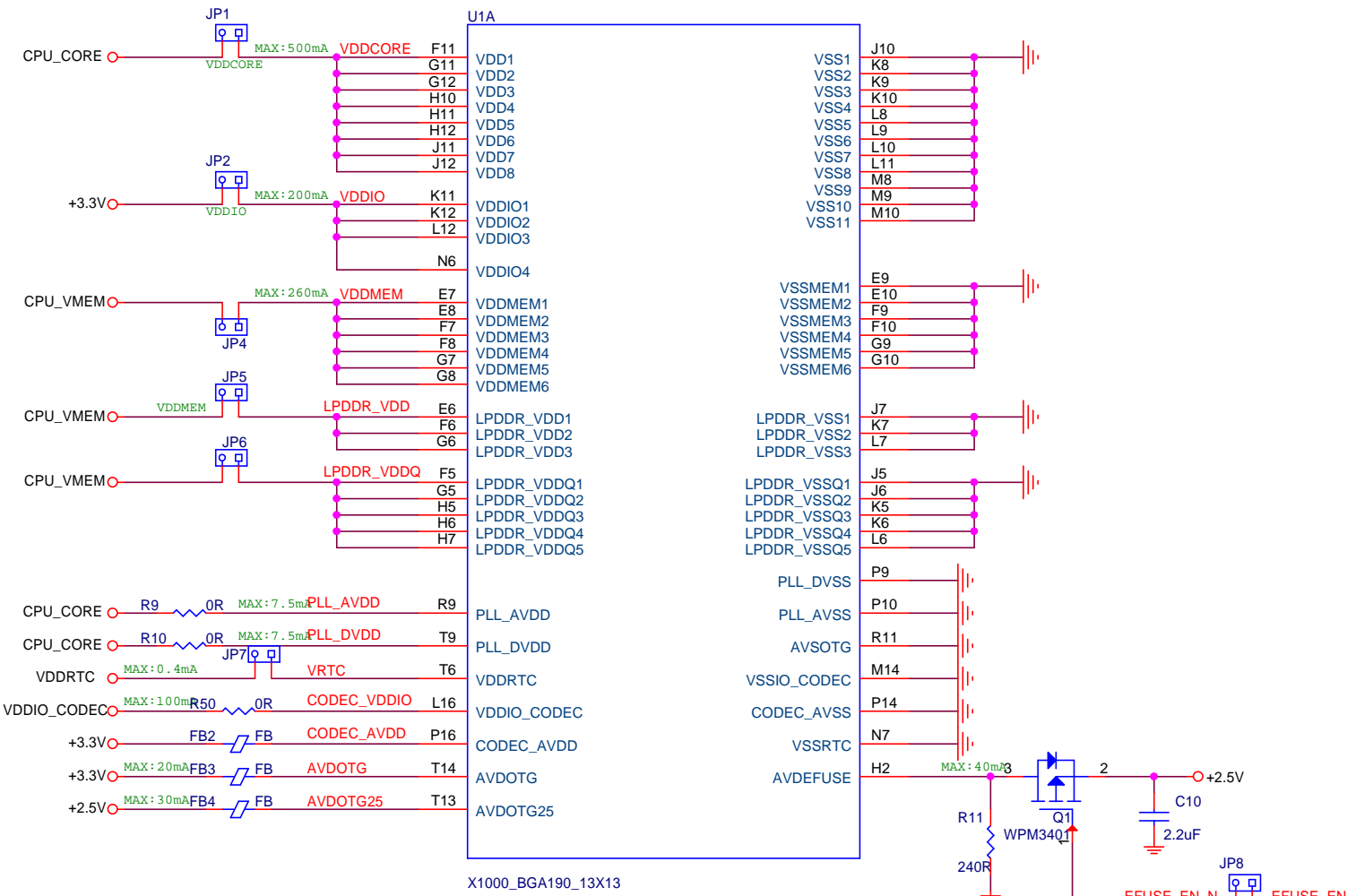
RD_X1000_PHOENIX_BOARD_V2.0.1

COVER SHEET/POWER ARCHITECTURE	1
SYSTEM ARCHITECTURE	2
CPU_PWR/PMU	3
BOOT/CLK	4
WIFI/BLUETOOTH	5
AUDIO	6
COMMON	7
RMII 10M/100M MAC	8
REVISION HISTORY	9

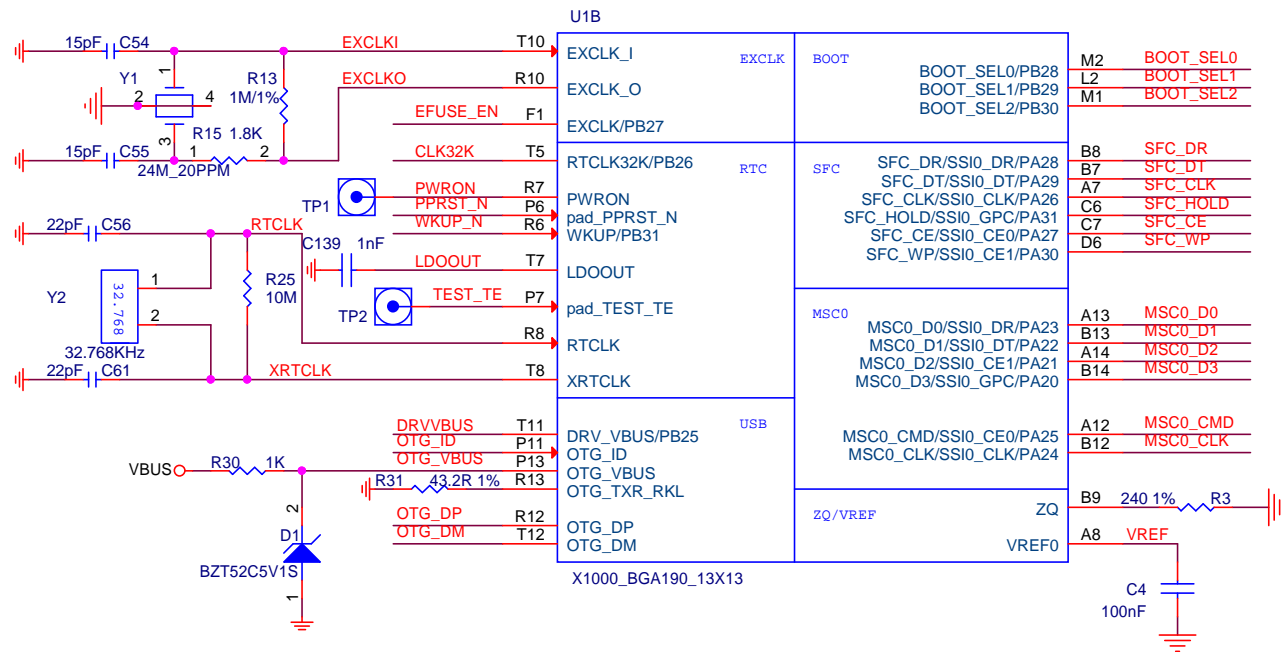




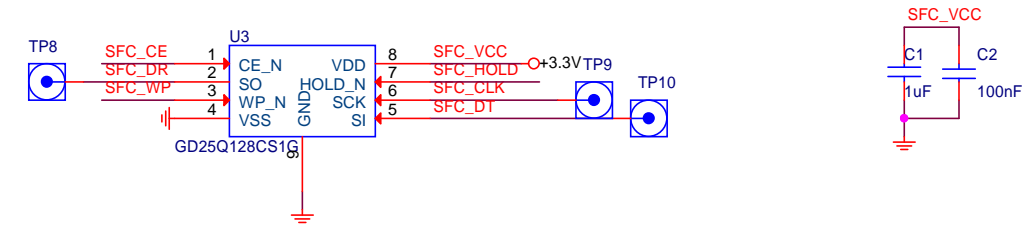
INGENIC SEMICONDUCTOR CO.,LTD		
Title RD_X1000_PHOENIX_BOARD		
Size A3	Document Number SYSTEM ARCHITECTURE	Rev 2.0.1
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Title	RD_X1000_PHOENIX_BOARD	
Size	Document Number	Rev
A3	CPU_PWR/PMU	2.0.1
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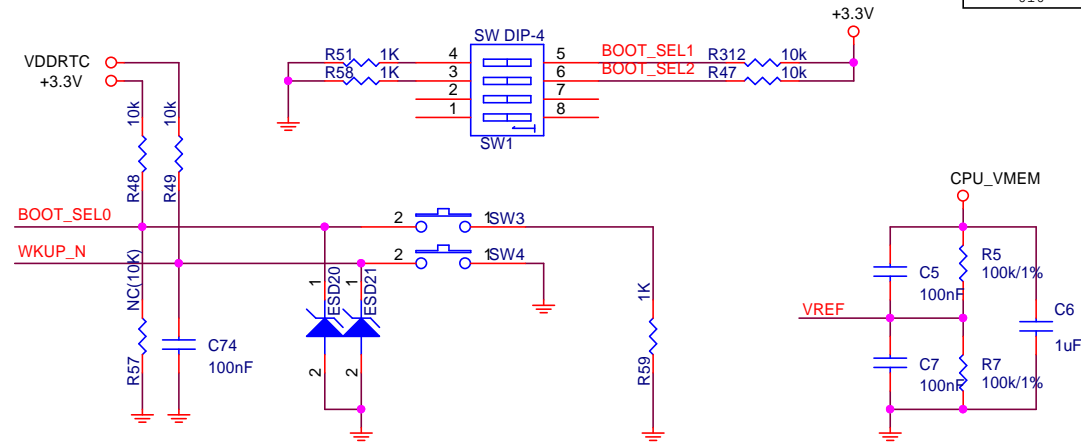
SFC FLASH



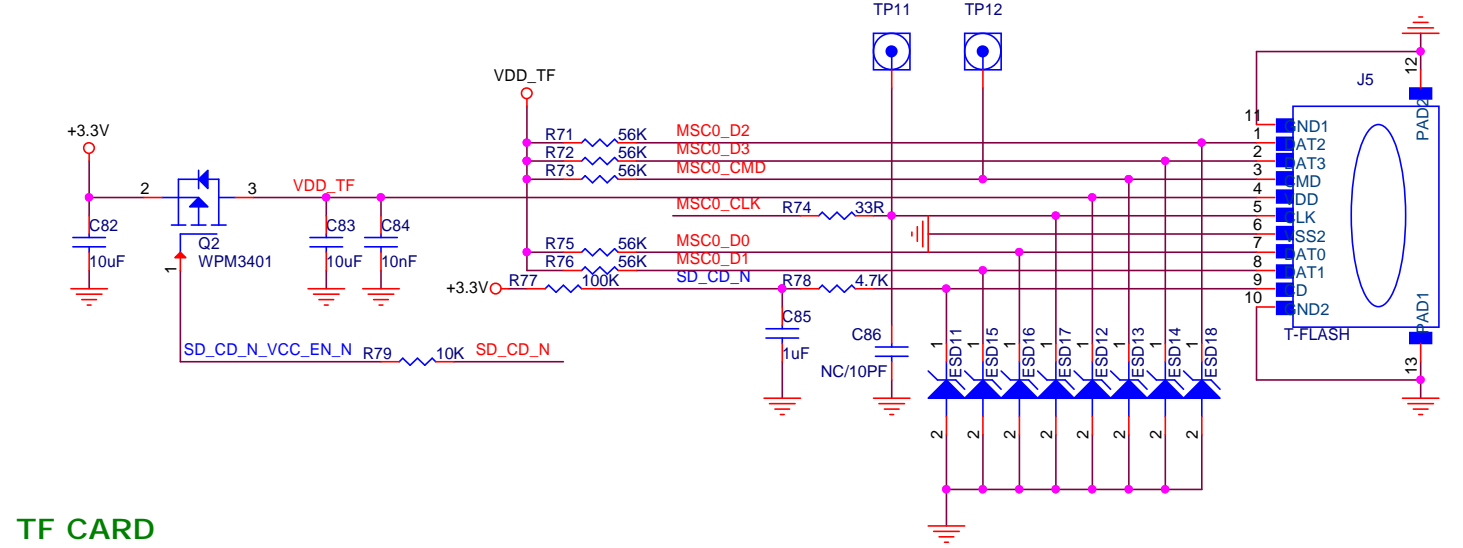
BOOT CFG & VREF

Boot Mode Select

BOOT_SEL[2:1:0]	BOOT FROM
111	SFC Boot @ 26M
011	SFC Boot @ 24M(default)
101	SD Boot @ 26M
001	SD Boot @ 24M
110	USB Boot @ 26M
010	USB Boot @ 24M



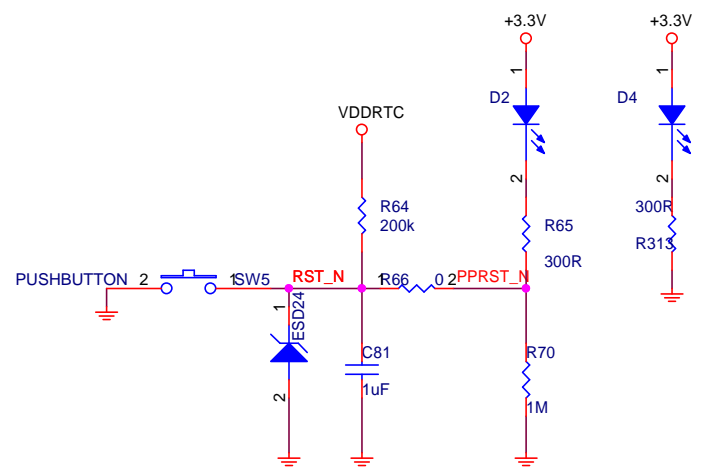
TF CARD



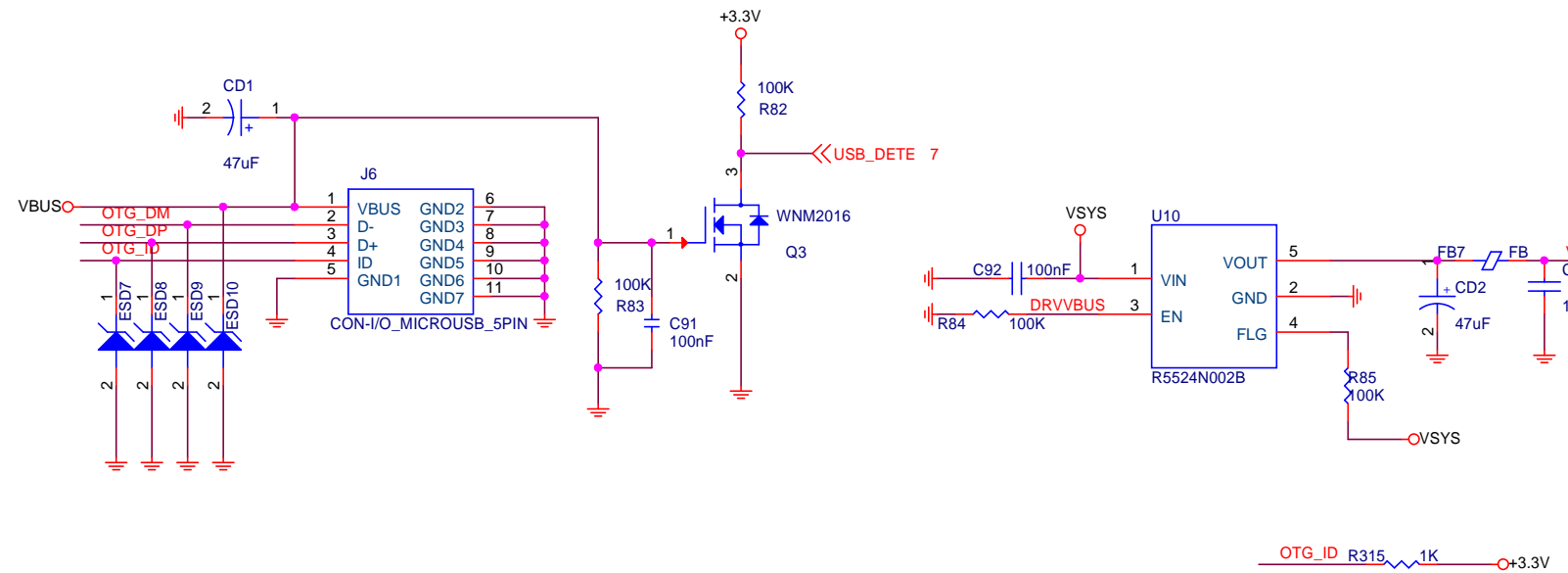
RESET & INDICATE

RESET LED

POWER LED



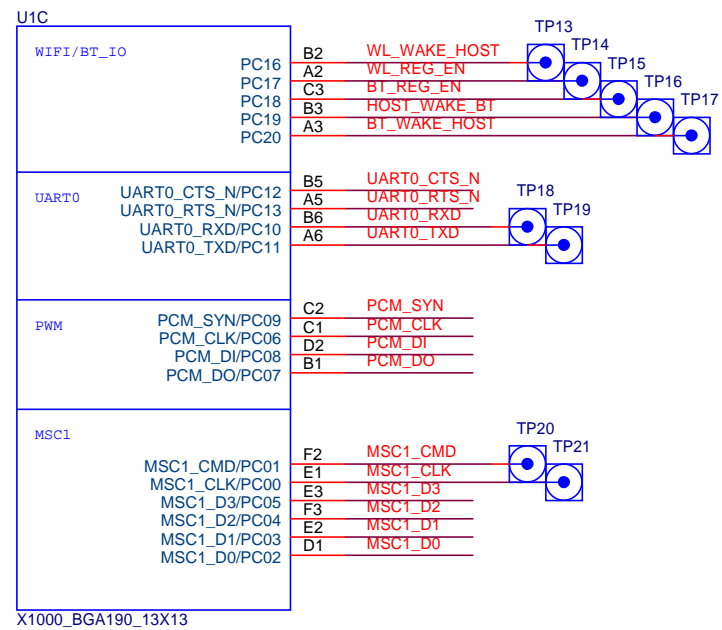
USB OTG



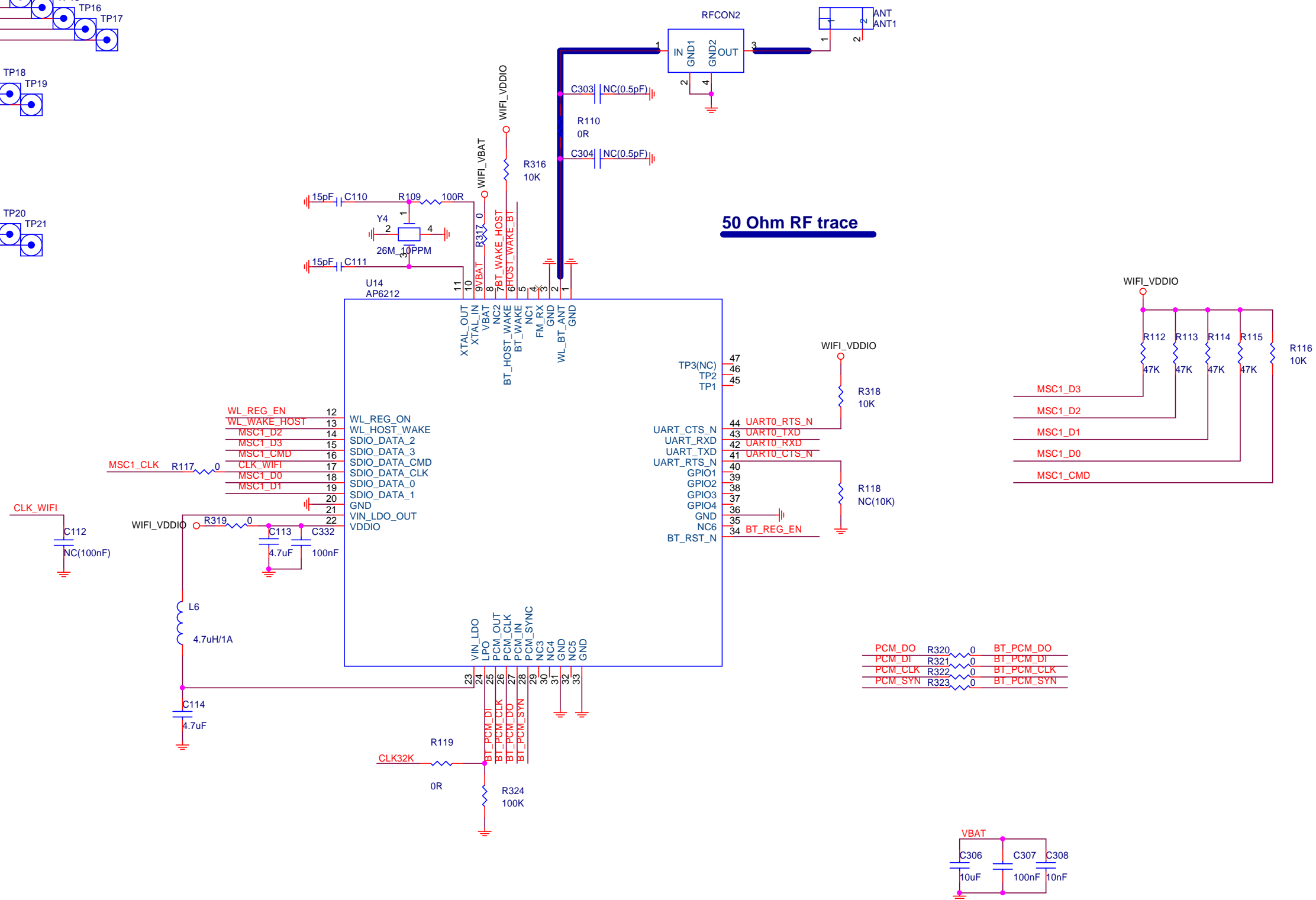
- 7 RST_N
- 7 SD_CD_N
- 3 WKUP_N
- 3 PPRST_N
- 5 CLK32K
- 3 VDDRTC
- 3,6 PWR_SEL
- 6 OTG_ID
- 3 EFUSE_EN

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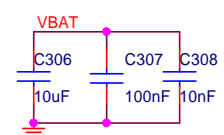
Title		RD_X1000_PHOENIX_BOARD
Size	Document Number	BOOT/CLK
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Date:	Wednesday, August 24, 2016	Sheet 4 of 9



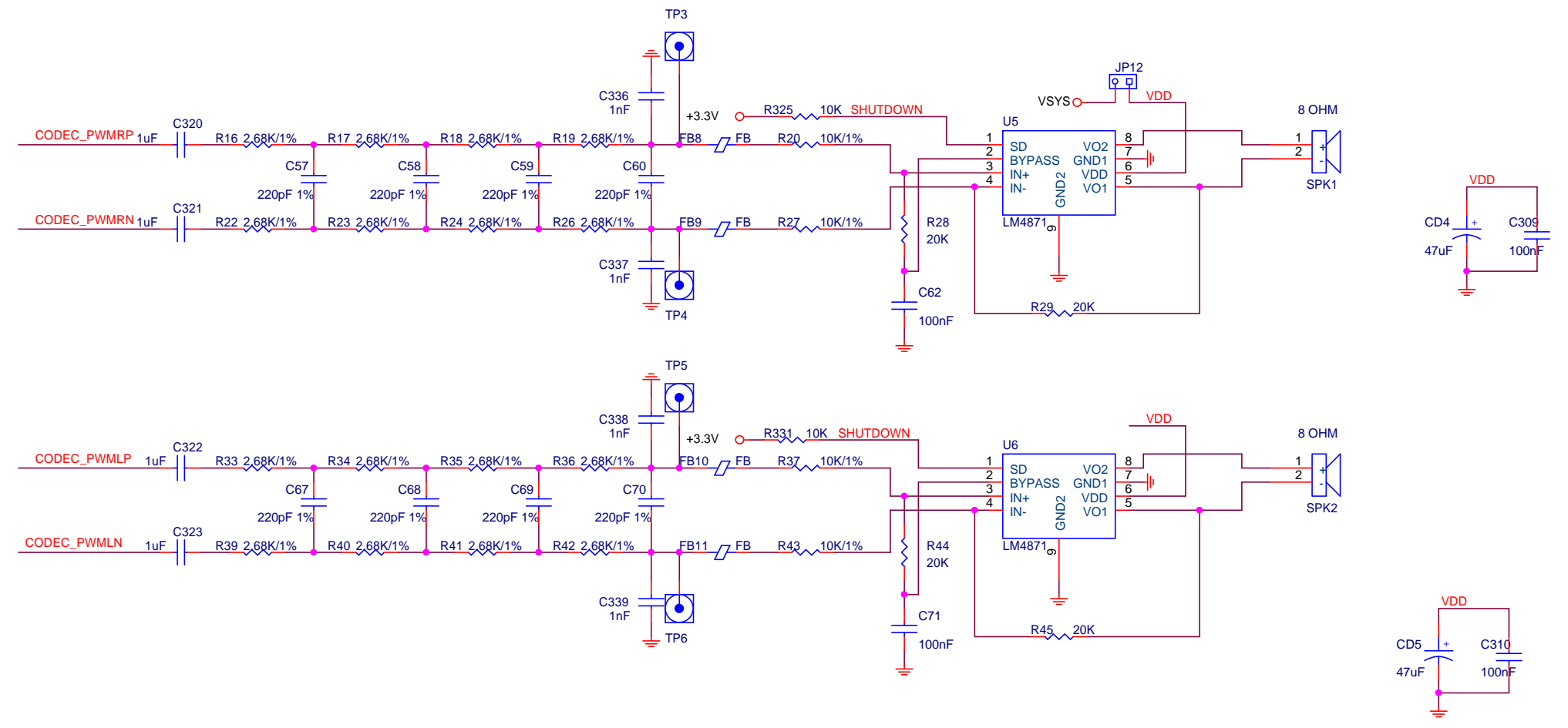
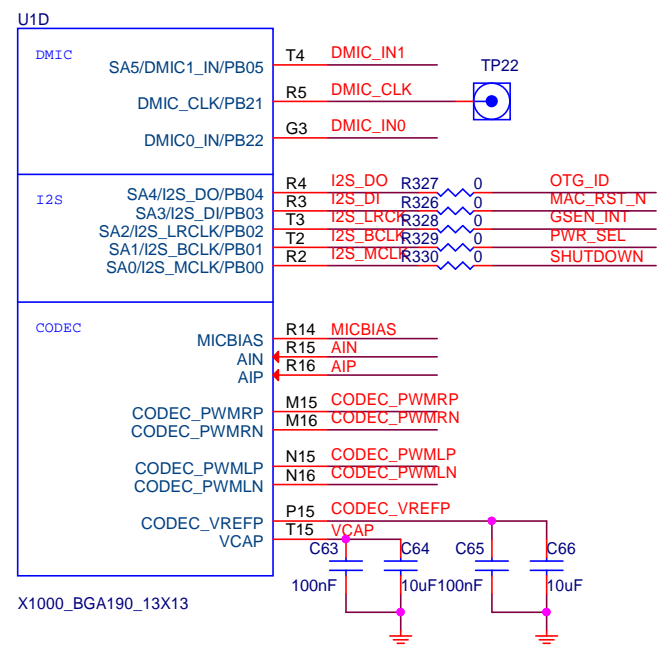
X1000_BGA190_13X13



PCM_DO	R320	0	BT_PCM_DO
PCM_DI	R321	0	BT_PCM_DI
PCM_CLK	R322	0	BT_PCM_CLK
PCM_SYN	R323	0	BT_PCM_SYN

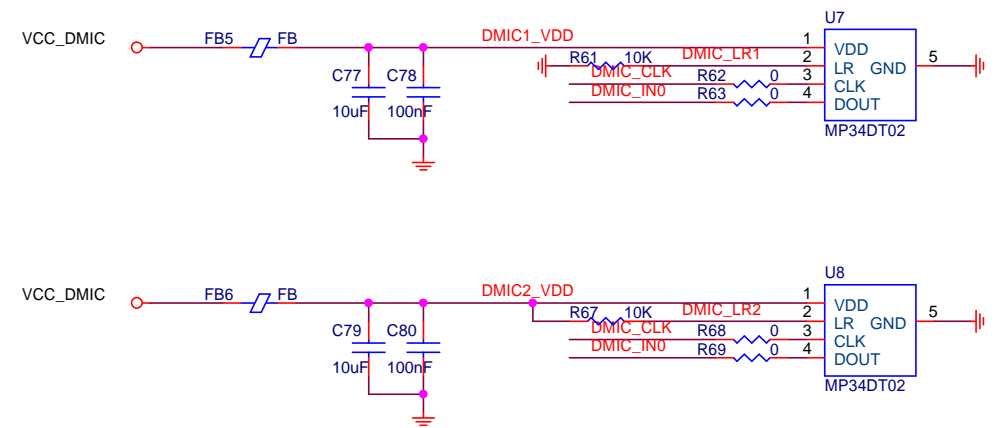


4 CLK32K >>>

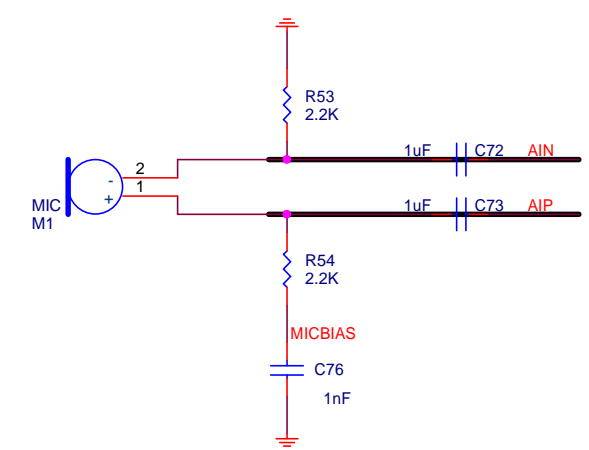


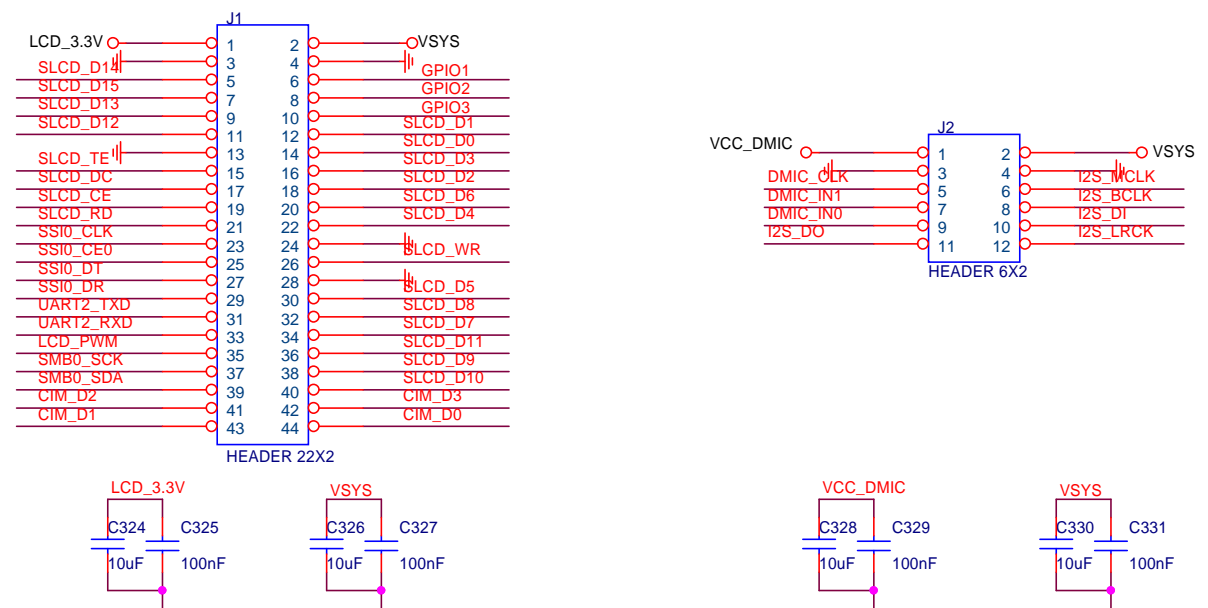
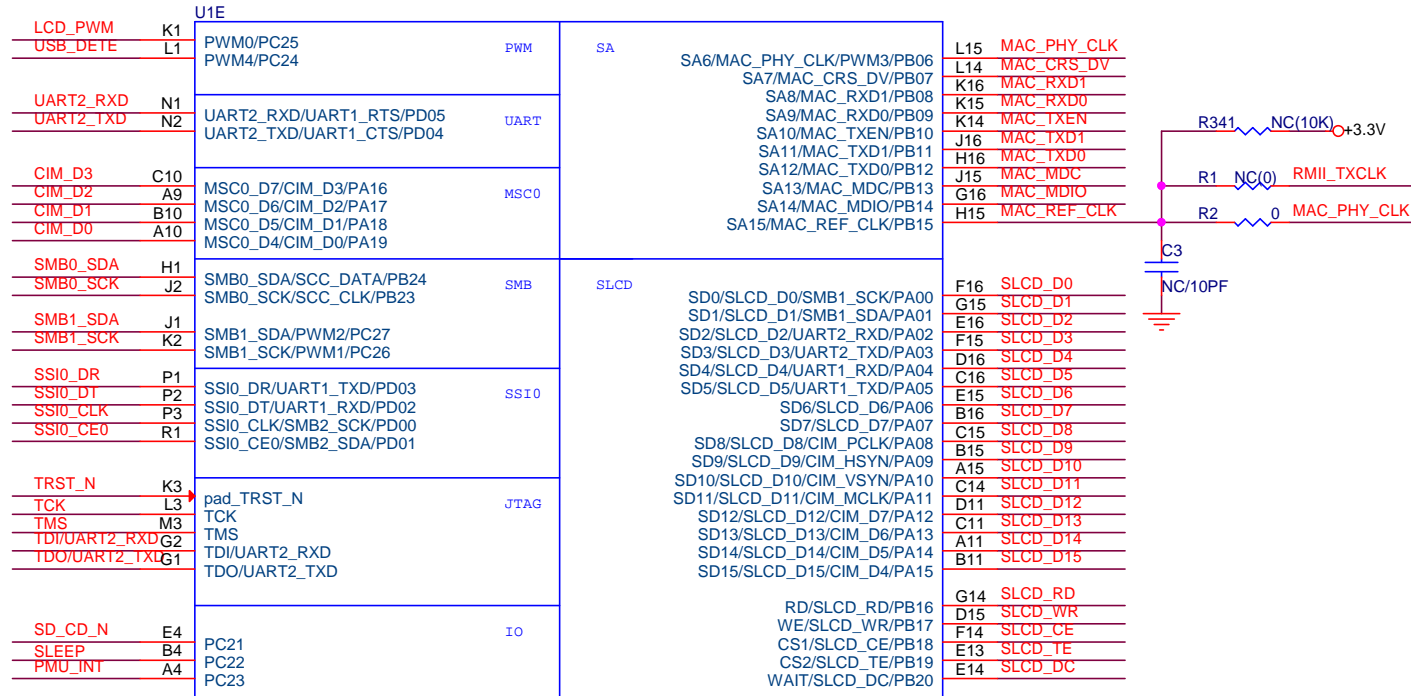
AUDIO AMPLIFIER

DMIC

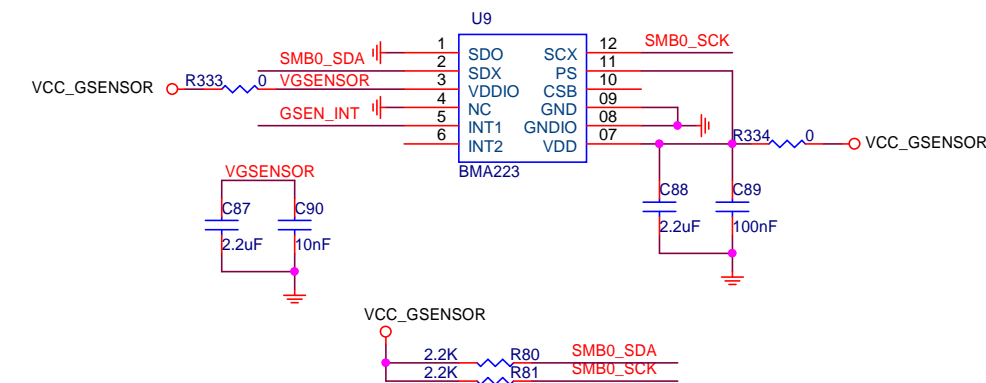
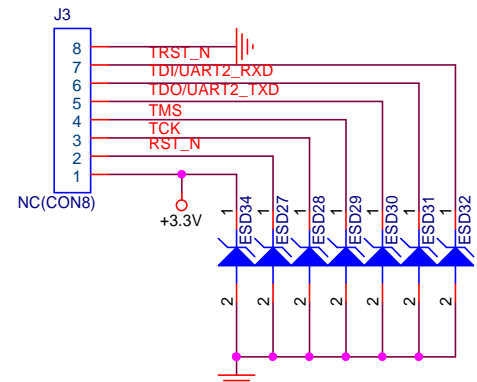


AMIC

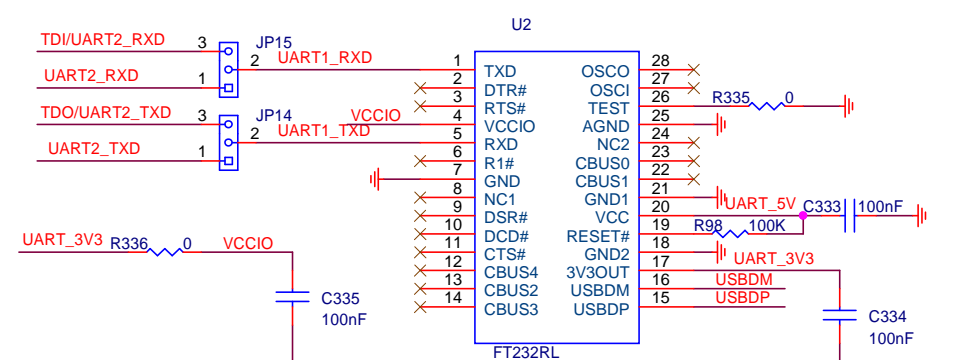




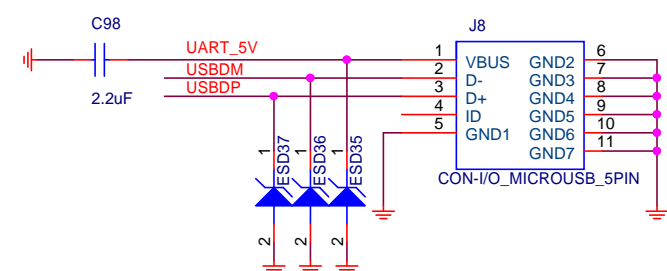
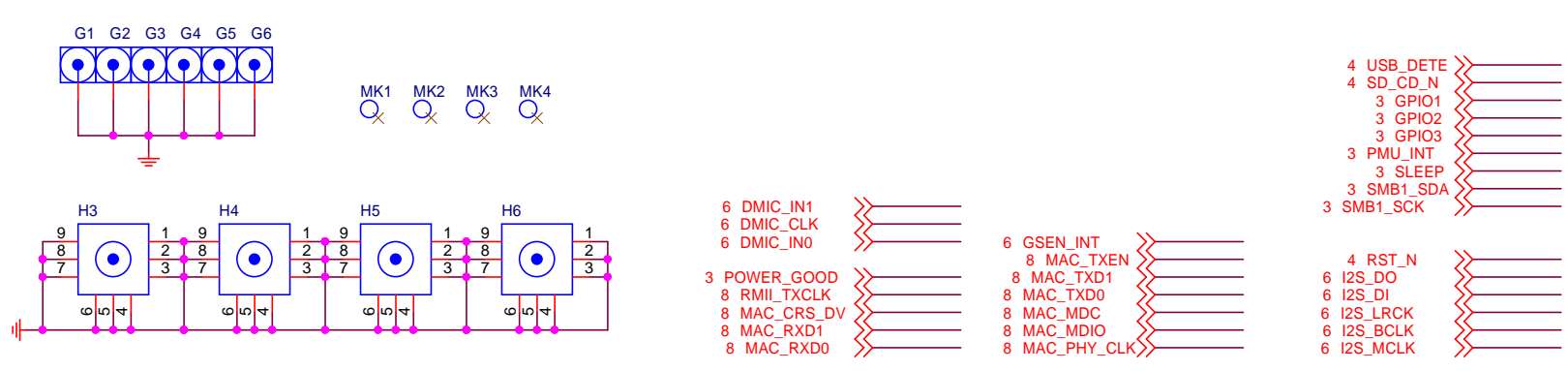
LCD & CIM & I2S



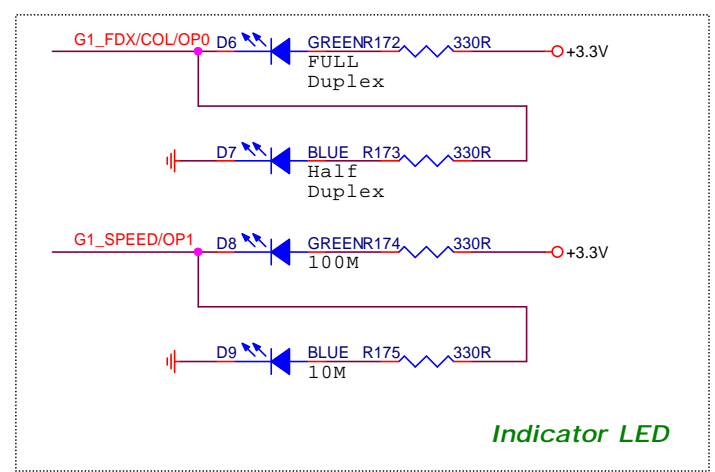
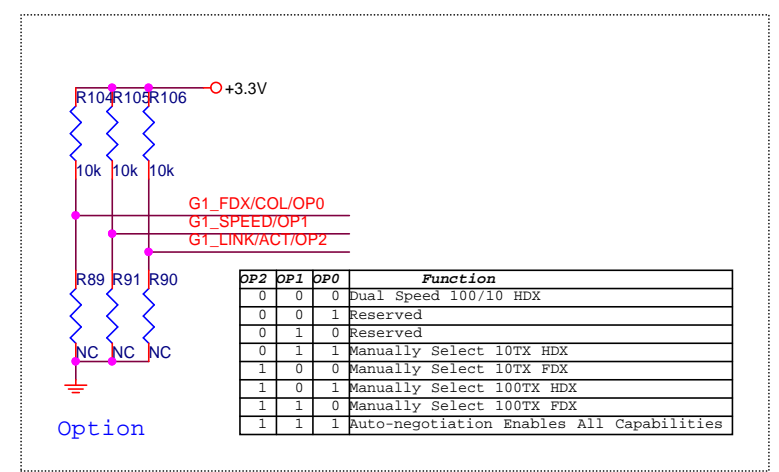
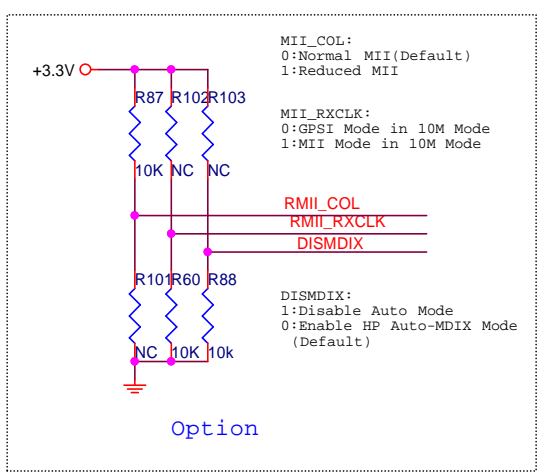
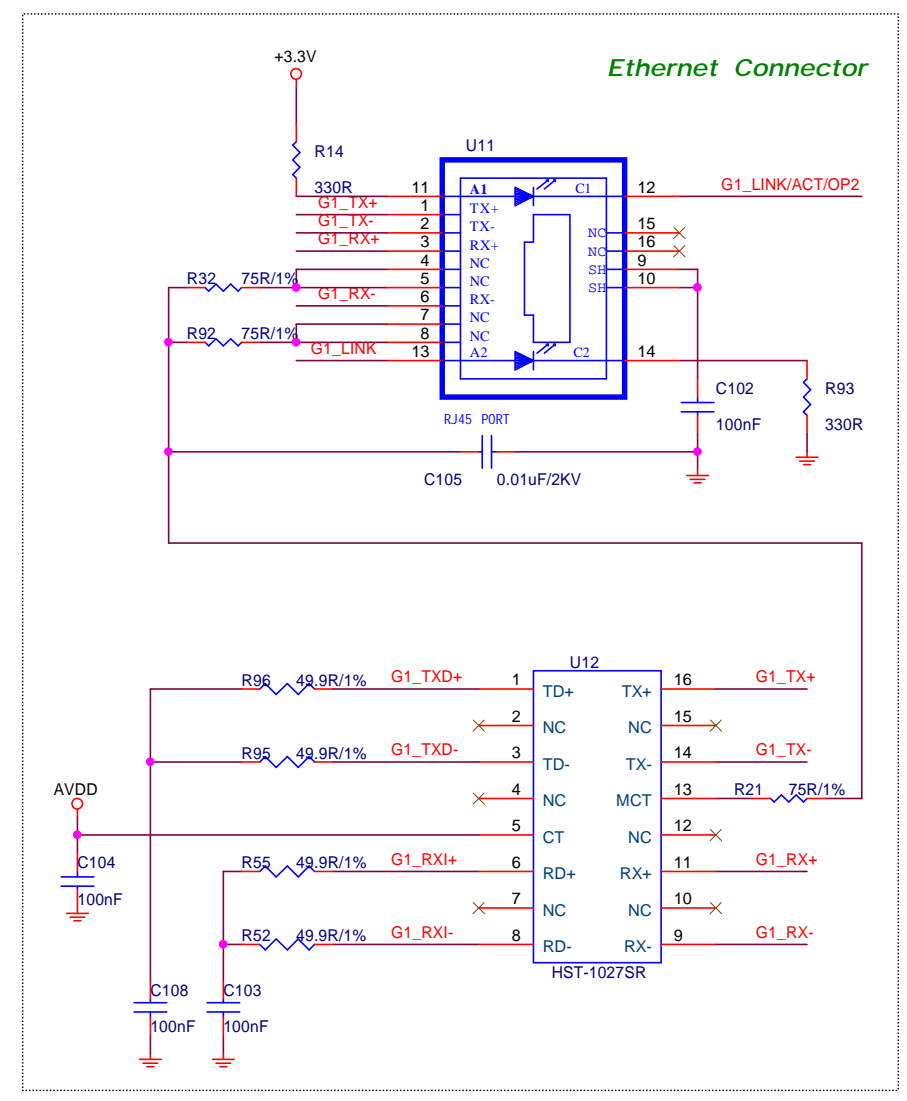
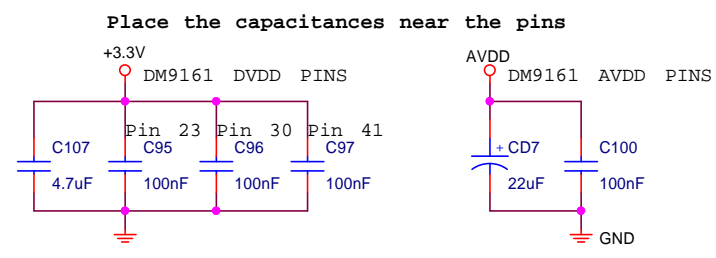
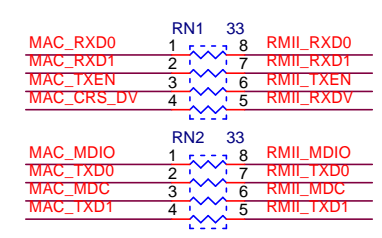
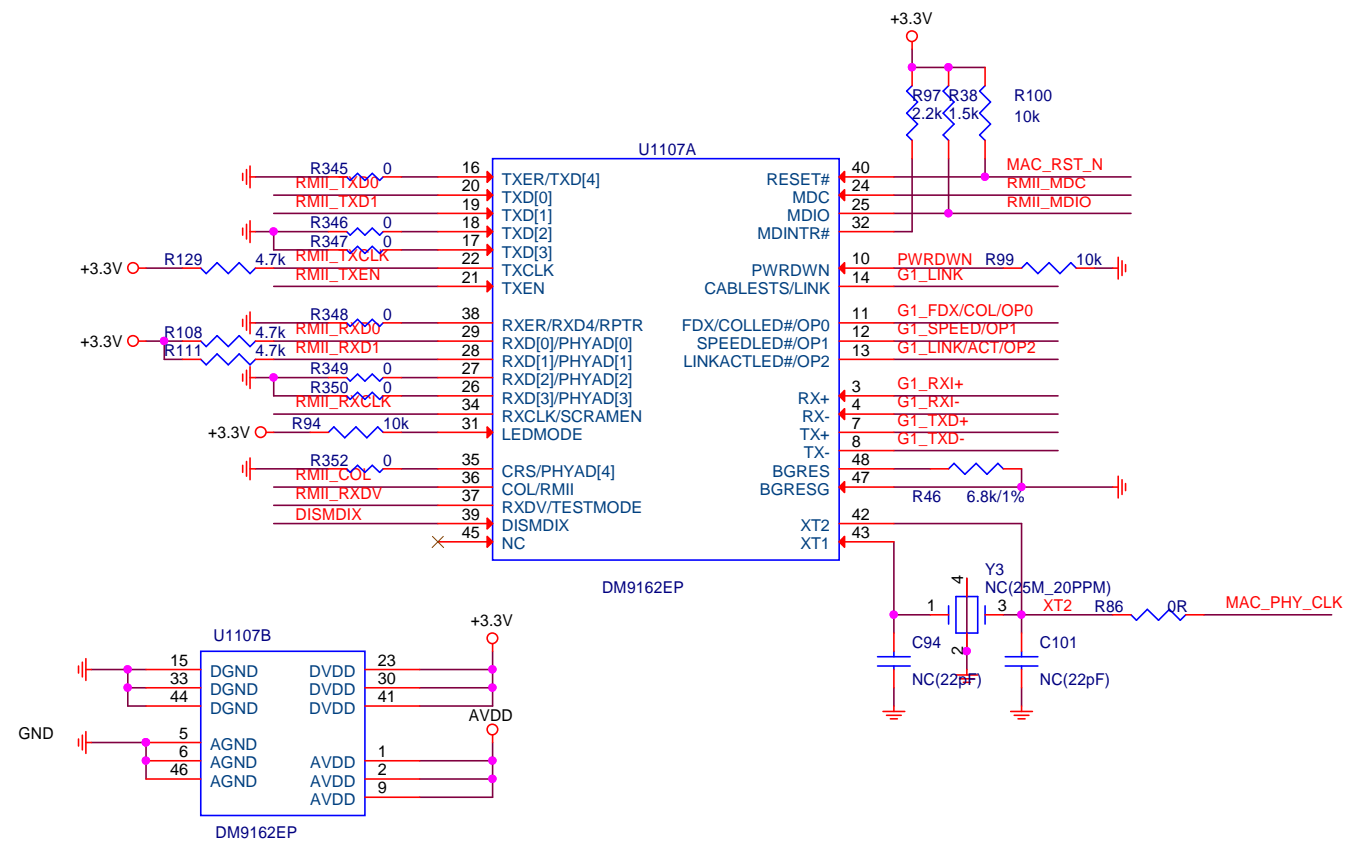
GSENSOR



USB to RS232(for debug only)



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- 7 MAC_PHY_CLK
- 7 RMII_TXCLK
- 6 MAC_RST_N
- 7 MAC_CRSDV
- 7 MAC_RXD1
- 7 MAC_RXD0
- 7 MAC_TXEN
- 7 MAC_TXD1
- 7 MAC_TXD0
- 7 MAC_MDC
- 7 MAC_MDIO

Data Revision Change

Apr 02 2014	Rev1.0	1. First Revision
Jul 24 2014	Rev2.0	1. PAGE08: Change MAC circuit from LAN8720 to DM9162 2. PAGE07: Change UART circuit to USB-RS232(FT232) 3. PAGE05: Delete C305 4. PAGE03: Add R351 for power on/off 5. PAGE04: Delete D3 and R314 6. PAGE03: Pull U4 CE to +3.3V
Aug 24 2016	Rev2.0.1	1. PAGE06: Add C336, C337, C338, C339.