inside SECURE Content Protection & Entertainment

HDCP SOLUTIONS SOFTWARE AND HARDWARE IP

Reducing the cost and complexity of protecting digital media for anytime, anywhere use

• features

- Comprehensive support for the HDCP standard
  - Efficient solutions for both compressed and uncompressed data streams
  - Support for HDCP 2.0, 2.1 and 2.2
  - Backward compatibility with HDCP 1.3, 1.4
- Configurable for designs using a Trusted Execution Environment (TEE)
- Hardware IP-based content protection for designs without a TEE

Exploding demand for anywhere, any device access to digital media

Digital media is fast becoming the preferred consumer entertainment choice. Whether in the home or on-the-go, the amount of premium High Definition audio/video and the number of devices that can be used to distribute and use it is growing exponentially.
Formerly stand-alone devices are now networked and repurposed as consumers are using storage drives and gaming consoles to store and serve content; while phones and tablets are augmenting television screens making multi-screen capabilities a must have feature.
To improve the user experience standards such as HDCP have been put in place to ensure the interoperability of these devices and to protect the content being purchased and consumed.

Accelerating the development of interoperable media devices both, wired and wireless

High-bandwidth Digital Content Protection (HDCP) is a method of protecting digital entertainment content such as high-definition movies, pay-per-view television or music on home and personal networks including devices such as PCs, tablets, smartphones and gaming devices. Licensed to device manufacturers by Digital Content Protection LLC (DCP), the initial 1.x versions of HDCP were mainly used over HDMI wired connections with great success, achieving over 3 billion implementations. As content distribution has moved to phones and tablets and key leakage vulnerabilities were found, the HDCP standard has evolved to keep pace, with 2.x versions that protect TCP/ IP based connections across an array of wired and wireless interfaces and provide greater key protection.
HDCP combines the need for managing and performing advanced cryptographic functions, incorporating authentication, digital signature algorithms, key storage and management all in accordance with the specified standard. The development of this type of encryption engine and management software requires expertise in cryptography, digital rights management, hardware and software design, a level of security specialization best handled by experts and which can lead to long development cycles for those looking to implement HDCP internally without the required expertise.

Inside Secure's solutions offer alternative methods for implementing an HDCP solution:

- Designs using a Trusted Execution Environment (TEE): As part of the HDCP license, an integrator agrees to certain rules, including the use of hardware protection for storing secret keys and for implementing the cryptographic functions. A TEE is considered to provide hardware based protection; Inside Secure provides a software solution, operating within the TEE, which implements all the functions of the HDCP protocol. Hardware acceleration options are also available to enhance the TEE-based solution in cases where higher performance or more CPU offloading are required.
- Designs without a TEE: When a TEE is not part of the system design, Inside Secure delivers a solution with all the HDCP content protection functions implemented in a highly secure hardware IP module.

Both approaches significantly reduce the cost and complexity of security solutions while helping designers get to market quickly with HDCP compliant, robust cryptographic content protection across a range of architectures and use cases.

Memberships and partnerships

DCP · GlobalPlatform · SOLACIA · TrustZone System Security by ARM · VESA · TRUSTONIC

Optimized for mobile to support the growing number and type of mobile devices

Multi-screen viewing and the ability to view premium content on a mobile device has become the expected standard rather than the exception. Given this expectation and the need to protect content, which invariably relies on encryption, device manufactures are faced with a challenge of meeting the requirements of content providers and delivering a positive consumer experience. This challenge arises because power consumption on mobile devices is at a premium and encryption typically is power intensive, as a consequence deploying an optimized solution is key. INSIDE Secure's HDCP solutions are optimized to reduce power consumption and also offers the ability to off-load processing to hardware thus greatly reducing the power used in the HDCP media consumption.

INSIDE Secure's HDCP Software Solution

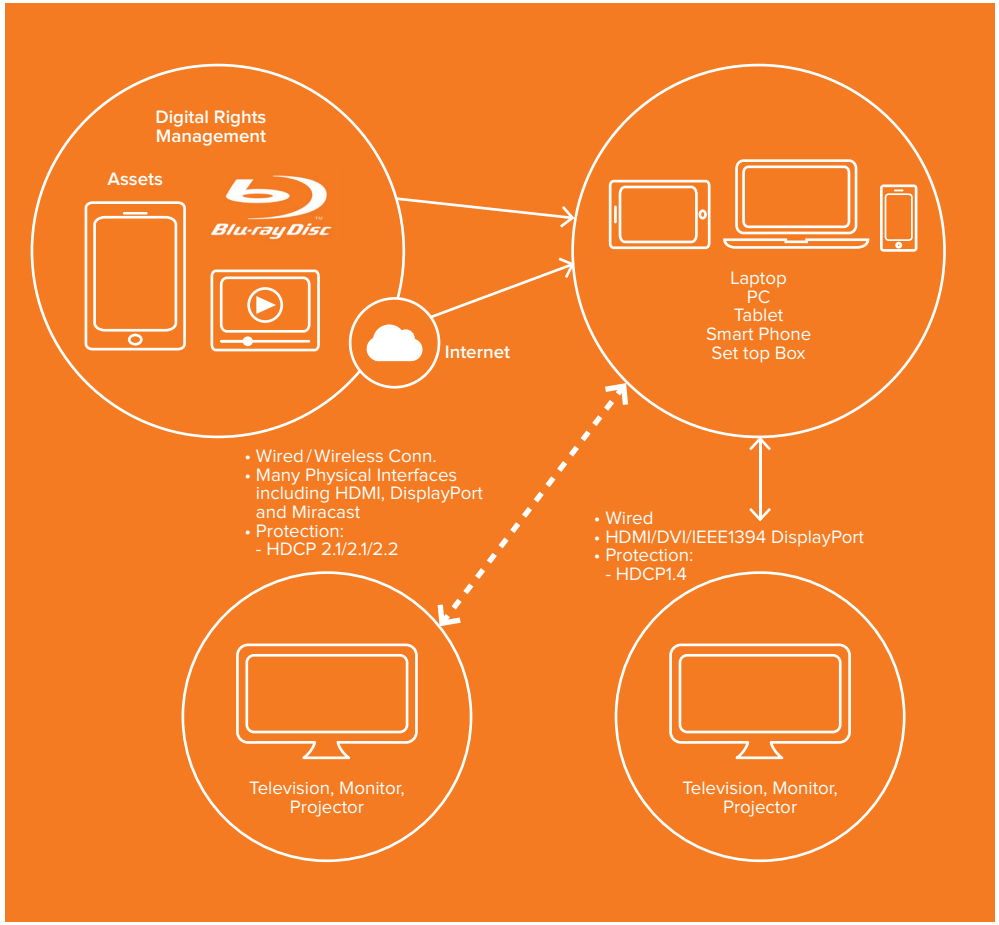
The Inside Secure HDCP2.2 High-bandwidth Digital Content Protection software solution provides all the required features for a complete content protection solution and includes all control and management software for the HDCP2.2 specification. It is fully backwards compatible with the earlier versions: HDCP2.1 and HDCP2.0.

The HDCP software, without hardware acceleration, is sufficient in cases where a TEE is available and the content is in a compressed data stream. In this situation, very high performance is not a requirement.

For situations where a TEE is available but using an uncompressed video protocol (for example, HDMI or DisplayPort), the HDCP software needs to access the EIP-114 Datapath Engine, an AES cypher IP core which delivers the required level of high-bandwidth performance. This module implements the HDCP 1.4 and HDCP 2.x data plane in hardware. It is designed for integration with a TEE and must be located within the security boundary of the processor.

The HDCP software also includes specific API's for signaling the HDCP protection status to a higher level content control function like DRM, and can be used in combination with Inside Secure's DRM PlayReady and Widevine software solutions to implement a complete end-to-end content protection solution.

Secure Distribution of High-value Digital Content



INSIDE Secure's HDCP Hardware IP Solution

For implementations that do not include a TEE, the EIP-115 Hardware Security Module is available. This security module provides all the required technology for implementing a secure HDCP2.2 content protection solution. It includes functions like Secure Key Storage, all cryptographic computations and AES based ciphering as defined in the HDCP2.2 specifications. The EIP-114 IP module is offered for systems with at TEE that need to support uncompressed content with HDCP1.4/2.x protection. The EIP-114 module includes a data plane only implementation, where the EIP-115 implements both the HDCP control plane and the data plane for compressed streaming interfaces like DLNA and Miracast.

Both the EIP-114 and the EIP-115 modules include an AES-128 based cipher engine for encrypting or decrypting the content stream. The EIP-115 also provides all the cryptographic functions for Authentication, Key Exchange, Locality Check and certificate verification. In addition to a very high level of security the EIP-115 module offers significant performance improvements and reduced power consumption compared to a software only implementation.

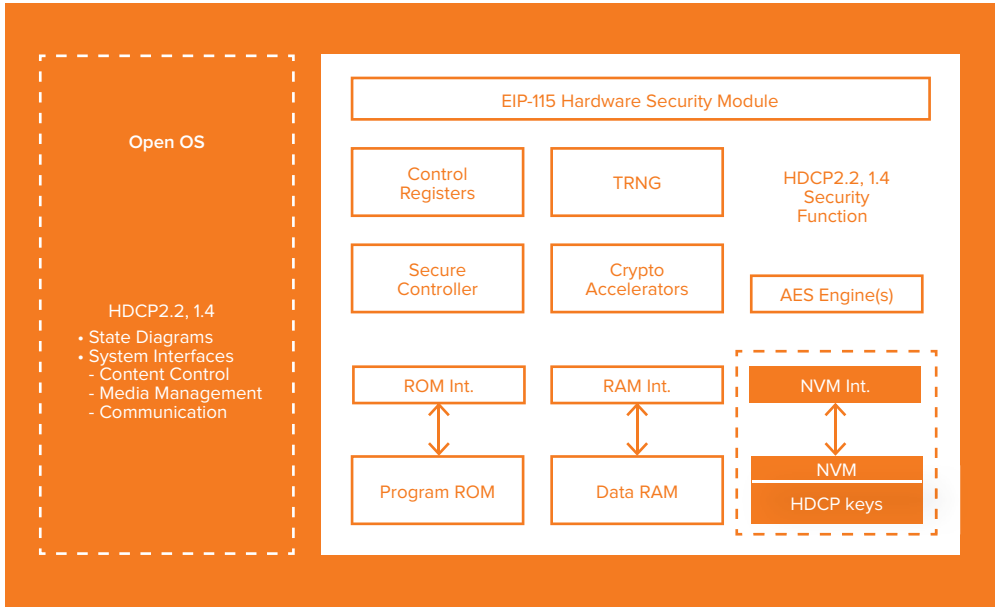
The EIP-115 module includes a secure interface to Non-Volatile Memory (NVM) for storing and retrieving the HDCP2.2 secure keys which must be programmed during the manufacturing of the device The EIP-115 hardware security module can be integrated into a wide range of semiconductors, including Application Processors, Multimedia Processors, SOC's for Settop Boxes and Graphics Processors. The EIP-115 generates session keys and input vectors which are then used by the AES-128 based cipher core within the module. EIP-115 supports a variety of interfaces, including USB, WiFi and Ethernet for streaming compressed video. In addition, for systems without a TEE EIP-115 can be used as both the control plane and data plane security module for the protection of streaming un-compressed video over HDMI and DisplayPort.

Backward Compatibility

Inside Secure's HDCP solutions, both software and hardware, support HCDP 2.0, 2.1 and 2.2. They also support the older HCDP 1.4, often used with Display Port or HDMI. However, the ciphers and protocol definitions are totally different between HDCP 1.4 and HDCP 2.x.

	HDCP 1.4	HDCP 2.2
Symmetric Crypto Algorithms	HDCP Block Cipher	AES
Asymmetric Crypto Algorithms	RSASSA-PKCS1v5	RSASSA-PKCS1v5
Hash and HMAC Algorithms	SHA1	SHA2-256 / HMAC-SHA256

HDCP Functional Diagram



HDCP Functional Diagram

Interface	Wired/Wireless	HDCP version
HDMI/DVI/HDBASE-T	Wired	HDCP 1.4/2.2
MHL	Wired	HDCP 1.4/2.2
DisplayPort	Wired	HDCP 1.4/2.2
USB	Wired	HDCP 2.2/2.1/2.0
Ethernet	Wired	HDCP 2.2/2.1/2.0
Diiva	Wired	HDCP 2.0
Miracast (WiFi Display)	Wireless	HDCP 2.2/2.1/2.0
Bluetooth	Wireless	HDCP 2.2/2.1/2.0
WiFi	Wireless	HDCP 2.2/2.1/2.0
WiDi	Wireless	HDCP 2.2/2.1/2.0
WiGig	Wireless	HDCP 2.0
WHDI	Wireless	HDCP 2.0
WirelessHD	Wireless	HDCP 2.0/2.2

HDCP SOFTWARE SOLUTION

• Security Functions

Inside Secure's HDCP module (implemented in software or hardware) supports the security functions as defined in the HDCP 2.2 protocol, including:

- **Master key, session key and nonce generation**
  - NIST SP-800-90 compliant random number generation
- **Authentication and Key Exchange**
  - Generation of random numbers for r_{tx} and r_{rx}
  - Signature verification of $cert_{rx}$ using $kpub_{dcp}$
  - 3072-bit RSASSA-PKCS#1 v1.5
  - RSAES-OAEP (PKCS#1 v2.1) encrypt/decrypt
  - Derivation of kd using AES Counter mode
  - Computation and verification of H and H'
  - Computation and verification of V and V'
  - Pairing support (optional)
- **System Renewability (SRM)**
  - SRM signature verification using $kpub_{dcp}$
  - 3072-bit RSASSA-PKCS#1 v1.5
- **Session Key Exchange**
  - Generation and computation of ks and r_{iv}
  - Derivation of dkey2 using AES Counter mode
- **Locality Check**
  - Computation and verification of L and L'
  - Generation of nonce r_n
- **Stream Management**
  - AES Counter mode based HDCP 2.2 key stream generation

• Secure access to confidential material

- **Protected access for confidential parameters and key material such as private keys and session keys, as required by the robustness rules**

• Cryptographic functions

- **Symmetric crypto algorithms**
  - AES CTR mode with a key length of 128 bits
- **Asymmetric crypto algorithms**
  - RSA-CRT - with a modulus length of 512 bits
  - RSA - with modulus lengths of 1024 and 3072 bits
- **Hash and HMAC algorithms**
  - SHA-256
  - HMAC-SHA-256
- **True Random Number Generator**
  - Hardware-based, Non-deterministic Random Number Generator
  - Full digital implementation so no specific analog de sign is required
  - NIST SP 800-90 compliant

HARDWARE IP SPECIFICATIONS

• The EIP-114 HDCPv1.4 & v2.2 Datapath Engine

- **Description**

The EIP-114 Datapath Engine provides all the high-performance hardware encryption required for providing HDCP1.4 and HDCP2.2 streaming content protection for high-speed uncompressed interfaces like DisplayPort and HDMI. This module works seamlessly together with the Inside Secure HDCP protocol software running on a Trusted Execution Environment or in combination with the EIP-115 Hardware Security Module

• Hardware Configurations and gate count

The EIP-114 Hardware based Datapath engine is available in one configuration for integration with 1-4 lane Displayport interface

- 94k gates with TCM interface in TSMC 65nm: 28.8 Gb/s at 450MHz max frequency
- 97k gates with TCM interface in TSMC 40nm: 35.2 Gb/s at 550MHz max frequency
- 95k gates with TCM interface in TSMC 28nm: 32.4 Gb/s at 600MHz max frequency

• The EIP-115 Hardware Security Module

- **Description**

The EIP-115 Security Module includes all the secure components like AKE, LC, SRM, SKE, Key Storage, required for implementing the HDCP protocol as specified by DCP, LLC. This module is an ideal solution to be integrated into SoC's that do not include a Trusted Execution Environment

- **Hardware Configurations and gate count**

The EIP-115 Hardware based security module is available in two different configurations

- **EIP-115a Low gate count configuration:** 35k gates TCM in TSMC 40nm at 150MHz and with an AES-128 performance of up-to 2.4Gbps at 600MHz
- **EIP-115b High performance configuration:** 81k gates in TSMC 40nm at 150MHz and with an AES-128 performance of-up to 23Gbps at 600MHz

• Performance (HDCP2.2)

- **Authentication protocol – Transmitter (@150MHz):**
  - Verify certrx <3ms
  - RSAES-OAEP encrypt <2ms
  - Verify SRM Signature <11ms
  - Compute H <0.4ms
  - Compute L <0.4ms
- **Authentication protocol – Receiver (@150MHz):**
  - RSAES-OAEP decrypt <27ms
  - Compute H' <0.4ms
  - Compute L' <0.4ms
- **Pairing:**
  - Encrypt km <0.6ms
  - Decrypt km <0.6ms
- **Key stream generation:**
  - EIP-115a 4 bits/clock
  - EIP-115b 38.4 bits/clock

• Interfaces

- **The EIP-115 has a single 32-bit Host Slave Interface, available with the following bus interface types:**
  - TCM interface
  - AHB interface
  - AXI interface
- **NVM Interface:**
  - Generic memory interface for easy integration of Non-Volatile Memories

• Tools

- **Hardware Documentation Set:**
  - Hardware Reference Manual
  - Programmer Manual
  - Verification Specification
  - Integration Manual
- **NVM Image Tool for NVM content management:**
  - NV M Image Tool User Guide

For further details on all of INSIDE's security solutions, visit www.insidesecure.com

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