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Power Quality Analyser

UMG 605US

Modbus address list and Formulary

(Firmware 1.268)

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Modbus

Modbus functions (Master)

As a master, the UMG605US supports the following modbus functions:

01 Read Coil Status

Reads the ON/OFF status of discrete outputs (0X references, coils) in the slave. Broadcast is not supported.

02 Read Input Status

Reads the ON/OFF status of discrete inputs (0X references) in the slave. Broadcast is not supported.

03 Read Holding Registers

Reads the binary contents of holding registers (4X references) in the slave.

04 Read Input Registers

Reads the binary contents of input registers (3X references) in the slave.

05 Force Single Coil

Forces a single coil (0X references) to either ON or OFF. When broadcast, the function forces the same coil reference in all attached slaves.

06 Preset Single Register

Presets a value into a single holding register (4X reference). When broadcast, the function presets the same register reference in all attached slaves.

15 (0F Hex) Force Multiple Coils

Forces each coil (0X references) in a sequence of coils to either ON or OFF. When broadcast, the function forces the same coil reference in all attached slaves.

16 (10Hex) Preset Multiple Registers

Presets values into a sequence of holding registers (4X references). When broadcast, the function presets the same register references in all attached slaves.

23 (17Hex) Read/Write 4X Registers

Performs a combination of one read and one write operation in a single Modbus transaction. The function can write new contents to a group of 4XXXX registers, and then return the contents of another group of 4XXXX registers. Broadcast is not supported.

Modbus Functions (Slave)

As a slave, the UMG605US supports the following modbus functions:

03 Read Holding Registers

Reads the binary contents of holding registers (4X references) in the slave.

04 Read Input Registers

Reads the binary contents of input registers (3X references) in the slave.

06 Preset Single Register

Presets a value into a single holding register (4X reference). When broadcast, the function presets the same register reference in all attached slaves.

16 (10Hex) Preset Multiple Registers

Presets values into a sequence of holding registers (4X references). When broadcast, the function presets the same register references in all attached slaves.

23 (17Hex) Read/Write 4X Registers

Performs a combination of one read and one write operation in a single Modbus transaction. The function can write new contents to a group of 4XXXX registers, and then return the contents of another group of 4XXXX registers. Broadcast is not supported.

Transfer parameters

The UMG605US supports the following transfer parameters:

Baud rate	: 9.6kbps, 19.2kbps, 38.4kbps, 57.6kbps, 115.2 kbps and 921.6 kbps
Data bits	: 8
Parity	: none
Stop bits (UMG508)	: 2
Stop bits external	: 1 or 2

Byte sequence

The data in the modbus address list can be called up in the format

- Big-Endian (high-Byte before low-Byte) and in the
- Little-Endian (low-byte before high-byte)

The addresses described in this address list supply the data in the „Big-Endian“ format.
If you require the data in the „Little-Endian“ format, you must add the value 32768 to the address.

Update rate

The modbus register addresses are updated every 200ms.

Number formats

Type	Size	Minimum	Maximum
char	8 bit	0	255
byte	8 bit	-128	127
short	16 bit	-215	215 -1
int	32 bit	-231	231 -1
uint	32 bit	0	232 -1
long64	64 bit	-263	263 -1
float	32 bit	IEEE 754	IEEE 754
double	64 bit	IEEE 754	IEEE 754

Symbols and definitions

N	Total number of sample points per period (For example, in a period of 20 ms)
k	Sample value or number of samples per period (0 <= k < N)
p	Number or identification of the phase conductor (p = 1, 2 oder 3)
i_{pk}	Sample value k of the current of the phase conductor p
u_{pNk}	Sample value k of the neutral voltage of the phase conductor p
P_p	Real power of the phase conductor p

Explanations of the measured values

Measured value

- A measured value (in the UMG508) is a effective value which is formed over a period (measuring window) of 200ms.
- A measuring window is 10 periods in the 50Hz network and 12 periods in the 60Hz network.
- A measuring window has a start time and an end time.
- The resolution between the start time and end time is approximately 2ns.
- The accuracy of the start time and end time depends on the accuracy of the internal clock. (Typically +- 1 minute/month)
- In order to improve the accuracy of the internal clock, it is recommended that the clock in the device is compared with a time service and reset.

Mean value of measured value

- For each measured value, a sliding mean value is calculated over the selected averaging time.
- The mean value is calculated every 200ms.
- You can take the possible averaging times from the table.

n	Mean time / seconds
0	5
1	10
2	15
3	30
4	60
5	300
6	480
7	600
8	900

Max. value of measured value

- The ***max. value of the measured value*** is the largest measured value which has occurred since the last deletion.

Min. value of measured value

- The ***min. value of the measured value*** is the lowest measured value which has occurred since the last deletion.

Max. value of mean value

- The ***max. value of the mean value*** is the largest mean value which has occurred since the last deletion.

Nominal current, voltage, frequency

- The limit values for events and transients are set by the nominal value in percentage.

Nominal current I_{rated}

- The Irated is the nominal current of the transformers and is required for calculation of the K-factor.

Peak value negative

- Highest negative sampling value from the last 200ms measuring window..

Peak value positive

- Highest positive sampling value from the last 200ms measuring window.

Crest factor

- The crest factor describes the relation between the peak value and effective value of a periodic quantity. It serves as a characteristic value for general description of the curve form of a periodic quantity. The distortion factor is another example of a quantity for characterization of the difference from the pure sinusoidal form.

- Example
  A sinusoidal change voltage with an effective value of 230 V has a peak value of approx. 325 V. The crest factor is then 325 V / 230 V =1.414 .

Effective value of the current for phase conductor p

$$I_p = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} i_{p_k}^{\,2}}$$

Effective value of neutral conductor current

$$I_N = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} (i_{1_k} + i_{2_k} + i_{3_k})^2}$$

Effective voltage L-N

$$U_{pN} = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} u_{pN_k}^{\,2}}$$

Effective voltage L-L

$$U_{pg} = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} (u_{gN_k} - u_{pN_k})^2}$$

Star connection voltage (vectorial)

$$U_{Sternpunktspannung} = U_{1_{rms}} + U_{2_{rms}} + U_{3_{rms}}$$

Real power for phase conductor

$$P_p = \frac{1}{N} \cdot \sum_{k=0}^{N-1} (u_{pN_k} \times i_{p_k})$$

Apparent power for phase conductor

- Unsigned

$$S_p = U_{pN} \cdot I_p$$

Apparent power for phase conductor

- Unsigned

$$S_A = S_1 + S_2 + S_3$$

Order number of harmonics

xxx[0] = mains frequency (50Hz/60Hz)
xxx[1] = 2nd harmonic (100Hz/120Hz)
xxx[2] = 3rd harmonic (150Hz/180Hz)
etc.

THD

- THD (Total Harmonic Distortion) is the distortion factor and provides the relation of the harmonic parts of an oscillation to the mains frequency.

Distortion factor for the voltage

- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)
- fund corresponds to n=1

$$THD_U = \frac{1}{|U_{fund}|}\sqrt{\sum_{n=2}^{M}|U_{n.Harm}|^2}$$

Distortion factor for the current

- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)
- fund corresponds to n=1

$$THD_I = \frac{1}{|I_{fund}|}\sqrt{\sum_{n=2}^{M}|I_{n.Harm}|^2}$$

ZHD

- THD for the interharmonics.
- Is calculated in the product series and UMG511 UMG605.

Interharmonics

- Sinusoidal oscillations, which frequencies are not a multiple integer of the mains frequency.
- Is calculated in the product series and UMG511 UMG605.
- Calculation and measurement methods in accordance with the DIN EN 61000-4-30.
- The order number of inter harmonics corresponds to the order number of the next smallest harmonic. For example, between the 3rd and 4th harmonic of the 3rd inter harmonics.

TDD (I)

- TDD Total demand distortion, harmonic current distortion in % of maximum demand load current
- IL = Maximum demand load current
- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)

$$TDD = \frac{1}{I_L}\sqrt{\sum_{n=2}^{M} I_n^2} \times 100\%$$

Ripple control signal U (EN61000-4-30)

The ripple control signal U is a voltage (200ms measured value) which is measured at a carrier frequency specified by the user. Only frequencies beneath 3kHz are observed.

Ripple control signal I

The ripple control signal I is a current (200ms measured value) which is measured at a carrier frequency specified by the user. Only frequencies beneath 3kHz are observed.

Positive sequence-negative sequence-zero sequence

- The extent of a voltage or current imbalance in a three-phase system is identified using the positive sequence, negative sequence and zero sequence components.
- The balance of the rotation current system strived for in normal operation is disturbed by the unsymmetrical loads, errors and equipment.
- A three-phase system is called symmetric, when the three phase conductor voltages and currents are the same size and are displaced against each other by 120°. If one or both conditions are not fulfilled, the system is described as unsymmetrical. By calculating the symmetrical components consisting of the positive sequence, negative sequence and zero sequence, the simplified analysis of an imbalanced error is possible in a rotary current system..
- Imbalance is a feature of the network quality for the limits specified in international norms (EN 50160 for example).

Positive sequence

$$U_{Mit} = \frac{1}{3}\left|U_{L1,fund} + U_{L2,fund} \cdot e^{j\frac{2\pi}{3}} + U_{L3,fund} \cdot e^{j\frac{4\pi}{3}}\right|$$

Negative sequence

$$U_{Geg} = \frac{1}{3}\left|U_{L1,fund} + U_{L2,fund} \cdot e^{-j\frac{2\pi}{3}} + U_{L3,fund} \cdot e^{-j\frac{4\pi}{3}}\right|$$

Zero sequence

$$U_{Nullsystem} = \frac{1}{3}\left|U_{L1,fund} + U_{L2,fund} + U_{L3,fund}\right|$$

A zero component can only occur if a sum current can flow back through the main conductor.

Voltage imbalance

$$Unsymmetrie = \frac{U_{Geg}}{U_{Mit}}$$

Under difference U (EN61000-4-30)

$$U_{unter} = \frac{U_{din} - \sqrt{\frac{\sum_{i=1}^{n} U_{rms-unter,i}^2}{n}}}{U_{din}}[\%]$$

Under difference I

$$I_{unter} = \frac{I_{Nennstrom} - \sqrt{\frac{\sum_{i=1}^{n} I_{rms-unter,i}^2}{n}}}{I_{Nennstrom}}[\%]$$

K-Factor

- The K-factor describes the increase of the eddy current losses when loaded with harmonics. For a sinusoidal load on the transformer, the K-factor =1. The larger the K-factor, the heavier a transformer can be loaded with harmonics without overheating.

$$K\text{-}factor = \frac{1}{I_R^2} \sum_{h=1}^{\infty} I_h^2 h^2$$

Power Factor (vectorial) - Lambda

- The power factor is unsigned.

$$PF_A = \frac{|P|}{S_A}$$

CosPhi - Fundamental Power Factor

- Only the mains frequency part is used for calculation of the cosphi.
- CosPhi sign:
  - = for the supply of real power
  + = for obtaining real power

$$PF_1 = \cos(\varphi) = \frac{P_1}{S_1}$$

CosPhi total

- CosPhi sign:
  - = for the supply of real power
  + = for obtaining real power

$$\cos(\varphi)_{Sum_3} = \frac{P_{1_{fund}} + P_{2_{fund}} + P_{3_{fund}}}{\sqrt{(P_{1_{fund}} + P_{2_{fund}} + P_{3_{fund}})^2 + (Q_{1_{fund}} + Q_{2_{fund}} + Q_{3_{fund}})^2}}$$

$$\cos(\varphi)_{Sum_4} = \frac{P_{1_{fund}} + P_{2_{fund}} + P_{3_{fund}} + P_{4_{fund}}}{\sqrt{(P_{1_{fund}} + P_{2_{fund}} + P_{3_{fund}} + P_{4_{fund}})^2 + (Q_{1_{fund}} + Q_{2_{fund}} + Q_{3_{fund}} + Q_{4_{fund}})^2}}$$

Phase Angle Phi

- The phase angle between current and voltage of the external conductor p is calculated according to DIN EN 61557-12 and displayed.
- The sign of the phase angle corresponding to the sign of the reactive power.

Mains frequency power factor

The mains frequency power factor is the power factor of the mains frequency and is calculated using the fourier analysis (FFT). The voltage and current must not be sinusoidal. All in the device calculated reactive power are resulting of fundamental reactive power.

Power factor sign

- Sign Q = +1 for phi in the range 0° .. 180° (inductive)
- Sign Q = -1 for phi in the range 180° .. 360° (capacitive)

$$Vorzeichen\,Q(\varphi_p) = +1 \; falls \; \varphi_p \in [0° - 180°]$$

$$Vorzeichen\,Q(\varphi_p) = -1 \; falls \; \varphi_p \in [180° - 360°]$$

Reactive power for phase conductor p

- Reactive power of the mains frequency.

$$Q_{fund\,p} = Vorzeichen\,Q(\varphi_p) \cdot \sqrt{S_{fund\,p}^{\,2} - P_{fund\,p}^{\,2}}$$

Total reactive power

- Reactive power of the mains frequency.

$$Q_V = Q_1 + Q_2 + Q_3$$

Distortion power factor

- The distortion power factor is the power factor of all mains frequencies and is calculated using the fourier analysis (FFT).

$$D = \sqrt{S^2 - P^2 - Q_{fund}^2}$$

- The apparent power „S” contains all fundamental harmonics and all harmonic rates up to the M-th harmonic.
- The effective power „P” contains all fundamental harmonics and all harmonic rates up to the M-th harmonic.
- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)

Reactive energy per phase

$$E_{r_{L1}} = \int Q_{L1}(t) \cdot \Delta t$$

Reactive energy per phase, inductive

$$E_{r(ind)_{L1}} = \int Q_{L1}(t) \cdot \Delta t \qquad \text{für } Q_{L1}(t) > 0$$

Reactive energy per phase, capazitive

$$E_{r(cap)_{L1}} = \int Q_{L1}(t) \cdot \Delta t \qquad \text{für } Q_{L1}(t) < 0$$

Reactive energy, sum L1-L3

$$E_{r_{L1,L2,L3}} = \int (Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) \cdot \Delta t$$

Reactive energy, sum L1-L3, inductive

$$E_{r(ind)_{L1,L2,L3}} = \int (Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) \cdot \Delta t$$

für $(Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) > 0$

Reactive energy, sum L1-L3, capazitive

$$E_{r(cap)_{L1,L2,L3}} = \int (Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) \cdot \Delta t$$

für $(Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) < 0$

Frequently required readings

Address	Format	RD/WR	Designation	Unit	Note
19000	float	RD	_G_ULN[0]	V	Voltage L1-N
19002	float	RD	_G_ULN[1]	V	Voltage L2-N
19004	float	RD	_G_ULN[2]	V	Voltage L3-N
19006	float	RD	_G_ULL[0]	V	Voltage L1-L2
19008	float	RD	_G_ULL[1]	V	Voltage L2-L3
19010	float	RD	_G_ULL[2]	V	Voltage L3-L1
19012	float	RD	_G_ILN[0]	A	Apparent current, L1-N
19014	float	RD	_G_ILN[1]	A	Apparent current, L2-N
19016	float	RD	_G_ILN[2]	A	Apparent current, L3-N
19018	float	RD	_G_I_SUM3	A	Vector sum; IN=I1+I2+I3
19020	float	RD	_G_PLN[0]	W	Real power L1-N
19022	float	RD	_G_PLN[1]	W	Real power L2-N
19024	float	RD	_G_PLN[2]	W	Real power L3-N
19026	float	RD	_G_P_SUM3	W	Psum3=P1+P2+P3
19028	float	RD	_G_SLN[0]	VA	Apparent power L1-N
19030	float	RD	_G_SLN[1]	VA	Apparent power L2-N
19032	float	RD	_G_SLN[2]	VA	Apparent power L3-N
19034	float	RD	_G_S_SUM3	VA	Sum; Ssum3=S1+S2+S3
19036	float	RD	_G_QLN[0]	var	Reactive power L1 (fundamental comp.)
19038	float	RD	_G_QLN[1]	var	Reactive power L2 (fundamental comp.)
19040	float	RD	_G_QLN[2]	var	Reactive power L3 (fundamental comp.)
19042	float	RD	_G_Q_SUM3	var	Qsum3=Q1+Q2+Q3 (fundamental comp.)
19044	float	RD	_G_COS_PHI[0]	-	CosPhi; UL1 IL1 (fundamental comp.)
19046	float	RD	_G_COS_PHI[1]	-	CosPhi; UL2 IL2 (fundamental comp.)
19048	float	RD	_G_COS_PHI[2]	-	CosPhi; UL3 IL3 (fundamental comp.)
19050	float	RD	_G_FREQ	Hz	Measured frequency
19052	float	RD	_G_PHASE_SEQ	-	Rotation field; 1=right, 0=none, -1=left
19054	float	RD	_G_WH[0]	Wh	Real energy L1
19056	float	RD	_G_WH[1]	Wh	Real energy L2
19058	float	RD	_G_WH[2]	Wh	Real energy L3
19060	float	RD	_G_WH_SUML13	Wh	Real energy L1..L3
19062	float	RD	_G_WH_V[0]	Wh	Real energy L1, consumed
19064	float	RD	_G_WH_V[1]	Wh	Real energy L2, consumed
19066	float	RD	_G_WH_V[2]	Wh	Real energy L3, consumed
19068	float	RD	_G_WH_V_HT_SUML13	Wh	Real energy L1..L3, consumed, rate 1
19070	float	RD	_G_WH_Z[0]	Wh	Real energy L1, delivered
19072	float	RD	_G_WH_Z[1]	Wh	Real energy L2, delivered
19074	float	RD	_G_WH_Z[2]	Wh	Real energy L3, delivered
19076	float	RD	_G_WH_Z_SUML13	Wh	Real energy L1..L3, delivered
19078	float	RD	_G_WH_S[0]	VAh	Apparent energy L1
19080	float	RD	_G_WH_S[1]	VAh	Apparent energy L2
19082	float	RD	_G_WH_S[2]	VAh	Apparent energy L3
19084	float	RD	_G_WH_S_SUML13	VAh	Apparent energy L1..L3
19086	float	RD	_G_QH[0]	varh	Reaktive energy L1 (fundamental comp.)
19088	float	RD	_G_QH[1]	varh	Reaktive energy L2 (fundamental comp.)
19090	float	RD	_G_QH[2]	varh	Reaktive energy L3 (fundamental comp.)
19092	float	RD	_G_QH_SUML13	varh	Reaktive energy L1..L3 (fundamental comp.)
19094	float	RD	_G_IQH[0]	varh	Reactive energy, inductive, L1 (fundamental comp.)
19096	float	RD	_G_IQH[1]	varh	Reactive energy, inductive, L2 (fundamental comp.)
19098	float	RD	_G_IQH[2]	varh	Reactive energy, inductive, L3 (fundamental comp.)
19100	float	RD	_G_IQH_SUML13	varh	Reactive energy L1..L3, ind. (fundamental comp.)
19102	float	RD	_G_CQH[0]	varh	Reactive energy, capacitive, L1 (fundamental comp.)
19104	float	RD	_G_CQH[1]	varh	Reactive energy, capacitive, L2 (fundamental comp.)
19106	float	RD	_G_CQH[2]	varh	Reactive energy, capacitive, L3 (fundamental comp.)
19108	float	RD	_G_CQH_SUML13	varh	Reactive energy L1..L3, cap. (fundamental comp.)
19110	float	RD	_G_THD_ULN[0]	%	Harmonic, THD,U L1-N
19112	float	RD	_G_THD_ULN[1]	%	Harmonic, THD,U L2-N
19114	float	RD	_G_THD_ULN[2]	%	Harmonic, THD,U L3-N
19116	float	RD	_G_THD_ILN[0]	%	Harmonic, THD,I L1
19118	float	RD	_G_THD_ILN[1]	%	Harmonic, THD,I L2

Adresse	Format	RD/WR	Designation	Unit	Note
19120	float	RD	_G_THD_ILN[2]	%	Harmonic, THD,I L3
19122	float	RD	_IND_CAP_SUM3		Cos phi, sum, L1-L3
19124	float	RD	_IND_CAP_SUM		Cos phi, sum, L1-L4
19126	float	RD/WR	_TEMPERATUR_OFFSET	°C	Temperature offset, ext. Temp.

Date and time

Adrress	Format	RD/WR	Designation	Unit	Note
0	long64	RD	_REALTIME	2 ns	Time (UTC)
4	int	RD/WR	_SYSTIME	sec	Time (UTC)
6	short	RD	_DAY		Day (1..31)
7	short	RD	_MONTH		Month (0=Jan, .. 11=Dec)
8	short	RD	_YEAR		Year
9	short	RD	_HOUR	h	Hour (1..24)
10	short	RD	_MIN	min	Minute (1..59)
11	short	RD	_SEC	s	Second (1..59)
12	short	RD	_WEEKDAY		Weekday (0=Sun, .. 6=Mon)

Measured values (200ms measuring window)

Address	Format	RD/WR	Designation	Unit	Note
3793	float	RD	_THD_ULL[0]	%	Harmonic, THD, U L1-L2
3795	float	RD	_THD_ULL[1]	%	Harmonic, THD, U L2-L3
3797	float	RD	_THD_ULL[2]	%	Harmonic, THD, U L3-L1
3799	float	RD	_ZHD_ULL[0]	%	Interharmonics, U L1-L2
3801	float	RD	_ZHD_ULL[1]	%	Interharmonics, U L2-L3
3803	float	RD	_ZHD_ULL[2]	%	Interharmonics, U L3-L1
3805	float	RD	_THD_ULN[0]	%	Harmonic, THD, U L1-N
3807	float	RD	_THD_ULN[1]	%	Harmonic, THD, U L2-N
3809	float	RD	_THD_ULN[2]	%	Harmonic, THD, U L3-N
3811	float	RD	_THD_ULN[3]	%	Harmonic, THD, U L4-N
3813	float	RD	_THD_IL[0]	%	Harmonic, THD, I L1-N
3815	float	RD	_THD_IL[1]	%	Harmonic, THD, I L2-N
3817	float	RD	_THD_IL[2]	%	Harmonic, THD, I L3-N
3819	float	RD	_THD_IL[3]	%	Harmonic, THD, I L4-N
3821	float	RD	_ZHD_ULN[0]	%	Interharmonics, U L1-N
3823	float	RD	_ZHD_ULN[1]	%	Interharmonics, U L2-N
3825	float	RD	_ZHD_ULN[2]	%	Interharmonics, U L3-N
3827	float	RD	_ZHD_ULN[3]	%	Interharmonics, U L4-N
3829	float	RD	_ZHD_ILN[0]	%	Interharmonics, I L1-N
3831	float	RD	_ZHD_ILN[1]	%	Interharmonics, I L2-N
3833	float	RD	_ZHD_ILN[2]	%	Interharmonics, I L3-N
3835	float	RD	_ZHD_ILN[3]	%	Interharmonics, I L4-N
3837	float	RD	_KFACT[0]		K-Factor, L1
3839	float	RD	_KFACT[1]		K-Factor, L2
3841	float	RD	_KFACT[2]		K-Factor, L3
3843	float	RD	_KFACT[3]		K-Factor, L4
3845	float	RD	_ULN[0]	V	Voltage, L1-N
3847	float	RD	_ULN[1]	V	Voltage, L2-N
3849	float	RD	_ULN[2]	V	Voltage, L3-N
3851	float	RD	_ULN[3]	V	Voltage, L4-N
3853	float	RD	_ILN[0]	A	Apparent current, L1
3855	float	RD	_ILN[1]	A	Apparent current, L2
3857	float	RD	_ILN[2]	A	Apparent current, L3
3859	float	RD	_ILN[3]	A	Apparent current, L4
3861	float	RD	_PLN[0]	W	Real power, L1
3863	float	RD	_PLN[1]	W	Real power, L2
3865	float	RD	_PLN[2]	W	Real power, L3
3867	float	RD	_PLN[3]	W	Real power, L4
3869	float	RD	_QLN[0]	Var	Reactive power, L1
3871	float	RD	_QLN[1]	Var	Reactive power, L2
3873	float	RD	_QLN[2]	Var	Reactive power, L3
3875	float	RD	_QLN[3]	Var	Reactive power, L4
3877	float	RD	_SLN[0]	VA	Apparent power, L1
3879	float	RD	_SLN[1]	VA	Apparent power, L2
3881	float	RD	_SLN[2]	VA	Apparent power, L3
3883	float	RD	_SLN[3]	VA	Apparent power, L4
3885	float	RD	_DLN[0]	VA	Distortion power factor, L1
3887	float	RD	_DLN[1]	VA	Distortion power factor, L2
3889	float	RD	_DLN[2]	VA	Distortion power factor, L3
3891	float	RD	_DLN[3]	VA	Distortion power factor, L4
3893	float	RD	_PFLN[0]		Power Factor, L1
3895	float	RD	_PFLN[1]		Power Factor, L2
3897	float	RD	_PFLN[2]		Power Factor, L3
3899	float	RD	_PFLN[3]		Power Factor, L4
3901	float	RD	_ULL[0]	V	Phase conductor voltage, U L1-L2
3903	float	RD	_ULL[1]	V	Phase conductor voltage, U L2-L3
3905	float	RD	_ULL[2]	V	Phase conductor voltage, U L3-L1
3907	float	RD	_ULL_RE[0]	V	Phase conductor voltage real part, U L1-L2

Adresse	Format	RD/WR	Designation	Unit	Note
3909	float	RD	_ULL_RE[1]	V	Phase conductor voltage real part, U L2-L3
3911	float	RD	_ULL_RE[2]	V	Phase conductor voltage real part, U L3-L1
3913	float	RD	_ULL_IM[0]	V	Phase conductor voltage imaginary part, U L1-L2
3915	float	RD	_ULL_IM[1]	V	Phase conductor voltage imaginary part, U L2-L3
3917	float	RD	_ULL_IM[2]	V	Phase conductor voltage imaginary part, U L3-L1
3919	float	RD	_I_SUM3	A	Vector sum, IN = I1 + I2 + I3
3921	float	RD	_I_SUM	A	Vector sum, I1 + I2 + I3 + I4
3923	float	RD	_S_SUM3	VA	Sum, S = S1 + S2 + S3
3925	float	RD	_P_SUM3	W	Sum, P = P1 + P2 + P3
3927	float	RD	_Q_SUM3	Var	Mains frequency reactive power sum, Q = Q1 + Q2 + Q3
3929	float	RD	_COS_SUM3		CosPhi of mains frequency Calculated from Psum3 and Qsum3
3931	float	RD	_S_SUM	VA	Sum, S = S1 + S2 + S3 + S4
3933	float	RD	_P_SUM	W	Sum, P = P1 + P2 + P3 + P4
3935	float	RD	_Q_SUM	Var	Mains frequency reactive power sum, Q = Q1 + Q2 + Q3 + Q4
3937	float	RD	_COS_SUM		CosPhi of mains frequency Calculated from Psum and Qsum
3939	float	RD	_ULN_RE[0]	V	Voltage, real part, L1-N
3941	float	RD	_ULN_RE[1]	V	Voltage, real part, L2-N
3943	float	RD	_ULN_RE[2]	V	Voltage, real part, L3-N
3945	float	RD	_ULN_RE[3]	V	Voltage, real part, L4-N
3947	float	RD	_ULN_IM[0]	V	Voltage, imaginary part, L1-N
3949	float	RD	_ULN_IM[1]	V	Voltage, imaginary part, L2-N
3951	float	RD	_ULN_IM[2]	V	Voltage, imaginary part, L3-N
3953	float	RD	_ULN_IM[3]	V	Voltage, imaginary part, L4-N
3955	float	RD	_IL_RE[0]	A	Current, real part, L1
3957	float	RD	_IL_RE[1]	A	Current, real part, L2
3959	float	RD	_IL_RE[2]	A	Current, real part, L3
3961	float	RD	_IL_RE[3]	A	Current, real part, L4
3963	float	RD	_IL_IM[0]	A	Current, imaginary part, L1
3965	float	RD	_IL_IM[1]	A	Current, imaginary part, L2
3967	float	RD	_IL_IM[2]	A	Current, imaginary part, L3
3969	float	RD	_IL_IM[3]	A	Current, imaginary part, L4
3971	float	RD	_PHASE[0]	°	Phase, UL1 IL1
3973	float	RD	_PHASE[1]	°	Phase, UL1 IL2
3975	float	RD	_PHASE[2]	°	Phase, UL1 IL3
3977	float	RD	_PHASE[3]	°	Phase, UL1 IL4
3979	float	RD	_COS_PHI[0]		Fund. power factor, CosPhi; UL1 IL1
3981	float	RD	_COS_PHI[1]		Fund. power factor, CosPhi; UL2 IL2
3983	float	RD	_COS_PHI[2]		Fund. power factor, CosPhi; UL3 IL3
3985	float	RD	_COS_PHI[3]		Fund. power factor, CosPhi; UL4 IL4
3987	float	RD	_IND_CAP[0]		Sign, Q L1, +1=ind., -1=cap.
3989	float	RD	_IND_CAP[1]		Sign, Q L2, +1=ind., -1=cap.
3991	float	RD	_IND_CAP[2]		Sign, Q L3, +1=ind., -1=cap.
3993	float	RD	_IND_CAP[3]		Sign, Q L4, +1=ind., -1=cap.
3995	float	RD	_FREQ	Hz	Measured frequency
3997	float	RD	_NORM_FREQ	Hz	Nominal frequency
3999	float	RD	_UN	V	Zero sequence, voltage
4001	float	RD	_UM	V	Positive sequence, voltage
4003	float	RD	_UG	V	Negative sequence, voltage
4005	float	RD	_U_SYM	%	Unsymmetrical; voltage
4007	float	RD	_I_SYM	%	Unsymmetrical; current
4009	float	RD	_PHASE_SEQ		Rotation field; 1=right, 0=none, -1=left

Address	Format	RD/WR	Designation	Unit	Note
4011	float	RD	_IN	A	Zero sequence, current
4013	float	RD	_IM	A	Positive sequence, current
4015	float	RD	_IG	A	Negative sequence, current
4017	float	RD	_S0_POWER[0]	W	Input 1, measured value
4019	float	RD	_S0_POWER[1]	W	Input 2, measured value
4021	float	RD	_IL_CF[0]	A	Crest factor, I L1
4023	float	RD	_IL_CF[1]	A	Crest factor, I L2
4025	float	RD	_IL_CF[2]	A	Crest factor, I L3
4027	float	RD	_IL_CF[3]	A	Crest factor, I L4
4029	float	RD	_ULN_CF[0]	V	Crest factor, U L1
4031	float	RD	_ULN_CF[1]	V	Crest factor, U L2
4033	float	RD	_ULN_CF[2]	V	Crest factor, U L3
4035	float	RD	_ULN_CF[3]	V	Crest factor, U L4
4037	float	RD	_ULL_CF[0]	V	Crest factor, U L1-L2
4039	float	RD	_ULL_CF[1]	V	Crest factor, U L2-L3
4041	float	RD	_ULL_CF[2]	V	Crest factor, U L3-L1
4043	float	RD	_IL_NEG_PEAK[0]	A	Peak value negative, I L1
4045	float	RD	_IL_NEG_PEAK[1]	A	Peak value negative, I L2
4047	float	RD	_IL_NEG_PEAK[2]	A	Peak value negative, I L3
4049	float	RD	_IL_NEG_PEAK[3]	A	Peak value negative, I L4
4051	float	RD	_ULN_NEG_PEAK[0]	V	Peak value negative, U L1-N
4053	float	RD	_ULN_NEG_PEAK[1]	V	Peak value negative, U L2-N
4055	float	RD	_ULN_NEG_PEAK[2]	V	Peak value negative, U L3-N
4057	float	RD	_ULN_NEG_PEAK[3]	V	Peak value negative, U L4-N
4059	float	RD	_IL_POS_PEAK[0]	A	Peak value positive, I L1
4061	float	RD	_IL_POS_PEAK[1]	A	Peak value positive, I L2
4063	float	RD	_IL_POS_PEAK[2]	A	Peak value positive, I L3
4065	float	RD	_IL_POS_PEAK[3]	A	Peak value positive, I L4
4067	float	RD	_ULN_POS_PEAK[0]	V	Peak value positive, U L1-N
4069	float	RD	_ULN_POS_PEAK[1]	V	Peak value positive, U L2-N
4071	float	RD	_ULN_POS_PEAK[2]	V	Peak value positive, U L3-N
4073	float	RD	_ULN_POS_PEAK[3]	V	Peak value positive, U L4-N
4075	float	RD	_IL_PEAK_PEAK[0]	A	Peak-peak value positive, I L1
4077	float	RD	_IL_PEAK_PEAK[1]	A	Peak-peak value positive, I L2
4079	float	RD	_IL_PEAK_PEAK[2]	A	Peak-peak value positive, I L3
4081	float	RD	_IL_PEAK_PEAK[3]	A	Peak-peak value positive, I L4
4083	float	RD	_ULN_PEAK_PEAK[0]	V	Peak-peak value positive, U L1-N
4085	float	RD	_ULN_PEAK_PEAK[1]	V	Peak-peak value positive, U L2-N
4087	float	RD	_ULN_PEAK_PEAK[2]	V	Peak-peak value positive, U L3-N
4089	float	RD	_ULN_PEAK_PEAK[3]	V	Peak-peak value positive, U L4-N
4091	float	RD	_IL_UNDER[0]	%	Under difference, I L1
4093	float	RD	_IL_UNDER[1]	%	Under difference, I L2
4095	float	RD	_IL_UNDER[2]	%	Under difference, I L3
4097	float	RD	_IL_UNDER[3]	%	Under difference, I L4
4099	float	RD	_ULN_UNDER[0]	%	Under difference, U L1 (61000-4-30)
4101	float	RD	_ULN_UNDER[1]	%	Under difference, U L2 (61000-4-30)
4103	float	RD	_ULN_UNDER[2]	%	Under difference, U L3 (61000-4-30)
4105	float	RD	_ULN_UNDER[3]	%	Under difference, U L4 (61000-4-30)
4107	float	RD	_IL_OVER[0]	%	Over difference, I L1
4109	float	RD	_IL_OVER[1]	%	Over difference, I L2
4111	float	RD	_IL_OVER[2]	%	Over difference, I L3
4113	float	RD	_IL_OVER[3]	%	Over difference, I L4
4115	float	RD	_ULN_OVER[0]	%	Over difference, U L1 (61000-4-30)
4117	float	RD	_ULN_OVER[1]	%	Over difference, U L2 (61000-4-30)
4119	float	RD	_ULN_OVER[2]	%	Over difference, U L3 (61000-4-30)
4121	float	RD	_ULN_OVER[3]	%	Over difference, U L4 (61000-4-30)
4123	float	RD	_ULL_NEG_PEAK[0]	V	Peak value negative, U L1-L2
4125	float	RD	_ULL_NEG_PEAK[1]	V	Peak value negative, U L2-L3

Adresse	Format	RD/WR	Designation	Unit	Note
4127	float	RD	_ULL_NEG_PEAK[2]	V	Peak value negative, U L3-L1
4129	float	RD	_ULL_POS_PEAK[0]	V	Peak value positive, U L1-L2
4131	float	RD	_ULL_POS_PEAK[1]	V	Peak value positive, U L2-L3
4133	float	RD	_ULL_POS_PEAK[2]	V	Peak value positive, U L3-L1
4135	float	RD	_ULL_PEAK_PEAK[0]	V	Peak-peak value, U L1-L2
4137	float	RD	_ULL_PEAK_PEAK[1]	V	Peak-peak value, U L2-L3
4139	float	RD	_ULL_PEAK_PEAK[2]	V	Peak-peak value, U L3-L1
4141	float	RD	_ULL_UNDER[0]	%	Under difference, U L1-L2 (61000-4-30)
4143	float	RD	_ULL_UNDER[1]	%	Under difference, U L2-L3 (61000-4-30)
4145	float	RD	_ULL_UNDER[2]	%	Under difference, U L3-L1 (61000-4-30)
4147	float	RD	_ULL_OVER[0]	%	Over difference, U L1-L2 (61000-4-30)
4149	float	RD	_ULL_OVER[1]	%	Over difference, U L2-L3 (61000-4-30)
4151	float	RD	_ULL_OVER[2]	%	Over difference, U L3-L1 (61000-4-30)
4153	float	RD	_FLI_PF5[0]		Current flicker Pf5, L1-N
4155	float	RD	_FLI_PF5[1]		Current flicker Pf5, L2-N
4157	float	RD	_FLI_PF5[2]		Current flicker Pf5, L3-N
4159	float	RD	_FLI_PF5[3]		Current flicker Pf5, L4-N
4161	float	RD	_FLI_SHORT_TERM[0]		Short-term flicker level, Pst (10m), L1-N
4163	float	RD	_FLI_SHORT_TERM[1]		Short-term flicker level, Pst (10m), L2-N
4165	float	RD	_FLI_SHORT_TERM[2]		Short-term flicker level, Pst (10m), L3-N
4167	float	RD	_FLI_SHORT_TERM[3]		Short-term flicker level, Pst (10m), L4-N
4169	float	RD	_FLI_LONG_TERM[0]		Long-term flicker level, Plt (2h), L1-N
4171	float	RD	_FLI_LONG_TERM[1]		Long-term flicker level, Plt (2h), L2-N
4173	float	RD	_FLI_LONG_TERM[2]		Long-term flicker level, Plt (2h), L3-N
4175	float	RD	_FLI_LONG_TERM[3]		Long-term flicker level, Plt (2h), L4-N
4177	float	RD	_URC[0]	V	Ripple control signal, U L1-N (61000-4-30)
4179	float	RD	_URC[1]	V	Ripple control signal, U L2-N (61000-4-30)
4181	float	RD	_URC[2]	V	Ripple control signal, U L3-N (61000-4-30)
4183	float	RD	_URC[3]	V	Ripple control signal, U L4-N (61000-4-30)
4185	float	RD	_IRC[0]	A	Ripple control signal, I L1
4187	float	RD	_IRC[1]	A	Ripple control signal, I L2
4189	float	RD	_IRC[2]	A	Ripple control signal, I L3
4191	float	RD	_IRC[3]	A	Ripple control signal, I L4
4193	float	RD	_ULL_RC[0]	V	Ripple control signal, U L1-L2 (61000-4-30)
4195	float	RD	_ULL_RC[1]	V	Ripple control signal, U L2-L3 (61000-4-30)
4197	float	RD	_ULL_RC[2]	V	Ripple control signal, U L3-L1 (61000-4-30)
4209	float	RD	_EXT_TEMPERATUR	°C	Internal temperature
10955	float	RD/WR	_IRATED_TDD[0]	A	Maximum demand load current, L1
10957	float	RD/WR	_IRATED_TDD[1]	A	Maximum demand load current, L2
10959	float	RD/WR	_IRATED_TDD[2]	A	Maximum demand load current, L3
10961	float	RD/WR	_IRATED_TDD[3]	A	Maximum demand load current, L4
10963	float	RD	_TDD_IL[0]	%	TDD, Total Demand Distortion, IIL1
10965	float	RD	_TDD_IL[1]	%	TDD, Total Demand Distortion, IIL2
10967	float	RD	_TDD_IL[2]	%	TDD, Total Demand Distortion, IIL3
10969	float	RD	_TDD_IL[3]	%	TDD, Total Demand Distortion, IIL4
10977	float	RD	_U_SYM_U0	%	Sum, voltage imbalance

Mean values (float type)

Address	Format	RD/WR	Designation	Unit	Note
4211	float	RD/WR	_ULN_AVG[0]	V	Average, U L1-N
4213	float	RD/WR	_ULN_AVG[1]	V	Average, U L2-N
4215	float	RD/WR	_ULN_AVG[2]	V	Average, U L3-N
4217	float	RD/WR	_ULN_AVG[3]	V	Average, U L4-N
4219	float	RD/WR	_ULL_AVG[0]	V	Average, U L1-L2
4221	float	RD/WR	_ULL_AVG[1]	V	Average, U L2-L3
4223	float	RD/WR	_ULL_AVG[2]	V	Average, U L3-L4
4225	float	RD/WR	_ULN_CF_AVG[0]	%	Mean value of the crest factor, U L1-N
4227	float	RD/WR	_ULN_CF_AVG[1]	%	Mean value of the crest factor, U L2-N
4229	float	RD/WR	_ULN_CF_AVG[2]	%	Mean value of the crest factor, U L3-N
4231	float	RD/WR	_ULN_CF_AVG[3]	%	Mean value of the crest factor, U L4-N
4233	float	RD/WR	_ULL_CF_AVG[0]	%	Mean value of the crest factor, U L1-L2
4235	float	RD/WR	_ULL_CF_AVG[1]	%	Mean value of the crest factor, U L2-L3
4237	float	RD/WR	_ULL_CF_AVG[2]	%	Mean value of the crest factor, U L3-L4
4239	float	RD/WR	_UN_AVG	V	Mean value zero sequence
4241	float	RD/WR	_UM_AVG	V	Mean value positive sequence
4243	float	RD/WR	_UG_AVG	V	Mean value negative sequence
4245	float	RD/WR	_THD_ULN_AVG[0]	%	Mean value THD U L1-N
4247	float	RD/WR	_THD_ULN_AVG[1]	%	Mean value THD U L2-N
4249	float	RD/WR	_THD_ULN_AVG[2]	%	Mean value THD U L3-N
4251	float	RD/WR	_THD_ULN_AVG[3]	%	Mean value THD U L4-N
4253	float	RD/WR	_THD_ZLN_AVG[0]	%	Mean value ZHD U L1-N
4255	float	RD/WR	_THD_ZLN_AVG[1]	%	Mean value ZHD U L2-N
4257	float	RD/WR	_THD_ZLN_AVG[2]	%	Mean value ZHD U L3-N
4259	float	RD/WR	_THD_ZLN_AVG[3]	%	Mean value ZHD U L4-N
4261	float	RD/WR	_ULN_OVER_AVG[0]	%	
4263	float	RD/WR	_ULN_OVER_AVG[1]	%	
4265	float	RD/WR	_ULN_OVER_AVG[2]	%	
4267	float	RD/WR	_ULN_OVER_AVG[3]	%	
4269	float	RD/WR	_ULN_UNDER_AVG[0]	%	
4271	float	RD/WR	_ULN_UNDER_AVG[1]	%	
4273	float	RD/WR	_ULN_UNDER_AVG[2]	%	
4275	float	RD/WR	_ULN_UNDER_AVG[3]	%	
4277	float	RD/WR	_ULN_NEG_PEAK_AVG[0]	V	
4279	float	RD/WR	_ULN_NEG_PEAK_AVG[1]	V	
4281	float	RD/WR	_ULN_NEG_PEAK_AVG[2]	V	
4283	float	RD/WR	_ULN_NEG_PEAK_AVG[3]	V	
4285	float	RD/WR	_ULN_POS_PEAK_AVG[0]	V	
4287	float	RD/WR	_ULN_POS_PEAK_AVG[1]	V	
4289	float	RD/WR	_ULN_POS_PEAK_AVG[2]	V	
4291	float	RD/WR	_ULN_POS_PEAK_AVG[3]	V	
4293	float	RD/WR	_ULN_PEAK_PEAK_AVG[0]	V	
4295	float	RD/WR	_ULN_PEAK_PEAK_AVG[1]	V	
4297	float	RD/WR	_ULN_PEAK_PEAK_AVG[2]	V	
4299	float	RD/WR	_ULN_PEAK_PEAK_AVG[3]	V	
4301	float	RD/WR	_THD_ULL_AVG[0]	%	
4303	float	RD/WR	_THD_ULL_AVG[1]	%	
4305	float	RD/WR	_THD_ULL_AVG[2]	%	
4307	float	RD/WR	_THD_ZLL_AVG[0]	%	
4309	float	RD/WR	_THD_ZLL_AVG[1]	%	
4311	float	RD/WR	_THD_ZLL_AVG[2]	%	
4313	float	RD/WR	_ULL_OVER_AVG[0]	%	
4315	float	RD/WR	_ULL_OVER_AVG[1]	%	
4317	float	RD/WR	_ULL_OVER_AVG[2]	%	
4319	float	RD/WR	_ULL_UNDER_AVG[0]	%	
4321	float	RD/WR	_ULL_UNDER_AVG[1]	%	
4323	float	RD/WR	_ULL_UNDER_AVG[2]	%	
4325	float	RD/WR	_ULL_NEG_PEAK_AVG[0]	V	

Adresse	Format	RD/WR	Designation	Unit	Note
4327	float	RD/WR	_ULL_NEG_PEAK_AVG[1]	V	
4329	float	RD/WR	_ULL_NEG_PEAK_AVG[2]	V	
4331	float	RD/WR	_ULL_POS_PEAK_AVG[0]	V	
4333	float	RD/WR	_ULL_POS_PEAK_AVG[1]	V	
4335	float	RD/WR	_ULL_POS_PEAK_AVG[2]	V	
4337	float	RD/WR	_ULL_PEAK_PEAK_AVG[0]	V	
4339	float	RD/WR	_ULL_PEAK_PEAK_AVG[1]	V	
4341	float	RD/WR	_ULL_PEAK_PEAK_AVG[2]	V	
4343	float	RD/WR	_U_STERN_AVG	V	
4345	float	RD/WR	_U_SYM_AVG	%	
4347	float	RD/WR	_FREQ_AVG	Hz	
4349	float	RD/WR	_NORM_FREQ_AVG	Hz	
4351	float	RD/WR	_PLN_AVG[0]	W	Average, P L1
4353	float	RD/WR	_PLN_AVG[1]	W	Average, P L2
4355	float	RD/WR	_PLN_AVG[2]	W	Average, P L3
4357	float	RD/WR	_PLN_AVG[3]	W	Average, P L4
4359	float	RD/WR	_P_SUM_AVG	W	Average, Psum=P1+P2+P3+P4
4361	float	RD/WR	_Q_SUM_AVG	Var	Average, Qsum=Q1+Q2+Q3+Q4
4363	float	RD/WR	_QLN_AVG[0]	Var	Average, Q L1
4365	float	RD/WR	_QLN_AVG[1]	Var	Average, Q L2
4367	float	RD/WR	_QLN_AVG[2]	Var	Average, Q L3
4369	float	RD/WR	_QLN_AVG[3]	Var	Average, Q L4
4371	float	RD/WR	_P_SUM3_AVG	W	Average, Psum3=P1+P2+P3
4373	float	RD/WR	_Q_SUM3_AVG	Var	Average, Qsum3=Q1+Q2+Q3
4375	float	RD/WR	_ILN_AVG[0]	A	Average, I L1
4377	float	RD/WR	_ILN_AVG[1]	A	Average, I L2
4379	float	RD/WR	_ILN_AVG[2]	A	Average, I L3
4381	float	RD/WR	_ILN_AVG[3]	A	Average, I L4
4383	float	RD/WR	_SLN_AVG[0]	VA	Average, S L1
4385	float	RD/WR	_SLN_AVG[1]	VA	Average, S L2
4387	float	RD/WR	_SLN_AVG[2]	VA	Average, S L3
4389	float	RD/WR	_SLN_AVG[3]	VA	Average, S L4
4391	float	RD/WR	_I_SUM3_AVG	A	Average, IN=I1+I2+I3
4393	float	RD/WR	_I_SUM_AVG	A	Average, Isum=I1+I2+I3+I4
4395	float	RD/WR	_S_SUM3_AVG	VA	Average, Ssum3=S1+S2+S3
4397	float	RD/WR	_S_SUM_AVG	VA	Average, Ssum=S1+S2+S3+S4
4399	float	RD/WR	_THD_IL_AVG[0]	%	
4401	float	RD/WR	_THD_IL_AVG[1]	%	
4403	float	RD/WR	_THD_IL_AVG[2]	%	
4405	float	RD/WR	_THD_IL_AVG[3]	%	
4407	float	RD/WR	_ZHD_IL_AVG[0]	%	
4409	float	RD/WR	_ZHD_IL_AVG[1]	%	
4411	float	RD/WR	_ZHD_IL_AVG[2]	%	
4413	float	RD/WR	_ZHD_IL_AVG[3]	%	
4415	float	RD/WR	_ILN_CF_AVG[0]	%	
4417	float	RD/WR	_ILN_CF_AVG[1]	%	
4419	float	RD/WR	_ILN_CF_AVG[2]	%	
4421	float	RD/WR	_ILN_CF_AVG[3]	%	
4423	float	RD/WR	_IN_AVG	A	Average, current, zero sequence
4425	float	RD/WR	_IM_AVG	A	Average, current, positive sequence
4427	float	RD/WR	_IG_AVG	A	Average, current, negative sequence
4429	float	RD/WR	_I_SYM_AVG	%	
4431	float	RD/WR	_ILN_OVER_AVG[0]	%	
4433	float	RD/WR	_ILN_OVER_AVG[1]	%	
4435	float	RD/WR	_ILN_OVER_AVG[2]	%	
4437	float	RD/WR	_ILN_OVER_AVG[3]	%	
4439	float	RD/WR	_ILN_UNDER_AVG[0]	%	
4441	float	RD/WR	_ILN_UNDER_AVG[1]	%	

Address	Format	RD/WR	Designation	Unit	Note
4443	float	RD/WR	_ILN_UNDER_AVG[2]	%	
4445	float	RD/WR	_ILN_UNDER_AVG[3]	%	
4447	float	RD/WR	_ILN_NEG_PEAK_AVG[0]	A	
4449	float	RD/WR	_ILN_NEG_PEAK_AVG[1]	A	
4451	float	RD/WR	_ILN_NEG_PEAK_AVG[2]	A	
4453	float	RD/WR	_ILN_NEG_PEAK_AVG[3]	A	
4455	float	RD/WR	_ILN_POS_PEAK_AVG[0]	A	
4457	float	RD/WR	_ILN_POS_PEAK_AVG[1]	A	
4459	float	RD/WR	_ILN_POS_PEAK_AVG[2]	A	
4461	float	RD/WR	_ILN_POS_PEAK_AVG[3]	A	
4463	float	RD/WR	_ILN_PEAK_PEAK_AVG[0]	A	
4465	float	RD/WR	_ILN_PEAK_PEAK_AVG[1]	A	
4467	float	RD/WR	_ILN_PEAK_PEAK_AVG[2]	A	
4469	float	RD/WR	_ILN_PEAK_PEAK_AVG[3]	A	
4471	float	RD/WR	_FLI_PF5_AVG[0]		
4473	float	RD/WR	_FLI_PF5_AVG[1]		
4475	float	RD/WR	_FLI_PF5_AVG[2]		
4477	float	RD/WR	_FLI_PF5_AVG[3]		
4479	float	RD/WR	_FLI_ST_AVG[0]		
4481	float	RD/WR	_FLI_ST_AVG[1]		
4483	float	RD/WR	_FLI_ST_AVG[2]		
4485	float	RD/WR	_FLI_ST_AVG[3]		
4487	float	RD/WR	_FLI_LT_AVG[0]		
4489	float	RD/WR	_FLI_LT_AVG[1]		
4491	float	RD/WR	_FLI_LT_AVG[2]		
4493	float	RD/WR	_FLI_LT_AVG[3]		
4495	float	RD/WR	_IRC_AVG[0]	A	
4497	float	RD/WR	_IRC_AVG[1]	A	
4499	float	RD/WR	_IRC_AVG[2]	A	
4501	float	RD/WR	_IRC_AVG[3]	A	
4503	float	RD/WR	_ULL_RC_AVG[0]	V	
4505	float	RD/WR	_ULL_RC_AVG[1]	V	
4507	float	RD/WR	_ULL_RC_AVG[2]	V	
4519	float	RD/WR	_PFLN_AVG[0]	%	
4521	float	RD/WR	_PFLN_AVG[1]	%	
4523	float	RD/WR	_PFLN_AVG[2]	%	
4525	float	RD/WR	_PFLN_AVG[3]	%	
4527	float	RD/WR	_DLN_AVG[0]	Var	
4529	float	RD/WR	_DLN_AVG[1]	Var	
4531	float	RD/WR	_DLN_AVG[2]	Var	
4533	float	RD/WR	_DLN_AVG[3]	Var	
4535	float	RD/WR	_KFACT_AVG[0]	%	
4537	float	RD/WR	_KFACT_AVG[1]	%	
4539	float	RD/WR	_KFACT_AVG[2]	%	
4541	float	RD/WR	_KFACT_AVG[3]	%	
4543	float	RD/WR	_S0_POWER_AVG[0]	W	
4545	float	RD/WR	_S0_POWER_AVG[1]	W	
4547	float	RD/WR	_EXT_TEMPERATUR_AVG	°C	
10979	float	RD	_U_SYM_AVG_U0	%	Average, sum, voltage imbalance

Minimum values (float type)

Adresse	Format	RD/WR	Designation	Unit	Note
4549	float	RD/WR	_ULN_MIN[0]	V	Minimum, U L1-N
4551	float	RD/WR	_ULN_MIN[1]	V	Minimum, U L2-N
4553	float	RD/WR	_ULN_MIN[2]	V	Minimum, U L3-N
4555	float	RD/WR	_ULN_MIN[3]	V	Minimum, U L4-N
4557	float	RD/WR	_ULL_MIN[0]	V	Minimum, U L1-L2
4559	float	RD/WR	_ULL_MIN[1]	V	Minimum, U L2-L3
4561	float	RD/WR	_ULL_MIN[2]	V	Minimum, U L3-L4
4563	float	RD/WR	_ULN_CF_MIN[0]	%	
4565	float	RD/WR	_ULN_CF_MIN[1]	%	
4567	float	RD/WR	_ULN_CF_MIN[2]	%	
4569	float	RD/WR	_ULN_CF_MIN[3]	%	
4571	float	RD/WR	_ULL_CF_MIN[0]	%	
4573	float	RD/WR	_ULL_CF_MIN[1]	%	
4575	float	RD/WR	_ULL_CF_MIN[2]	%	
4577	float	RD/WR	_UN_MIN	V	
4579	float	RD/WR	_UM_MIN	V	
4581	float	RD/WR	_UG_MIN	V	
4583	float	RD/WR	_URC_MIN[0]	V	
4585	float	RD/WR	_URC_MIN[1]	V	
4587	float	RD/WR	_URC_MIN[2]	V	
4589	float	RD/WR	_URC_MIN[3]	V	
4591	float	RD/WR	_THD_ULN_MIN[0]	%	
4593	float	RD/WR	_THD_ULN_MIN[1]	%	
4595	float	RD/WR	_THD_ULN_MIN[2]	%	
4597	float	RD/WR	_THD_ULN_MIN[3]	%	
4599	float	RD/WR	_THD_ZLN_MIN[0]	%	
4601	float	RD/WR	_THD_ZLN_MIN[1]	%	
4603	float	RD/WR	_THD_ZLN_MIN[2]	%	
4605	float	RD/WR	_THD_ZLN_MIN[3]	%	
4607	float	RD/WR	_ULN_OVER_MIN[0]	%	
4609	float	RD/WR	_ULN_OVER_MIN[1]	%	
4611	float	RD/WR	_ULN_OVER_MIN[2]	%	
4613	float	RD/WR	_ULN_OVER_MIN[3]	%	
4615	float	RD/WR	_ULN_UNDER_MIN[0]	%	
4617	float	RD/WR	_ULN_UNDER_MIN[1]	%	
4619	float	RD/WR	_ULN_UNDER_MIN[2]	%	
4621	float	RD/WR	_ULN_UNDER_MIN[3]	%	
4623	float	RD/WR	_ULN_NEG_PEAK_MIN[0]	V	
4625	float	RD/WR	_ULN_NEG_PEAK_MIN[1]	V	
4627	float	RD/WR	_ULN_NEG_PEAK_MIN[2]	V	
4629	float	RD/WR	_ULN_NEG_PEAK_MIN[3]	V	
4631	float	RD/WR	_ULN_POS_PEAK_MIN[0]	V	
4633	float	RD/WR	_ULN_POS_PEAK_MIN[1]	V	
4635	float	RD/WR	_ULN_POS_PEAK_MIN[2]	V	
4637	float	RD/WR	_ULN_POS_PEAK_MIN[3]	V	
4639	float	RD/WR	_ULN_PEAK_PEAK_MIN[0]	V	
4641	float	RD/WR	_ULN_PEAK_PEAK_MIN[1]	V	
4643	float	RD/WR	_ULN_PEAK_PEAK_MIN[2]	V	
4645	float	RD/WR	_ULN_PEAK_PEAK_MIN[3]	V	
4647	float	RD/WR	_THD_ULL_MIN[0]	%	
4649	float	RD/WR	_THD_ULL_MIN[1]	%	
4651	float	RD/WR	_THD_ULL_MIN[2]	%	
4653	float	RD/WR	_THD_ZLL_MIN[0]	%	
4655	float	RD/WR	_THD_ZLL_MIN[1]	%	
4657	float	RD/WR	_THD_ZLL_MIN[2]	%	
4659	float	RD/WR	_ULL_OVER_MIN[0]	%	
4661	float	RD/WR	_ULL_OVER_MIN[1]	%	
4663	float	RD/WR	_ULL_OVER_MIN[2]	%	

Address	Format	RD/WR	Designation	Unit	Note
4665	float	RD/WR	_ULL_UNDER_MIN[0]	%	
4667	float	RD/WR	_ULL_UNDER_MIN[1]	%	
4669	float	RD/WR	_ULL_UNDER_MIN[2]	%	
4671	float	RD/WR	_ULL_NEG_PEAK_MIN[0]	V	
4673	float	RD/WR	_ULL_NEG_PEAK_MIN[1]	V	
4675	float	RD/WR	_ULL_NEG_PEAK_MIN[2]	V	
4677	float	RD/WR	_ULL_POS_PEAK_MIN[0]	V	
4679	float	RD/WR	_ULL_POS_PEAK_MIN[1]	V	
4681	float	RD/WR	_ULL_POS_PEAK_MIN[2]	V	
4683	float	RD/WR	_ULL_PEAK_PEAK_MIN[0]	V	
4685	float	RD/WR	_ULL_PEAK_PEAK_MIN[1]	V	
4687	float	RD/WR	_ULL_PEAK_PEAK_MIN[2]	V	
4689	float	RD/WR	_U_STERN_MIN	V	
4691	float	RD/WR	_U_SYM_MIN	%	
4693	float	RD/WR	_FREQ_MIN	Hz	
4695	float	RD/WR	_NORM_FREQ_MIN	Hz	
4697	float	RD/WR	_PLN_MIN[0]	W	
4699	float	RD/WR	_PLN_MIN[1]	W	
4701	float	RD/WR	_PLN_MIN[2]	W	
4703	float	RD/WR	_PLN_MIN[3]	W	
4705	float	RD/WR	_P_SUM_MIN	W	
4707	float	RD/WR	_Q_SUM_MIN	Var	
4709	float	RD/WR	_QLN_MIN[0]	Var	
4711	float	RD/WR	_QLN_MIN[1]	Var	
4713	float	RD/WR	_QLN_MIN[2]	Var	
4715	float	RD/WR	_QLN_MIN[3]	Var	
4717	float	RD/WR	_P_SUM3_MIN	W	
4719	float	RD/WR	_Q_SUM3_MIN	Var	
4721	float	RD/WR	_EXT_TEMPERATUR_MIN	°C	
10981	float	RD	_U_SYM_MIN_U0	%	Minimum, sum, voltage imbalance

Maximum values (float type)

Adresse	Format	RD/WR	Designation	Unit	Note
4723	float	RD/WR	_ULN_MAX[0]	V	Maximum, U L1-N
4725	float	RD/WR	_ULN_MAX[1]	V	Maximum, U L2-N
4727	float	RD/WR	_ULN_MAX[2]	V	Maximum, U L3-N
4729	float	RD/WR	_ULN_MAX[3]	V	Maximum, U L4-N
4731	float	RD/WR	_ULL_MAX[0]	V	Maximum, U L1-L2
4733	float	RD/WR	_ULL_MAX[1]	V	Maximum, U L2-L3
4735	float	RD/WR	_ULL_MAX[2]	V	Maximum, U L3-L4
4737	float	RD/WR	_ULN_CF_MAX[0]	%	
4739	float	RD/WR	_ULN_CF_MAX[1]	%	
4741	float	RD/WR	_ULN_CF_MAX[2]	%	
4743	float	RD/WR	_ULN_CF_MAX[3]	%	
4745	float	RD/WR	_ULL_CF_MAX[0]	%	
4747	float	RD/WR	_ULL_CF_MAX[1]	%	
4749	float	RD/WR	_ULL_CF_MAX[2]	%	
4751	float	RD/WR	_UN_MAX	V	
4753	float	RD/WR	_UM_MAX	V	
4755	float	RD/WR	_UG_MAX	V	
4757	float	RD/WR	_URC_MAX[0]	V	
4759	float	RD/WR	_URC_MAX[1]	V	
4761	float	RD/WR	_URC_MAX[2]	V	
4763	float	RD/WR	_URC_MAX[3]	V	
4765	float	RD/WR	_THD_ULN_MAX[0]	%	
4767	float	RD/WR	_THD_ULN_MAX[1]	%	
4769	float	RD/WR	_THD_ULN_MAX[2]	%	
4771	float	RD/WR	_THD_ULN_MAX[3]	%	
4773	float	RD/WR	_THD_ZLN_MAX[0]	%	
4775	float	RD/WR	_THD_ZLN_MAX[1]	%	
4777	float	RD/WR	_THD_ZLN_MAX[2]	%	
4779	float	RD/WR	_THD_ZLN_MAX[3]	%	
4781	float	RD/WR	_ULN_OVER_MAX[0]	%	
4783	float	RD/WR	_ULN_OVER_MAX[1]	%	
4785	float	RD/WR	_ULN_OVER_MAX[2]	%	
4787	float	RD/WR	_ULN_OVER_MAX[3]	%	
4789	float	RD/WR	_ULN_UNDER_MAX[0]	%	
4791	float	RD/WR	_ULN_UNDER_MAX[1]	%	
4793	float	RD/WR	_ULN_UNDER_MAX[2]	%	
4795	float	RD/WR	_ULN_UNDER_MAX[3]	%	
4797	float	RD/WR	_ULN_NEG_PEAK_MAX[0]	V	
4799	float	RD/WR	_ULN_NEG_PEAK_MAX[1]	V	
4801	float	RD/WR	_ULN_NEG_PEAK_MAX[2]	V	
4803	float	RD/WR	_ULN_NEG_PEAK_MAX[3]	V	
4805	float	RD/WR	_ULN_POS_PEAK_MAX[0]	V	
4807	float	RD/WR	_ULN_POS_PEAK_MAX[1]	V	
4809	float	RD/WR	_ULN_POS_PEAK_MAX[2]	V	
4811	float	RD/WR	_ULN_POS_PEAK_MAX[3]	V	
4813	float	RD/WR	_ULN_PEAK_PEAK_MAX[0]	V	
4815	float	RD/WR	_ULN_PEAK_PEAK_MAX[1]	V	
4817	float	RD/WR	_ULN_PEAK_PEAK_MAX[2]	V	
4819	float	RD/WR	_ULN_PEAK_PEAK_MAX[3]	V	
4821	float	RD/WR	_THD_ULL_MAX[0]	%	
4823	float	RD/WR	_THD_ULL_MAX[1]	%	
4825	float	RD/WR	_THD_ULL_MAX[2]	%	
4827	float	RD/WR	_THD_ZLL_MAX[0]	%	
4829	float	RD/WR	_THD_ZLL_MAX[1]	%	
4831	float	RD/WR	_THD_ZLL_MAX[2]	%	
4833	float	RD/WR	_ULL_OVER_MAX[0]	%	
4835	float	RD/WR	_ULL_OVER_MAX[1]	%	
4837	float	RD/WR	_ULL_OVER_MAX[2]	%	

Address	Format	RD/WR	Designation	Unit	Note
4839	float	RD/WR	_ULL_UNDER_MAX[0]	%	
4841	float	RD/WR	_ULL_UNDER_MAX[1]	%	
4843	float	RD/WR	_ULL_UNDER_MAX[2]	%	
4845	float	RD/WR	_ULL_NEG_PEAK_MAX[0]	V	
4847	float	RD/WR	_ULL_NEG_PEAK_MAX[1]	V	
4849	float	RD/WR	_ULL_NEG_PEAK_MAX[2]	V	
4851	float	RD/WR	_ULL_POS_PEAK_MAX[0]	V	
4853	float	RD/WR	_ULL_POS_PEAK_MAX[1]	V	
4855	float	RD/WR	_ULL_POS_PEAK_MAX[2]	V	
4857	float	RD/WR	_ULL_PEAK_PEAK_MAX[0]	V	
4859	float	RD/WR	_ULL_PEAK_PEAK_MAX[1]	V	
4861	float	RD/WR	_ULL_PEAK_PEAK_MAX[2]	V	
4863	float	RD/WR	_U_STERN_MAX	V	
4865	float	RD/WR	_U_SYM_MAX	%	
4867	float	RD/WR	_FREQ_MAX	Hz	
4869	float	RD/WR	_NORM_FREQ_MAX	Hz	
4871	float	RD/WR	_PLN_MAX[0]	W	
4873	float	RD/WR	_PLN_MAX[1]	W	
4875	float	RD/WR	_PLN_MAX[2]	W	
4877	float	RD/WR	_PLN_MAX[3]	W	
4879	float	RD/WR	_P_SUM_MAX	W	
4881	float	RD/WR	_Q_SUM_MAX	Var	
4883	float	RD/WR	_QLN_MAX[0]	Var	
4885	float	RD/WR	_QLN_MAX[1]	Var	
4887	float	RD/WR	_QLN_MAX[2]	Var	
4889	float	RD/WR	_QLN_MAX[3]	Var	
4891	float	RD/WR	_P_SUM3_MAX	W	
4893	float	RD/WR	_Q_SUM3_MAX	Var	
4895	float	RD/WR	_ILN_MAX[0]	A	
4897	float	RD/WR	_ILN_MAX[1]	A	
4899	float	RD/WR	_ILN_MAX[2]	A	
4901	float	RD/WR	_ILN_MAX[3]	A	
4903	float	RD/WR	_SLN_MAX[0]	VA	
4905	float	RD/WR	_SLN_MAX[1]	VA	
4907	float	RD/WR	_SLN_MAX[2]	VA	
4909	float	RD/WR	_SLN_MAX[3]	VA	
4911	float	RD/WR	_I_SUM3_MAX	A	
4913	float	RD/WR	_I_SUM_MAX	A	
4915	float	RD/WR	_S_SUM3_MAX	VA	
4917	float	RD/WR	_S_SUM_MAX	VA	
4919	float	RD/WR	_THD_IL_MAX[0]	%	
4921	float	RD/WR	_THD_IL_MAX[1]	%	
4923	float	RD/WR	_THD_IL_MAX[2]	%	
4925	float	RD/WR	_THD_IL_MAX[3]	%	
4927	float	RD/WR	_ZHD_IL_MAX[0]	%	
4929	float	RD/WR	_ZHD_IL_MAX[1]	%	
4931	float	RD/WR	_ZHD_IL_MAX[2]	%	
4933	float	RD/WR	_ZHD_IL_MAX[3]	%	
4935	float	RD/WR	_ILN_CF_MAX[0]		
4937	float	RD/WR	_ILN_CF_MAX[1]		
4939	float	RD/WR	_ILN_CF_MAX[2]		
4941	float	RD/WR	_ILN_CF_MAX[3]		
4943	float	RD/WR	_IN_MAX	A	
4945	float	RD/WR	_IM_MAX	A	
4947	float	RD/WR	_IG_MAX	A	
4949	float	RD/WR	_I_SYM_MAX	%	
4951	float	RD/WR	_ILN_OVER_MAX[0]	%	
4953	float	RD/WR	_ILN_OVER_MAX[1]	%	

Adresse	Format	RD/WR	Designation	Unit	Note
4955	float	RD/WR	_ILN_OVER_MAX[2]	%	
4957	float	RD/WR	_ILN_OVER_MAX[3]	%	
4959	float	RD/WR	_ILN_UNDER_MAX[0]	%	
4961	float	RD/WR	_ILN_UNDER_MAX[1]	%	
4963	float	RD/WR	_ILN_UNDER_MAX[2]	%	
4965	float	RD/WR	_ILN_UNDER_MAX[3]	%	
4967	float	RD/WR	_ILN_NEG_PEAK_MAX[0]	A	
4969	float	RD/WR	_ILN_NEG_PEAK_MAX[1]	A	
4971	float	RD/WR	_ILN_NEG_PEAK_MAX[2]	A	
4973	float	RD/WR	_ILN_NEG_PEAK_MAX[3]	A	
4975	float	RD/WR	_ILN_POS_PEAK_MAX[0]	A	
4977	float	RD/WR	_ILN_POS_PEAK_MAX[1]	A	
4979	float	RD/WR	_ILN_POS_PEAK_MAX[2]	A	
4981	float	RD/WR	_ILN_POS_PEAK_MAX[3]	A	
4983	float	RD/WR	_ILN_PEAK_PEAK_MAX[0]	A	
4985	float	RD/WR	_ILN_PEAK_PEAK_MAX[1]	A	
4987	float	RD/WR	_ILN_PEAK_PEAK_MAX[2]	A	
4989	float	RD/WR	_ILN_PEAK_PEAK_MAX[3]	A	
4991	float	RD/WR	_FLI_PF5_MAX[0]		
4993	float	RD/WR	_FLI_PF5_MAX[1]		
4995	float	RD/WR	_FLI_PF5_MAX[2]		
4997	float	RD/WR	_FLI_PF5_MAX[3]		
4999	float	RD/WR	_FLI_ST_MAX[0]		
5001	float	RD/WR	_FLI_ST_MAX[1]		
5003	float	RD/WR	_FLI_ST_MAX[2]		
5005	float	RD/WR	_FLI_ST_MAX[3]		
5007	float	RD/WR	_FLI_LT_MAX[0]		
5009	float	RD/WR	_FLI_LT_MAX[1]		
5011	float	RD/WR	_FLI_LT_MAX[2]		
5013	float	RD/WR	_FLI_LT_MAX[3]		
5015	float	RD/WR	_ILN_RC_MAX[0]	A	
5017	float	RD/WR	_ILN_RC_MAX[1]	A	
5019	float	RD/WR	_ILN_RC_MAX[2]	A	
5021	float	RD/WR	_ILN_RC_MAX[3]	A	
5023	float	RD/WR	_ULL_RC_MAX[0]	V	
5025	float	RD/WR	_ULL_RC_MAX[1]	V	
5027	float	RD/WR	_ULL_RC_MAX[2]	V	
5039	float	RD/WR	_PFLN_MAX[0]	%	
5041	float	RD/WR	_PFLN_MAX[1]	%	
5043	float	RD/WR	_PFLN_MAX[2]	%	
5045	float	RD/WR	_PFLN_MAX[3]	%	
5047	float	RD/WR	_DLN_MAX[0]	Var	
5049	float	RD/WR	_DLN_MAX[1]	Var	
5051	float	RD/WR	_DLN_MAX[2]	Var	
5053	float	RD/WR	_DLN_MAX[3]	Var	
5055	float	RD/WR	_KFACT_MAX[0]	%	
5057	float	RD/WR	_KFACT_MAX[1]	%	
5059	float	RD/WR	_KFACT_MAX[2]	%	
5061	float	RD/WR	_KFACT_MAX[3]	%	
5063	float	RD/WR	_S0_POWER_MAX[0]	W	
5065	float	RD/WR	_S0_POWER_MAX[1]	W	
5067	float	RD/WR	_EXT_TEMPERATUR_MAX	°C	
10983	float	RD	_U_SYM_MAX_U0	%	Maximum, sum, voltage imbalance

Averaging time

Address	Format	RD/WR	Designation	Unit	Note
5069	short	RD/WR	_ULN_AVG_T[0]	n	Averaging time, U L1-N
5070	short	RD/WR	_ULN_AVG_T[1]	n	Averaging time, U L2-N
5071	short	RD/WR	_ULN_AVG_T[2]	n	Averaging time, U L3-N
5072	short	RD/WR	_ULN_AVG_T[3]	n	Averaging time, U L4-N
5073	short	RD/WR	_ULL_AVG_T[0]	n	Averaging time, U L1-L2
5074	short	RD/WR	_ULL_AVG_T[1]	n	Averaging time, U L2-L3
5075	short	RD/WR	_ULL_AVG_T[2]	n	Averaging time, U L3-L4
5076	short	RD/WR	_ULN_CF_AVG_T[0]	n	
5077	short	RD/WR	_ULN_CF_AVG_T[1]	n	
5078	short	RD/WR	_ULN_CF_AVG_T[2]	n	
5079	short	RD/WR	_ULN_CF_AVG_T[3]	n	
5080	short	RD/WR	_ULL_CF_AVG_T[0]	n	
5081	short	RD/WR	_ULL_CF_AVG_T[1]	n	
5082	short	RD/WR	_ULL_CF_AVG_T[2]	n	
5083	short	RD/WR	_UN_AVG_T	n	
5084	short	RD/WR	_UM_AVG_T	n	
5085	short	RD/WR	_UG_AVG_T	n	
5086	short	RD/WR	_URC_AVG_T[0]	n	
5087	short	RD/WR	_URC_AVG_T[1]	n	
5088	short	RD/WR	_URC_AVG_T[2]	n	
5089	short	RD/WR	_URC_AVG_T[3]	n	
5090	short	RD/WR	_THD_ULN_AVG_T[0]	n	
5091	short	RD/WR	_THD_ULN_AVG_T[1]	n	
5092	short	RD/WR	_THD_ULN_AVG_T[2]	n	
5093	short	RD/WR	_THD_ULN_AVG_T[3]	n	
5094	short	RD/WR	_THD_ZLN_AVG_T[0]	n	
5095	short	RD/WR	_THD_ZLN_AVG_T[1]	n	
5096	short	RD/WR	_THD_ZLN_AVG_T[2]	n	
5097	short	RD/WR	_THD_ZLN_AVG_T[3]	n	
5098	short	RD/WR	_ULN_OVER_AVG_T[0]	n	
5099	short	RD/WR	_ULN_OVER_AVG_T[1]	n	
5100	short	RD/WR	_ULN_OVER_AVG_T[2]	n	
5101	short	RD/WR	_ULN_OVER_AVG_T[3]	n	
5102	short	RD/WR	_ULN_UNDER_AVG_T[0]	n	
5103	short	RD/WR	_ULN_UNDER_AVG_T[1]	n	
5104	short	RD/WR	_ULN_UNDER_AVG_T[2]	n	
5105	short	RD/WR	_ULN_UNDER_AVG_T[3]	n	
5106	short	RD/WR	_ULN_NEG_PEAK_AVG_T[0]	n	
5107	short	RD/WR	_ULN_NEG_PEAK_AVG_T[1]	n	
5108	short	RD/WR	_ULN_NEG_PEAK_AVG_T[2]	n	
5109	short	RD/WR	_ULN_NEG_PEAK_AVG_T[3]	n	
5110	short	RD/WR	_ULN_POS_PEAK_AVG_T[0]	n	
5111	short	RD/WR	_ULN_POS_PEAK_AVG_T[1]	n	
5112	short	RD/WR	_ULN_POS_PEAK_AVG_T[2]	n	
5113	short	RD/WR	_ULN_POS_PEAK_AVG_T[3]	n	
5114	short	RD/WR	_ULN_PEAK_PEAK_AVG_T[0]	n	
5115	short	RD/WR	_ULN_PEAK_PEAK_AVG_T[1]	n	
5116	short	RD/WR	_ULN_PEAK_PEAK_AVG_T[2]	n	
5117	short	RD/WR	_ULN_PEAK_PEAK_AVG_T[3]	n	
5118	short	RD/WR	_THD_ULL_AVG_T[0]	n	
5119	short	RD/WR	_THD_ULL_AVG_T[1]	n	
5120	short	RD/WR	_THD_ULL_AVG_T[2]	n	
5121	short	RD/WR	_THD_ZLL_AVG_T[0]	n	
5122	short	RD/WR	_THD_ZLL_AVG_T[1]	n	
5123	short	RD/WR	_THD_ZLL_AVG_T[2]	n	
5124	short	RD/WR	_ULL_OVER_AVG_T[0]	n	
5125	short	RD/WR	_ULL_OVER_AVG_T[1]	n	
5126	short	RD/WR	_ULL_OVER_AVG_T[2]	n	

Adresse	Format	RD/WR	Designation	Unit	Note
5127	short	RD/WR	_ULL_UNDER_AVG_T[0]	n	
5128	short	RD/WR	_ULL_UNDER_AVG_T[1]	n	
5129	short	RD/WR	_ULL_UNDER_AVG_T[2]	n	
5130	short	RD/WR	_ULL_NEG_PEAK_AVG_T[0]	n	
5131	short	RD/WR	_ULL_NEG_PEAK_AVG_T[1]	n	
5132	short	RD/WR	_ULL_NEG_PEAK_AVG_T[2]	n	
5133	short	RD/WR	_ULL_POS_PEAK_AVG_T[0]	n	
5134	short	RD/WR	_ULL_POS_PEAK_AVG_T[1]	n	
5135	short	RD/WR	_ULL_POS_PEAK_AVG_T[2]	n	
5136	short	RD/WR	_ULL_PEAK_PEAK_AVG_T[0]	n	
5137	short	RD/WR	_ULL_PEAK_PEAK_AVG_T[1]	n	
5138	short	RD/WR	_ULL_PEAK_PEAK_AVG_T[2]	n	
5139	short	RD/WR	_U_STERN_AVG_T	n	
5140	short	RD/WR	_U_SYM_AVG_T	n	
5141	short	RD/WR	_FREQ_AVG_T	n	
5142	short	RD/WR	_NORM_FREQ_AVG_T	n	
5143	short	RD/WR	_PLN_AVG_T[0]	n	
5144	short	RD/WR	_PLN_AVG_T[1]	n	
5145	short	RD/WR	_PLN_AVG_T[2]	n	
5146	short	RD/WR	_PLN_AVG_T[3]	n	
5147	short	RD/WR	_P_SUM_AVG_T	n	
5148	short	RD/WR	_Q_SUM_AVG_T	n	
5149	short	RD/WR	_QLN_AVG_T[0]	n	
5150	short	RD/WR	_QLN_AVG_T[1]	n	
5151	short	RD/WR	_QLN_AVG_T[2]	n	
5152	short	RD/WR	_QLN_AVG_T[3]	n	
5153	short	RD/WR	_P_SUM3_AVG_T	n	
5154	short	RD/WR	_Q_SUM3_AVG_T	n	
5155	short	RD/WR	_ILN_AVG_T[0]	n	
5156	short	RD/WR	_ILN_AVG_T[1]	n	
5157	short	RD/WR	_ILN_AVG_T[2]	n	
5158	short	RD/WR	_ILN_AVG_T[3]	n	
5159	short	RD/WR	_SLN_AVG_T[0]	n	
5160	short	RD/WR	_SLN_AVG_T[1]	n	
5161	short	RD/WR	_SLN_AVG_T[2]	n	
5162	short	RD/WR	_SLN_AVG_T[3]	n	
5163	short	RD/WR	_I_SUM3_AVG_T	n	
5164	short	RD/WR	_I_SUM_AVG_T	n	
5165	short	RD/WR	_S_SUM3_AVG_T	n	
5166	short	RD/WR	_S_SUM_AVG_T	n	
5167	short	RD/WR	_THD_IL_AVG_T[0]	n	
5168	short	RD/WR	_THD_IL_AVG_T[1]	n	
5169	short	RD/WR	_THD_IL_AVG_T[2]	n	
5170	short	RD/WR	_THD_IL_AVG_T[3]	n	
5171	short	RD/WR	_ZHD_IL_AVG_T[0]	n	
5172	short	RD/WR	_ZHD_IL_AVG_T[1]	n	
5173	short	RD/WR	_ZHD_IL_AVG_T[2]	n	
5174	short	RD/WR	_ZHD_IL_AVG_T[3]	n	
5175	short	RD/WR	_ILN_CF_AVG_T[0]	n	
5176	short	RD/WR	_ILN_CF_AVG_T[1]	n	
5177	short	RD/WR	_ILN_CF_AVG_T[2]	n	
5178	short	RD/WR	_ILN_CF_AVG_T[3]	n	
5179	short	RD/WR	_IN_AVG_T	n	
5180	short	RD/WR	_IM_AVG_T	n	
5181	short	RD/WR	_IG_AVG_T	n	
5182	short	RD/WR	_I_SYM_AVG_T	n	
5183	short	RD/WR	_ILN_OVER_AVG_T[0]	n	
5184	short	RD/WR	_ILN_OVER_AVG_T[1]	n	

Address	Format	RD/WR	Designation	Unit	Note
5185	short	RD/WR	_ILN_OVER_AVG_T[2]	n	
5186	short	RD/WR	_ILN_OVER_AVG_T[3]	n	
5187	short	RD/WR	_ILN_UNDER_AVG_T[0]	n	
5188	short	RD/WR	_ILN_UNDER_AVG_T[1]	n	
5189	short	RD/WR	_ILN_UNDER_AVG_T[2]	n	
5190	short	RD/WR	_ILN_UNDER_AVG_T[3]	n	
5191	short	RD/WR	_ILN_NEG_PEAK_AVG_T[0]	n	
5192	short	RD/WR	_ILN_NEG_PEAK_AVG_T[1]	n	
5193	short	RD/WR	_ILN_NEG_PEAK_AVG_T[2]	n	
5194	short	RD/WR	_ILN_NEG_PEAK_AVG_T[3]	n	
5195	short	RD/WR	_ILN_POS_PEAK_AVG_T[0]	n	
5196	short	RD/WR	_ILN_POS_PEAK_AVG_T[1]	n	
5197	short	RD/WR	_ILN_POS_PEAK_AVG_T[2]	n	
5198	short	RD/WR	_ILN_POS_PEAK_AVG_T[3]	n	
5199	short	RD/WR	_ILN_PEAK_PEAK_AVG_T[0]	n	
5200	short	RD/WR	_ILN_PEAK_PEAK_AVG_T[1]	n	
5201	short	RD/WR	_ILN_PEAK_PEAK_AVG_T[2]	n	
5202	short	RD/WR	_ILN_PEAK_PEAK_AVG_T[3]	n	
5203	short	RD/WR	_FLI_PF5_AVG_T[0]	n	
5204	short	RD/WR	_FLI_PF5_AVG_T[1]	n	
5205	short	RD/WR	_FLI_PF5_AVG_T[2]	n	
5206	short	RD/WR	_FLI_PF5_AVG_T[3]	n	
5207	short	RD/WR	_FLI_ST_AVG_T[0]	n	
5208	short	RD/WR	_FLI_ST_AVG_T[1]	n	
5209	short	RD/WR	_FLI_ST_AVG_T[2]	n	
5210	short	RD/WR	_FLI_ST_AVG_T[3]	n	
5211	short	RD/WR	_FLI_LT_AVG_T[0]	n	
5212	short	RD/WR	_FLI_LT_AVG_T[1]	n	
5213	short	RD/WR	_FLI_LT_AVG_T[2]	n	
5214	short	RD/WR	_FLI_LT_AVG_T[3]	n	
5215	short	RD/WR	_ILN_RC_AVG_T[0]	n	
5216	short	RD/WR	_ILN_RC_AVG_T[1]	n	
5217	short	RD/WR	_ILN_RC_AVG_T[2]	n	
5218	short	RD/WR	_ILN_RC_AVG_T[3]	n	
5219	short	RD/WR	_ULL_RC_AVG_T[0]	V	
5220	short	RD/WR	_ULL_RC_AVG_T[1]	V	
5221	short	RD/WR	_ULL_RC_AVG_T[2]	V	
5227	short	RD/WR	_PFLN_AVG_T[0]	n	
5228	short	RD/WR	_PFLN_AVG_T[1]	n	
5229	short	RD/WR	_PFLN_AVG_T[2]	n	
5230	short	RD/WR	_PFLN_AVG_T[3]	n	
5231	short	RD/WR	_DLN_AVG_T[0]	n	
5232	short	RD/WR	_DLN_AVG_T[1]	n	
5233	short	RD/WR	_DLN_AVG_T[2]	n	
5234	short	RD/WR	_DLN_AVG_T[3]	n	
5235	short	RD/WR	_KFACT_AVG_T[0]	n	
5236	short	RD/WR	_KFACT_AVG_T[1]	n	
5237	short	RD/WR	_KFACT_AVG_T[2]	n	
5238	short	RD/WR	_KFACT_AVG_T[3]	n	
5239	short	RD/WR	_S0_POWER_AVG_T[0]	n	
5240	short	RD/WR	_S0_POWER_AVG_T[1]	n	
5241	short	RD/WR	_EXT_TEMPERATUR_AVG_T	n	
10985	short	RD/WR	_U_SYM_AVG_T_U0	n	Averaging time, sum, voltage imbalance

Minimum values time stamp

Adresse	Format	RD/WR	Designation	Unit	Note
5242	uint	RD/WR	_ULN_MIN_T[0]	s	Time of min. val. (UTC), U L1-N
5244	uint	RD/WR	_ULN_MIN_T[1]	s	Time of min. val. (UTC), U L2-N
5246	uint	RD/WR	_ULN_MIN_T[2]	s	Time of min. val. (UTC), U L3-N
5248	uint	RD/WR	_ULN_MIN_T[3]	s	Time of min. val. (UTC), U L4-N
5250	uint	RD/WR	_ULL_MIN_T[0]	s	Time of min. val. (UTC), U L1-L2
5252	uint	RD/WR	_ULL_MIN_T[1]	s	Time of min. val. (UTC), U L2-L3
5254	uint	RD/WR	_ULL_MIN_T[2]	s	Time of min. val. (UTC), U L3-L4
5256	uint	RD/WR	_ULN_CF_MIN_T[0]	s	
5258	uint	RD/WR	_ULN_CF_MIN_T[1]	s	
5260	uint	RD/WR	_ULN_CF_MIN_T[2]	s	
5262	uint	RD/WR	_ULN_CF_MIN_T[3]	s	
5264	uint	RD/WR	_ULL_CF_MIN_T[0]	s	
5266	uint	RD/WR	_ULL_CF_MIN_T[1]	s	
5268	uint	RD/WR	_ULL_CF_MIN_T[2]	s	
5270	uint	RD/WR	_UN_MIN_T	s	
5272	uint	RD/WR	_UM_MIN_T	s	
5274	uint	RD/WR	_UG_MIN_T	s	
5276	uint	RD/WR	_URC_MIN_T[0]	s	
5278	uint	RD/WR	_URC_MIN_T[1]	s	
5280	uint	RD/WR	_URC_MIN_T[2]	s	
5282	uint	RD/WR	_URC_MIN_T[3]	s	
5284	uint	RD/WR	_THD_ULN_MIN_T[0]	s	
5286	uint	RD/WR	_THD_ULN_MIN_T[1]	s	
5288	uint	RD/WR	_THD_ULN_MIN_T[2]	s	
5290	uint	RD/WR	_THD_ULN_MIN_T[3]	s	
5292	uint	RD/WR	_THD_ZLN_MIN_T[0]	s	
5294	uint	RD/WR	_THD_ZLN_MIN_T[1]	s	
5296	uint	RD/WR	_THD_ZLN_MIN_T[2]	s	
5298	uint	RD/WR	_THD_ZLN_MIN_T[3]	s	
5300	uint	RD/WR	_ULN_OVER_MIN_T[0]	s	
5302	uint	RD/WR	_ULN_OVER_MIN_T[1]	s	
5304	uint	RD/WR	_ULN_OVER_MIN_T[2]	s	
5306	uint	RD/WR	_ULN_OVER_MIN_T[3]	s	
5308	uint	RD/WR	_ULN_UNDER_MIN_T[0]	s	
5310	uint	RD/WR	_ULN_UNDER_MIN_T[1]	s	
5312	uint	RD/WR	_ULN_UNDER_MIN_T[2]	s	
5314	uint	RD/WR	_ULN_UNDER_MIN_T[3]	s	
5316	uint	RD/WR	_ULN_NEG_PEAK_MIN_T[0]	s	
5318	uint	RD/WR	_ULN_NEG_PEAK_MIN_T[1]	s	
5320	uint	RD/WR	_ULN_NEG_PEAK_MIN_T[2]	s	
5322	uint	RD/WR	_ULN_NEG_PEAK_MIN_T[3]	s	
5324	uint	RD/WR	_ULN_POS_PEAK_MIN_T[0]	s	
5326	uint	RD/WR	_ULN_POS_PEAK_MIN_T[1]	s	
5328	uint	RD/WR	_ULN_POS_PEAK_MIN_T[2]	s	
5330	uint	RD/WR	_ULN_POS_PEAK_MIN_T[3]	s	
5332	uint	RD/WR	_ULN_PEAK_PEAK_MIN_T[0]	s	
5334	uint	RD/WR	_ULN_PEAK_PEAK_MIN_T[1]	s	
5336	uint	RD/WR	_ULN_PEAK_PEAK_MIN_T[2]	s	
5338	uint	RD/WR	_ULN_PEAK_PEAK_MIN_T[3]	s	
5340	uint	RD/WR	_THD_ULL_MIN_T[0]	s	
5342	uint	RD/WR	_THD_ULL_MIN_T[1]	s	
5344	uint	RD/WR	_THD_ULL_MIN_T[2]	s	
5346	uint	RD/WR	_THD_ZLL_MIN_T[0]	s	
5348	uint	RD/WR	_THD_ZLL_MIN_T[1]	s	
5350	uint	RD/WR	_THD_ZLL_MIN_T[2]	s	
5352	uint	RD/WR	_ULL_OVER_MIN_T[0]	s	
5354	uint	RD/WR	_ULL_OVER_MIN_T[1]	s	
5356	uint	RD/WR	_ULL_OVER_MIN_T[2]	s	

Address	Format	RD/WR	Designation	Unit	Note
5358	uint	RD/WR	_ULL_UNDER_MIN_T[0]	s	
5360	uint	RD/WR	_ULL_UNDER_MIN_T[1]	s	
5362	uint	RD/WR	_ULL_UNDER_MIN_T[2]	s	
5364	uint	RD/WR	_ULL_NEG_PEAK_MIN_T[0]	s	
5366	uint	RD/WR	_ULL_NEG_PEAK_MIN_T[1]	s	
5368	uint	RD/WR	_ULL_NEG_PEAK_MIN_T[2]	s	
5370	uint	RD/WR	_ULL_POS_PEAK_MIN_T[0]	s	
5372	uint	RD/WR	_ULL_POS_PEAK_MIN_T[1]	s	
5374	uint	RD/WR	_ULL_POS_PEAK_MIN_T[2]	s	
5376	uint	RD/WR	_ULL_PEAK_PEAK_MIN_T[0]	s	
5378	uint	RD/WR	_ULL_PEAK_PEAK_MIN_T[1]	s	
5380	uint	RD/WR	_ULL_PEAK_PEAK_MIN_T[2]	s	
5382	uint	RD/WR	_U_STERN_MIN_T	s	
5384	uint	RD/WR	_U_SYM_MIN_T	s	
5386	uint	RD/WR	_FREQ_MIN_T	s	
5388	uint	RD/WR	_NORM_FREQ_MIN_T	s	
5390	uint	RD/WR	_PLN_MIN_T[0]	s	
5392	uint	RD/WR	_PLN_MIN_T[1]	s	
5394	uint	RD/WR	_PLN_MIN_T[2]	s	
5396	uint	RD/WR	_PLN_MIN_T[3]	s	
5398	uint	RD/WR	_P_SUM_MIN_T	s	
5400	uint	RD/WR	_Q_SUM_MIN_T	s	
5402	uint	RD/WR	_QLN_MIN_T[0]	s	
5404	uint	RD/WR	_QLN_MIN_T[1]	s	
5406	uint	RD/WR	_QLN_MIN_T[2]	s	
5408	uint	RD/WR	_QLN_MIN_T[3]	s	
5410	uint	RD/WR	_P_SUM3_MIN_T	s	
5412	uint	RD/WR	_Q_SUM3_MIN_T	s	
5414	uint	RD/WR	_EXT_TEMPERATUR_MIN_T	s	
10986	short	RD/WR	_U_SYM_MIN_T_U0	n	

Maximum values time stamp

Adresse	Format	RD/WR	Designation	Unit	Note
5416	uint	RD/WR	_ULN_MAX_T[0]	s	Time of max. val. (UTC), U L1-N
5418	uint	RD/WR	_ULN_MAX_T[1]	s	Time of max. val. (UTC), U L2-N
5420	uint	RD/WR	_ULN_MAX_T[2]	s	Time of max. val. (UTC), U L3-N
5422	uint	RD/WR	_ULN_MAX_T[3]	s	Time of max. val. (UTC), U L4-N
5424	uint	RD/WR	_ULL_MAX_T[0]	s	Time of max. val. (UTC), U L1-L2
5426	uint	RD/WR	_ULL_MAX_T[1]	s	Time of max. val. (UTC), U L2-L3
5428	uint	RD/WR	_ULL_MAX_T[2]	s	Time of max. val. (UTC), U L3-L4
5430	uint	RD/WR	_ULN_CF_MAX_T[0]	s	
5432	uint	RD/WR	_ULN_CF_MAX_T[1]	s	
5434	uint	RD/WR	_ULN_CF_MAX_T[2]	s	
5436	uint	RD/WR	_ULN_CF_MAX_T[3]	s	
5438	uint	RD/WR	_ULL_CF_MAX_T[0]	s	
5440	uint	RD/WR	_ULL_CF_MAX_T[1]	s	
5442	uint	RD/WR	_ULL_CF_MAX_T[2]	s	
5444	uint	RD/WR	_UN_MAX_T	s	
5446	uint	RD/WR	_UM_MAX_T	s	
5448	uint	RD/WR	_UG_MAX_T	s	
5450	uint	RD/WR	_URC_MAX_T[0]	s	
5452	uint	RD/WR	_URC_MAX_T[1]	s	
5454	uint	RD/WR	_URC_MAX_T[2]	s	
5456	uint	RD/WR	_URC_MAX_T[3]	s	
5458	uint	RD/WR	_THD_ULN_MAX_T[0]	s	
5460	uint	RD/WR	_THD_ULN_MAX_T[1]	s	
5462	uint	RD/WR	_THD_ULN_MAX_T[2]	s	
5464	uint	RD/WR	_THD_ULN_MAX_T[3]	s	
5466	uint	RD/WR	_THD_ZLN_MAX_T[0]	s	
5468	uint	RD/WR	_THD_ZLN_MAX_T[1]	s	
5470	uint	RD/WR	_THD_ZLN_MAX_T[2]	s	
5472	uint	RD/WR	_THD_ZLN_MAX_T[3]	s	
5474	uint	RD/WR	_ULN_OVER_MAX_T[0]	s	
5476	uint	RD/WR	_ULN_OVER_MAX_T[1]	s	
5478	uint	RD/WR	_ULN_OVER_MAX_T[2]	s	
5480	uint	RD/WR	_ULN_OVER_MAX_T[3]	s	
5482	uint	RD/WR	_ULN_UNDER_MAX_T[0]	s	
5484	uint	RD/WR	_ULN_UNDER_MAX_T[1]	s	
5486	uint	RD/WR	_ULN_UNDER_MAX_T[2]	s	
5488	uint	RD/WR	_ULN_UNDER_MAX_T[3]	s	
5490	uint	RD/WR	_ULN_NEG_PEAK_MAX_T[0]	s	
5492	uint	RD/WR	_ULN_NEG_PEAK_MAX_T[1]	s	
5494	uint	RD/WR	_ULN_NEG_PEAK_MAX_T[2]	s	
5496	uint	RD/WR	_ULN_NEG_PEAK_MAX_T[3]	s	
5498	uint	RD/WR	_ULN_POS_PEAK_MAX_T[0]	s	
5500	uint	RD/WR	_ULN_POS_PEAK_MAX_T[1]	s	
5502	uint	RD/WR	_ULN_POS_PEAK_MAX_T[2]	s	
5504	uint	RD/WR	_ULN_POS_PEAK_MAX_T[3]	s	
5506	uint	RD/WR	_ULN_PEAK_PEAK_MAX_T[0]	s	
5508	uint	RD/WR	_ULN_PEAK_PEAK_MAX_T[1]	s	
5510	uint	RD/WR	_ULN_PEAK_PEAK_MAX_T[2]	s	
5512	uint	RD/WR	_ULN_PEAK_PEAK_MAX_T[3]	s	
5514	uint	RD/WR	_THD_ULL_MAX_T[0]	s	
5516	uint	RD/WR	_THD_ULL_MAX_T[1]	s	
5518	uint	RD/WR	_THD_ULL_MAX_T[2]	s	
5520	uint	RD/WR	_THD_ZLL_MAX_T[0]	s	
5522	uint	RD/WR	_THD_ZLL_MAX_T[1]	s	
5524	uint	RD/WR	_THD_ZLL_MAX_T[2]	s	
5526	uint	RD/WR	_ULL_OVER_MAX_T[0]	s	
5528	uint	RD/WR	_ULL_OVER_MAX_T[1]	s	
5530	uint	RD/WR	_ULL_OVER_MAX_T[2]	s	

Address	Format	RD/WR	Designation	Unit	Note
5532	uint	RD/WR	_ULL_UNDER_MAX_T[0]	s	
5534	uint	RD/WR	_ULL_UNDER_MAX_T[1]	s	
5536	uint	RD/WR	_ULL_UNDER_MAX_T[2]	s	
5538	uint	RD/WR	_ULL_NEG_PEAK_MAX_T[0]	s	
5540	uint	RD/WR	_ULL_NEG_PEAK_MAX_T[1]	s	
5542	uint	RD/WR	_ULL_NEG_PEAK_MAX_T[2]	s	
5544	uint	RD/WR	_ULL_POS_PEAK_MAX_T[0]	s	
5546	uint	RD/WR	_ULL_POS_PEAK_MAX_T[1]	s	
5548	uint	RD/WR	_ULL_POS_PEAK_MAX_T[2]	s	
5550	uint	RD/WR	_ULL_PEAK_PEAK_MAX_T[0]	s	
5552	uint	RD/WR	_ULL_PEAK_PEAK_MAX_T[1]	s	
5554	uint	RD/WR	_ULL_PEAK_PEAK_MAX_T[2]	s	
5556	uint	RD/WR	_U_STERN_MAX_T	s	
5558	uint	RD/WR	_U_SYM_MAX_T	s	
5560	uint	RD/WR	_FREQ_MAX_T	s	
5562	uint	RD/WR	_NORM_FREQ_MAX_T	s	
5564	uint	RD/WR	_PLN_MAX_T[0]	s	
5566	uint	RD/WR	_PLN_MAX_T[1]	s	
5568	uint	RD/WR	_PLN_MAX_T[2]	s	
5570	uint	RD/WR	_PLN_MAX_T[3]	s	
5572	uint	RD/WR	_P_SUM_MAX_T	s	
5574	uint	RD/WR	_Q_SUM_MAX_T	s	
5576	uint	RD/WR	_QLN_MAX_T[0]	s	
5578	uint	RD/WR	_QLN_MAX_T[1]	s	
5580	uint	RD/WR	_QLN_MAX_T[2]	s	
5582	uint	RD/WR	_QLN_MAX_T[3]	s	
5584	uint	RD/WR	_P_SUM3_MAX_T	s	
5586	uint	RD/WR	_Q_SUM3_MAX_T	s	
5588	uint	RD/WR	_ILN_MAX_T[0]	s	
5590	uint	RD/WR	_ILN_MAX_T[1]	s	
5592	uint	RD/WR	_ILN_MAX_T[2]	s	
5594	uint	RD/WR	_ILN_MAX_T[3]	s	
5596	uint	RD/WR	_SLN_MAX_T[0]	s	
5598	uint	RD/WR	_SLN_MAX_T[1]	s	
5600	uint	RD/WR	_SLN_MAX_T[2]	s	
5602	uint	RD/WR	_SLN_MAX_T[3]	s	
5604	uint	RD/WR	_I_SUM3_MAX_T	s	
5606	uint	RD/WR	_I_SUM_MAX_T	s	
5608	uint	RD/WR	_S_SUM3_MAX_T	s	
5610	uint	RD/WR	_S_SUM_MAX_T	s	
5612	uint	RD/WR	_THD_IL_MAX_T[0]	s	
5614	uint	RD/WR	_THD_IL_MAX_T[1]	s	
5616	uint	RD/WR	_THD_IL_MAX_T[2]	s	
5618	uint	RD/WR	_THD_IL_MAX_T[3]	s	
5620	uint	RD/WR	_ZHD_IL_MAX_T[0]	s	
5622	uint	RD/WR	_ZHD_IL_MAX_T[1]	s	
5624	uint	RD/WR	_ZHD_IL_MAX_T[2]	s	
5626	uint	RD/WR	_ZHD_IL_MAX_T[3]	s	
5628	uint	RD/WR	_ILN_CF_MAX_T[0]	s	
5630	uint	RD/WR	_ILN_CF_MAX_T[1]	s	
5632	uint	RD/WR	_ILN_CF_MAX_T[2]	s	
5634	uint	RD/WR	_ILN_CF_MAX_T[3]	s	
5636	uint	RD/WR	_IN_MAX_T	s	
5638	uint	RD/WR	_IM_MAX_T	s	
5640	uint	RD/WR	_IG_MAX_T	s	
5642	uint	RD/WR	_I_SYM_MAX_T	s	
5644	uint	RD/WR	_ILN_OVER_MAX_T[0]	s	
5646	uint	RD/WR	_ILN_OVER_MAX_T[1]	s	

Adresse	Format	RD/WR	Designation	Unit	Note
5648	uint	RD/WR	_ILN_OVER_MAX_T[2]	s	
5650	uint	RD/WR	_ILN_OVER_MAX_T[3]	s	
5652	uint	RD/WR	_ILN_UNDER_MAX_T[0]	s	
5654	uint	RD/WR	_ILN_UNDER_MAX_T[1]	s	
5656	uint	RD/WR	_ILN_UNDER_MAX_T[2]	s	
5658	uint	RD/WR	_ILN_UNDER_MAX_T[3]	s	
5660	uint	RD/WR	_ILN_NEG_PEAK_MAX_T[0]	s	
5662	uint	RD/WR	_ILN_NEG_PEAK_MAX_T[1]	s	
5664	uint	RD/WR	_ILN_NEG_PEAK_MAX_T[2]	s	
5666	uint	RD/WR	_ILN_NEG_PEAK_MAX_T[3]	s	
5668	uint	RD/WR	_ILN_POS_PEAK_MAX_T[0]	s	
5670	uint	RD/WR	_ILN_POS_PEAK_MAX_T[1]	s	
5672	uint	RD/WR	_ILN_POS_PEAK_MAX_T[2]	s	
5674	uint	RD/WR	_ILN_POS_PEAK_MAX_T[3]	s	
5676	uint	RD/WR	_ILN_PEAK_PEAK_MAX_T[0]	s	
5678	uint	RD/WR	_ILN_PEAK_PEAK_MAX_T[1]	s	
5680	uint	RD/WR	_ILN_PEAK_PEAK_MAX_T[2]	s	
5682	uint	RD/WR	_ILN_PEAK_PEAK_MAX_T[3]	s	
5684	uint	RD/WR	_FLI_PF5_MAX_T[0]	s	
5686	uint	RD/WR	_FLI_PF5_MAX_T[1]	s	
5688	uint	RD/WR	_FLI_PF5_MAX_T[2]	s	
5690	uint	RD/WR	_FLI_PF5_MAX_T[3]	s	
5692	uint	RD/WR	_FLI_ST_MAX_T[0]	s	
5694	uint	RD/WR	_FLI_ST_MAX_T[1]	s	
5696	uint	RD/WR	_FLI_ST_MAX_T[2]	s	
5698	uint	RD/WR	_FLI_ST_MAX_T[3]	s	
5700	uint	RD/WR	_FLI_LT_MAX_T[0]	s	
5702	uint	RD/WR	_FLI_LT_MAX_T[1]	s	
5704	uint	RD/WR	_FLI_LT_MAX_T[2]	s	
5706	uint	RD/WR	_FLI_LT_MAX_T[3]	s	
5708	uint	RD/WR	_ILN_RC_MAX_T[0]	s	
5710	uint	RD/WR	_ILN_RC_MAX_T[1]	s	
5712	uint	RD/WR	_ILN_RC_MAX_T[2]	s	
5714	uint	RD/WR	_ILN_RC_MAX_T[3]	s	
5716	uint	RD/WR	_ULL_RC_MAX_T[0]	V	
5718	uint	RD/WR	_ULL_RC_MAX_T[1]	V	
5720	uint	RD/WR	_ULL_RC_MAX_T[2]	V	
5732	uint	RD/WR	_PFLN_MAX_T[0]	s	
5734	uint	RD/WR	_PFLN_MAX_T[1]	s	
5736	uint	RD/WR	_PFLN_MAX_T[2]	s	
5738	uint	RD/WR	_PFLN_MAX_T[3]	s	
5740	uint	RD/WR	_DLN_MAX_T[0]	s	
5742	uint	RD/WR	_DLN_MAX_T[1]	s	
5744	uint	RD/WR	_DLN_MAX_T[2]	s	
5746	uint	RD/WR	_DLN_MAX_T[3]	s	
5748	uint	RD/WR	_KFACT_MAX_T[0]	s	
5750	uint	RD/WR	_KFACT_MAX_T[1]	s	
5752	uint	RD/WR	_KFACT_MAX_T[2]	s	
5754	uint	RD/WR	_KFACT_MAX_T[3]	s	
5756	uint	RD/WR	_S0_POWER_MAX_T[0]	s	
5758	uint	RD/WR	_S0_POWER_MAX_T[1]	s	
5760	uint	RD/WR	_EXT_TEMPERATUR_MAX_T	s	
10987	short	RD/WR	_U_SYM_MAX_T_U0	n	

Maximum values of mean values (float type)

Address	Format	RD/WR	Designation	Unit	Note
5762	float	RD/WR	_ULN_AVG_MAX[0]	V	
5764	float	RD/WR	_ULN_AVG_MAX[1]	V	
5766	float	RD/WR	_ULN_AVG_MAX[2]	V	
5768	float	RD/WR	_ULN_AVG_MAX[3]	V	
5770	float	RD/WR	_ULL_AVG_MAX[0]	V	
5772	float	RD/WR	_ULL_AVG_MAX[1]	V	
5774	float	RD/WR	_ULL_AVG_MAX[2]	V	
5776	float	RD/WR	_ULN_CF_AVG_MAX[0]	%	
5778	float	RD/WR	_ULN_CF_AVG_MAX[1]	%	
5780	float	RD/WR	_ULN_CF_AVG_MAX[2]	%	
5782	float	RD/WR	_ULN_CF_AVG_MAX[3]	%	
5784	float	RD/WR	_ULL_CF_AVG_MAX[0]	%	
5786	float	RD/WR	_ULL_CF_AVG_MAX[1]	%	
5788	float	RD/WR	_ULL_CF_AVG_MAX[2]	%	
5790	float	RD/WR	_UN_AVG_MAX	V	
5792	float	RD/WR	_UM_AVG_MAX	V	
5794	float	RD/WR	_UG_AVG_MAX	V	
5796	float	RD/WR	_URC_AVG_MAX[0]	V	
5798	float	RD/WR	_URC_AVG_MAX[1]	V	
5800	float	RD/WR	_URC_AVG_MAX[2]	V	
5802	float	RD/WR	_URC_AVG_MAX[3]	V	
5804	float	RD/WR	_THD_ULN_AVG_MAX[0]	%	
5806	float	RD/WR	_THD_ULN_AVG_MAX[1]	%	
5808	float	RD/WR	_THD_ULN_AVG_MAX[2]	%	
5810	float	RD/WR	_THD_ULN_AVG_MAX[3]	%	
5812	float	RD/WR	_THD_ZLN_AVG_MAX[0]	%	
5814	float	RD/WR	_THD_ZLN_AVG_MAX[1]	%	
5816	float	RD/WR	_THD_ZLN_AVG_MAX[2]	%	
5818	float	RD/WR	_THD_ZLN_AVG_MAX[3]	%	
5820	float	RD/WR	_ULN_OVER_AVG_MAX[0]	%	
5822	float	RD/WR	_ULN_OVER_AVG_MAX[1]	%	
5824	float	RD/WR	_ULN_OVER_AVG_MAX[2]	%	
5826	float	RD/WR	_ULN_OVER_AVG_MAX[3]	%	
5828	float	RD/WR	_ULN_UNDER_AVG_MAX[0]	%	
5830	float	RD/WR	_ULN_UNDER_AVG_MAX[1]	%	
5832	float	RD/WR	_ULN_UNDER_AVG_MAX[2]	%	
5834	float	RD/WR	_ULN_UNDER_AVG_MAX[3]	%	
5836	float	RD/WR	_ULN_NEG_PEAK_AVG_MAX[0]	V	
5838	float	RD/WR	_ULN_NEG_PEAK_AVG_MAX[1]	V	
5840	float	RD/WR	_ULN_NEG_PEAK_AVG_MAX[2]	V	
5842	float	RD/WR	_ULN_NEG_PEAK_AVG_MAX[3]	V	
5844	float	RD/WR	_ULN_POS_PEAK_AVG_MAX[0]	V	
5846	float	RD/WR	_ULN_POS_PEAK_AVG_MAX[1]	V	
5848	float	RD/WR	_ULN_POS_PEAK_AVG_MAX[2]	V	
5850	float	RD/WR	_ULN_POS_PEAK_AVG_MAX[3]	V	
5852	float	RD/WR	_ULN_PEAK_PEAK_AVG_MAX[0]	V	
5854	float	RD/WR	_ULN_PEAK_PEAK_AVG_MAX[1]	V	
5856	float	RD/WR	_ULN_PEAK_PEAK_AVG_MAX[2]	V	
5858	float	RD/WR	_ULN_PEAK_PEAK_AVG_MAX[3]	V	
5860	float	RD/WR	_THD_ULL_AVG_MAX[0]	%	
5862	float	RD/WR	_THD_ULL_AVG_MAX[1]	%	
5864	float	RD/WR	_THD_ULL_AVG_MAX[2]	%	
5866	float	RD/WR	_THD_ZLL_AVG_MAX[0]	%	
5868	float	RD/WR	_THD_ZLL_AVG_MAX[1]	%	
5870	float	RD/WR	_THD_ZLL_AVG_MAX[2]	%	
5872	float	RD/WR	_ULL_OVER_AVG_MAX[0]	%	
5874	float	RD/WR	_ULL_OVER_AVG_MAX[1]	%	
5876	float	RD/WR	_ULL_OVER_AVG_MAX[2]	%	

Adresse	Format	RD/WR	Designation	Unit	Note
5878	float	RD/WR	_ULL_UNDER_AVG_MAX[0]	%	
5880	float	RD/WR	_ULL_UNDER_AVG_MAX[1]	%	
5882	float	RD/WR	_ULL_UNDER_AVG_MAX[2]	%	
5884	float	RD/WR	_ULL_NEG_PEAK_AVG_MAX[0]	V	
5886	float	RD/WR	_ULL_NEG_PEAK_AVG_MAX[1]	V	
5888	float	RD/WR	_ULL_NEG_PEAK_AVG_MAX[2]	V	
5890	float	RD/WR	_ULL_POS_PEAK_AVG_MAX[0]	V	
5892	float	RD/WR	_ULL_POS_PEAK_AVG_MAX[1]	V	
5894	float	RD/WR	_ULL_POS_PEAK_AVG_MAX[2]	V	
5896	float	RD/WR	_ULL_PEAK_PEAK_AVG_MAX[0]	V	
5898	float	RD/WR	_ULL_PEAK_PEAK_AVG_MAX[1]	V	
5900	float	RD/WR	_ULL_PEAK_PEAK_AVG_MAX[2]	V	
5902	float	RD/WR	_U_STERN_AVG_MAX	V	
5904	float	RD/WR	_U_SYM_AVG_MAX	%	
5906	float	RD/WR	_FREQ_AVG_MAX	Hz	
5908	float	RD/WR	_NORM_FREQ_AVG_MAX	Hz	
5910	float	RD/WR	_PLN_AVG_MAX[0]	W	
5912	float	RD/WR	_PLN_AVG_MAX[1]	W	
5914	float	RD/WR	_PLN_AVG_MAX[2]	W	
5916	float	RD/WR	_PLN_AVG_MAX[3]	W	
5918	float	RD/WR	_P_SUM_AVG_MAX	W	
5920	float	RD/WR	_Q_SUM_AVG_MAX	Var	
5922	float	RD/WR	_QLN_AVG_MAX[0]	Var	
5924	float	RD/WR	_QLN_AVG_MAX[1]	Var	
5926	float	RD/WR	_QLN_AVG_MAX[2]	Var	
5928	float	RD/WR	_QLN_AVG_MAX[3]	Var	
5930	float	RD/WR	_P_SUM3_AVG_MAX	W	
5932	float	RD/WR	_Q_SUM3_AVG_MAX	Var	
5934	float	RD/WR	_ILN_AVG_MAX[0]	A	
5936	float	RD/WR	_ILN_AVG_MAX[1]	A	
5938	float	RD/WR	_ILN_AVG_MAX[2]	A	
5940	float	RD/WR	_ILN_AVG_MAX[3]	A	
5942	float	RD/WR	_SLN_AVG_MAX[0]	VA	
5944	float	RD/WR	_SLN_AVG_MAX[1]	VA	
5946	float	RD/WR	_SLN_AVG_MAX[2]	VA	
5948	float	RD/WR	_SLN_AVG_MAX[3]	VA	
5950	float	RD/WR	_I_SUM3_AVG_MAX	A	
5952	float	RD/WR	_I_SUM_AVG_MAX	A	
5954	float	RD/WR	_S_SUM3_AVG_MAX	VA	
5956	float	RD/WR	_S_SUM_AVG_MAX	VA	
5958	float	RD/WR	_THD_IL_AVG_MAX[0]	%	
5960	float	RD/WR	_THD_IL_AVG_MAX[1]	%	
5962	float	RD/WR	_THD_IL_AVG_MAX[2]	%	
5964	float	RD/WR	_THD_IL_AVG_MAX[3]	%	
5966	float	RD/WR	_ZHD_IL_AVG_MAX[0]	%	
5968	float	RD/WR	_ZHD_IL_AVG_MAX[1]	%	
5970	float	RD/WR	_ZHD_IL_AVG_MAX[2]	%	
5972	float	RD/WR	_ZHD_IL_AVG_MAX[3]	%	
5974	float	RD/WR	_ILN_CF_AVG_MAX[0]	%	
5976	float	RD/WR	_ILN_CF_AVG_MAX[1]	%	
5978	float	RD/WR	_ILN_CF_AVG_MAX[2]	%	
5980	float	RD/WR	_ILN_CF_AVG_MAX[3]	%	
5982	float	RD/WR	_IN_AVG_MAX	A	
5984	float	RD/WR	_IM_AVG_MAX	A	
5986	float	RD/WR	_IG_AVG_MAX	A	
5988	float	RD/WR	_I_SYM_AVG_MAX	%	
5990	float	RD/WR	_ILN_OVER_AVG_MAX[0]	%	
5992	float	RD/WR	_ILN_OVER_AVG_MAX[1]	%	

Address	Format	RD/WR	Designation	Unit	Note
5994	float	RD/WR	_ILN_OVER_AVG_MAX[2]	%	
5996	float	RD/WR	_ILN_OVER_AVG_MAX[3]	%	
5998	float	RD/WR	_ILN_UNDER_AVG_MAX[0]	%	
6000	float	RD/WR	_ILN_UNDER_AVG_MAX[1]	%	
6002	float	RD/WR	_ILN_UNDER_AVG_MAX[2]	%	
6004	float	RD/WR	_ILN_UNDER_AVG_MAX[3]	%	
6006	float	RD/WR	_ILN_NEG_PEAK_AVG_MAX[0]	A	
6008	float	RD/WR	_ILN_NEG_PEAK_AVG_MAX[1]	A	
6010	float	RD/WR	_ILN_NEG_PEAK_AVG_MAX[2]	A	
6012	float	RD/WR	_ILN_NEG_PEAK_AVG_MAX[3]	A	
6014	float	RD/WR	_ILN_POS_PEAK_AVG_MAX[0]	A	
6016	float	RD/WR	_ILN_POS_PEAK_AVG_MAX[1]	A	
6018	float	RD/WR	_ILN_POS_PEAK_AVG_MAX[2]	A	
6020	float	RD/WR	_ILN_POS_PEAK_AVG_MAX[3]	A	
6022	float	RD/WR	_ILN_PEAK_PEAK_AVG_MAX[0]	A	
6024	float	RD/WR	_ILN_PEAK_PEAK_AVG_MAX[1]	A	
6026	float	RD/WR	_ILN_PEAK_PEAK_AVG_MAX[2]	A	
6028	float	RD/WR	_ILN_PEAK_PEAK_AVG_MAX[3]	A	
6030	float	RD/WR	_FLI_PF5_AVG_MAX[0]		
6032	float	RD/WR	_FLI_PF5_AVG_MAX[1]		
6034	float	RD/WR	_FLI_PF5_AVG_MAX[2]		
6036	float	RD/WR	_FLI_PF5_AVG_MAX[3]		
6038	float	RD/WR	_FLI_ST_AVG_MAX[0]		
6040	float	RD/WR	_FLI_ST_AVG_MAX[1]		
6042	float	RD/WR	_FLI_ST_AVG_MAX[2]		
6044	float	RD/WR	_FLI_ST_AVG_MAX[3]		
6046	float	RD/WR	_FLI_LT_AVG_MAX[0]		
6048	float	RD/WR	_FLI_LT_AVG_MAX[1]		
6050	float	RD/WR	_FLI_LT_AVG_MAX[2]		
6052	float	RD/WR	_FLI_LT_AVG_MAX[3]		
6054	float	RD/WR	_ILN_RC_AVG_MAX[0]	A	
6056	float	RD/WR	_ILN_RC_AVG_MAX[1]	A	
6058	float	RD/WR	_ILN_RC_AVG_MAX[2]	A	
6060	float	RD/WR	_ILN_RC_AVG_MAX[3]	A	
6062	float	RD/WR	_ULL_RC_AVG_MAX[0]	V	
6064	float	RD/WR	_ULL_RC_AVG_MAX[1]	V	
6066	float	RD/WR	_ULL_RC_AVG_MAX[2]	V	
6078	float	RD/WR	_PFLN_AVG_MAX[0]	%	
6080	float	RD/WR	_PFLN_AVG_MAX[1]	%	
6082	float	RD/WR	_PFLN_AVG_MAX[2]	%	
6084	float	RD/WR	_PFLN_AVG_MAX[3]	%	
6086	float	RD/WR	_DLN_AVG_MAX[0]	Var	
6088	float	RD/WR	_DLN_AVG_MAX[1]	Var	
6090	float	RD/WR	_DLN_AVG_MAX[2]	Var	
6092	float	RD/WR	_DLN_AVG_MAX[3]	Var	
6094	float	RD/WR	_KFACT_AVG_MAX[0]		
6096	float	RD/WR	_KFACT_AVG_MAX[1]		
6098	float	RD/WR	_KFACT_AVG_MAX[2]		
6100	float	RD/WR	_KFACT_AVG_MAX[3]		
6102	float	RD/WR	_S0_POWER_AVG_MAX[0]	W	
6104	float	RD/WR	_S0_POWER_AVG_MAX[1]	W	
6106	float	RD/WR	_EXT_TEMPERATUR_AVG_MAX	°C	
6108	uint	RD/WR	_THD_ULN_AVG_MAX_T[0]	s	
6110	uint	RD/WR	_THD_ULN_AVG_MAX_T[1]	s	
6112	uint	RD/WR	_THD_ULN_AVG_MAX_T[2]	s	
6114	uint	RD/WR	_THD_ULN_AVG_MAX_T[3]	s	
6116	uint	RD/WR	_ULN_AVG_MAX_T[0]	s	
6118	uint	RD/WR	_ULN_AVG_MAX_T[1]	s	

Adresse	Format	RD/WR	Designation	Unit	Note
6120	uint	RD/WR	_ULN_AVG_MAX_T[2]	s	
6122	uint	RD/WR	_ULN_AVG_MAX_T[3]	s	
6124	uint	RD/WR	_ULL_AVG_MAX_T[0]	s	
6126	uint	RD/WR	_ULL_AVG_MAX_T[1]	s	
6128	uint	RD/WR	_ULL_AVG_MAX_T[2]	s	
6130	uint	RD/WR	_ULN_CF_AVG_MAX_T[0]	s	
6132	uint	RD/WR	_ULN_CF_AVG_MAX_T[1]	s	
6134	uint	RD/WR	_ULN_CF_AVG_MAX_T[2]	s	
6136	uint	RD/WR	_ULN_CF_AVG_MAX_T[3]	s	
6138	uint	RD/WR	_ULL_CF_AVG_MAX_T[0]	s	
6140	uint	RD/WR	_ULL_CF_AVG_MAX_T[1]	s	
6142	uint	RD/WR	_ULL_CF_AVG_MAX_T[2]	s	
6144	uint	RD/WR	_UN_AVG_MAX_T	s	
6146	uint	RD/WR	_UM_AVG_MAX_T	s	
6148	uint	RD/WR	_UG_AVG_MAX_T	s	
6150	uint	RD/WR	_URC_AVG_MAX_T[0]	s	
6152	uint	RD/WR	_URC_AVG_MAX_T[1]	s	
6154	uint	RD/WR	_URC_AVG_MAX_T[2]	s	
6156	uint	RD/WR	_URC_AVG_MAX_T[3]	s	
6158	uint	RD/WR	_THD_ULN_AVG_MAX_T[0]	s	
6160	uint	RD/WR	_THD_ULN_AVG_MAX_T[1]	s	
6162	uint	RD/WR	_THD_ULN_AVG_MAX_T[2]	s	
6164	uint	RD/WR	_THD_ULN_AVG_MAX_T[3]	s	
6166	uint	RD/WR	_THD_ZLN_AVG_MAX_T[0]	s	
6168	uint	RD/WR	_THD_ZLN_AVG_MAX_T[1]	s	
6170	uint	RD/WR	_THD_ZLN_AVG_MAX_T[2]	s	
6172	uint	RD/WR	_THD_ZLN_AVG_MAX_T[3]	s	
6174	uint	RD/WR	_ULN_OVER_AVG_MAX_T[0]	s	
6176	uint	RD/WR	_ULN_OVER_AVG_MAX_T[1]	s	
6178	uint	RD/WR	_ULN_OVER_AVG_MAX_T[2]	s	
6180	uint	RD/WR	_ULN_OVER_AVG_MAX_T[3]	s	
6182	uint	RD/WR	_ULN_UNDER_AVG_MAX_T[0]	s	
6184	uint	RD/WR	_ULN_UNDER_AVG_MAX_T[1]	s	
6186	uint	RD/WR	_ULN_UNDER_AVG_MAX_T[2]	s	
6188	uint	RD/WR	_ULN_UNDER_AVG_MAX_T[3]	s	
6190	uint	RD/WR	_ULN_NEG_PEAK_AVG_MAX_T[0]	s	
6192	uint	RD/WR	_ULN_NEG_PEAK_AVG_MAX_T[1]	s	
6194	uint	RD/WR	_ULN_NEG_PEAK_AVG_MAX_T[2]	s	
6196	uint	RD/WR	_ULN_NEG_PEAK_AVG_MAX_T[3]	s	
6198	uint	RD/WR	_ULN_POS_PEAK_AVG_MAX_T[0]	s	
6200	uint	RD/WR	_ULN_POS_PEAK_AVG_MAX_T[1]	s	
6202	uint	RD/WR	_ULN_POS_PEAK_AVG_MAX_T[2]	s	
6204	uint	RD/WR	_ULN_POS_PEAK_AVG_MAX_T[3]	s	
6206	uint	RD/WR	_ULN_PEAK_PEAK_AVG_MAX_T[0]	s	
6208	uint	RD/WR	_ULN_PEAK_PEAK_AVG_MAX_T[1]	s	
6210	uint	RD/WR	_ULN_PEAK_PEAK_AVG_MAX_T[2]	s	
6212	uint	RD/WR	_ULN_PEAK_PEAK_AVG_MAX_T[3]	s	
6214	uint	RD/WR	_THD_ULL_AVG_MAX_T[0]	s	
6216	uint	RD/WR	_THD_ULL_AVG_MAX_T[1]	s	
6218	uint	RD/WR	_THD_ULL_AVG_MAX_T[2]	s	
6220	uint	RD/WR	_THD_ZLL_AVG_MAX_T[0]	s	
6222	uint	RD/WR	_THD_ZLL_AVG_MAX_T[1]	s	
6224	uint	RD/WR	_THD_ZLL_AVG_MAX_T[2]	s	
6226	uint	RD/WR	_ULL_OVER_AVG_MAX_T[0]	s	
6228	uint	RD/WR	_ULL_OVER_AVG_MAX_T[1]	s	
6230	uint	RD/WR	_ULL_OVER_AVG_MAX_T[2]	s	
6232	uint	RD/WR	_ULL_UNDER_AVG_MAX_T[0]	s	
6234	uint	RD/WR	_ULL_UNDER_AVG_MAX_T[1]	s	

Address	Format	RD/WR	Designation	Unit	Note
6236	uint	RD/WR	_ULL_UNDER_AVG_MAX_T[2]	s	
6238	uint	RD/WR	_ULL_NEG_PEAK_AVG_MAX_T[0]	s	
6240	uint	RD/WR	_ULL_NEG_PEAK_AVG_MAX_T[1]	s	
6242	uint	RD/WR	_ULL_NEG_PEAK_AVG_MAX_T[2]	s	
6244	uint	RD/WR	_ULL_POS_PEAK_AVG_MAX_T[0]	s	
6246	uint	RD/WR	_ULL_POS_PEAK_AVG_MAX_T[1]	s	
6248	uint	RD/WR	_ULL_POS_PEAK_AVG_MAX_T[2]	s	
6250	uint	RD/WR	_ULL_PEAK_PEAK_AVG_MAX_T[0]	s	
6252	uint	RD/WR	_ULL_PEAK_PEAK_AVG_MAX_T[1]	s	
6254	uint	RD/WR	_ULL_PEAK_PEAK_AVG_MAX_T[2]	s	
6256	uint	RD/WR	_U_STERN_AVG_MAX_T	s	
6258	uint	RD/WR	_U_SYM_AVG_MAX_T	s	
6260	uint	RD/WR	_FREQ_AVG_MAX_T	s	
6262	uint	RD/WR	_NORM_FREQ_AVG_MAX_T	s	
6264	uint	RD/WR	_PLN_AVG_MAX_T[0]	s	
6266	uint	RD/WR	_PLN_AVG_MAX_T[1]	s	
6268	uint	RD/WR	_PLN_AVG_MAX_T[2]	s	
6270	uint	RD/WR	_PLN_AVG_MAX_T[3]	s	
6272	uint	RD/WR	_P_SUM_AVG_MAX_T	s	
6274	uint	RD/WR	_Q_SUM_AVG_MAX_T	s	
6276	uint	RD/WR	_QLN_AVG_MAX_T[0]	s	
6278	uint	RD/WR	_QLN_AVG_MAX_T[1]	s	
6280	uint	RD/WR	_QLN_AVG_MAX_T[2]	s	
6282	uint	RD/WR	_QLN_AVG_MAX_T[3]	s	
6284	uint	RD/WR	_P_SUM3_AVG_MAX_T	s	
6286	uint	RD/WR	_Q_SUM3_AVG_MAX_T	s	
6288	uint	RD/WR	_ILN_AVG_MAX_T[0]	s	
6290	uint	RD/WR	_ILN_AVG_MAX_T[1]	s	
6292	uint	RD/WR	_ILN_AVG_MAX_T[2]	s	
6294	uint	RD/WR	_ILN_AVG_MAX_T[3]	s	
6296	uint	RD/WR	_SLN_AVG_MAX_T[0]	s	
6298	uint	RD/WR	_SLN_AVG_MAX_T[1]	s	
6300	uint	RD/WR	_SLN_AVG_MAX_T[2]	s	
6302	uint	RD/WR	_SLN_AVG_MAX_T[3]	s	
6304	uint	RD/WR	_I_SUM3_AVG_MAX_T	s	
6306	uint	RD/WR	_I_SUM_AVG_MAX_T	s	
6308	uint	RD/WR	_S_SUM3_AVG_MAX_T	s	
6310	uint	RD/WR	_S_SUM_AVG_MAX_T	s	
6312	uint	RD/WR	_THD_IL_AVG_MAX_T[0]	s	
6314	uint	RD/WR	_THD_IL_AVG_MAX_T[1]	s	
6316	uint	RD/WR	_THD_IL_AVG_MAX_T[2]	s	
6318	uint	RD/WR	_THD_IL_AVG_MAX_T[3]	s	
6320	uint	RD/WR	_ZHD_IL_AVG_MAX_T[0]	s	
6322	uint	RD/WR	_ZHD_IL_AVG_MAX_T[1]	s	
6324	uint	RD/WR	_ZHD_IL_AVG_MAX_T[2]	s	
6326	uint	RD/WR	_ZHD_IL_AVG_MAX_T[3]	s	
6328	uint	RD/WR	_ILN_CF_AVG_MAX_T[0]	s	
6330	uint	RD/WR	_ILN_CF_AVG_MAX_T[1]	s	
6332	uint	RD/WR	_ILN_CF_AVG_MAX_T[2]	s	
6334	uint	RD/WR	_ILN_CF_AVG_MAX_T[3]	s	
6336	uint	RD/WR	_IN_AVG_MAX_T	s	
6338	uint	RD/WR	_IM_AVG_MAX_T	s	
6340	uint	RD/WR	_IG_AVG_MAX_T	s	
6342	uint	RD/WR	_I_SYM_AVG_MAX_T	s	
6344	uint	RD/WR	_ILN_OVER_AVG_MAX_T[0]	s	
6346	uint	RD/WR	_ILN_OVER_AVG_MAX_T[1]	s	
6348	uint	RD/WR	_ILN_OVER_AVG_MAX_T[2]	s	
6350	uint	RD/WR	_ILN_OVER_AVG_MAX_T[3]	s	

Adresse	Format	RD/WR	Designation	Unit	Note
6352	uint	RD/WR	_ILN_UNDER_AVG_MAX_T[0]	s	
6354	uint	RD/WR	_ILN_UNDER_AVG_MAX_T[1]	s	
6356	uint	RD/WR	_ILN_UNDER_AVG_MAX_T[2]	s	
6358	uint	RD/WR	_ILN_UNDER_AVG_MAX_T[3]	s	
6360	uint	RD/WR	_ILN_NEG_PEAK_AVG_MAX_T[0]	s	
6362	uint	RD/WR	_ILN_NEG_PEAK_AVG_MAX_T[1]	s	
6364	uint	RD/WR	_ILN_NEG_PEAK_AVG_MAX_T[2]	s	
6366	uint	RD/WR	_ILN_NEG_PEAK_AVG_MAX_T[3]	s	
6368	uint	RD/WR	_ILN_POS_PEAK_AVG_MAX_T[0]	s	
6370	uint	RD/WR	_ILN_POS_PEAK_AVG_MAX_T[1]	s	
6372	uint	RD/WR	_ILN_POS_PEAK_AVG_MAX_T[2]	s	
6374	uint	RD/WR	_ILN_POS_PEAK_AVG_MAX_T[3]	s	
6376	uint	RD/WR	_ILN_PEAK_PEAK_AVG_MAX_T[0]	s	
6378	uint	RD/WR	_ILN_PEAK_PEAK_AVG_MAX_T[1]	s	
6380	uint	RD/WR	_ILN_PEAK_PEAK_AVG_MAX_T[2]	s	
6382	uint	RD/WR	_ILN_PEAK_PEAK_AVG_MAX_T[3]	s	
6384	uint	RD/WR	_FLI_PF5_AVG_MAX_T[0]	s	
6386	uint	RD/WR	_FLI_PF5_AVG_MAX_T[1]	s	
6388	uint	RD/WR	_FLI_PF5_AVG_MAX_T[2]	s	
6390	uint	RD/WR	_FLI_PF5_AVG_MAX_T[3]	s	
6392	uint	RD/WR	_FLI_ST_AVG_MAX_T[0]	s	
6394	uint	RD/WR	_FLI_ST_AVG_MAX_T[1]	s	
6396	uint	RD/WR	_FLI_ST_AVG_MAX_T[2]	s	
6398	uint	RD/WR	_FLI_ST_AVG_MAX_T[3]	s	
6400	uint	RD/WR	_FLI_LT_AVG_MAX_T[0]	s	
6402	uint	RD/WR	_FLI_LT_AVG_MAX_T[1]	s	
6404	uint	RD/WR	_FLI_LT_AVG_MAX_T[2]	s	
6406	uint	RD/WR	_FLI_LT_AVG_MAX_T[3]	s	
6408	uint	RD/WR	_ILN_RC_AVG_MAX_T[0]	s	
6410	uint	RD/WR	_ILN_RC_AVG_MAX_T[1]	s	
6412	uint	RD/WR	_ILN_RC_AVG_MAX_T[2]	s	
6414	uint	RD/WR	_ILN_RC_AVG_MAX_T[3]	s	
6416	uint	RD/WR	_ULL_RC_AVG_MAX_T[0]	V	
6418	uint	RD/WR	_ULL_RC_AVG_MAX_T[1]	V	
6420	uint	RD/WR	_ULL_RC_AVG_MAX_T[2]	V	
6432	uint	RD/WR	_PFLN_AVG_MAX_T[0]	%	
6434	uint	RD/WR	_PFLN_AVG_MAX_T[1]	%	
6436	uint	RD/WR	_PFLN_AVG_MAX_T[2]	%	
6438	uint	RD/WR	_PFLN_AVG_MAX_T[3]	%	
6440	uint	RD/WR	_DLN_AVG_MAX_T[0]	s	
6442	uint	RD/WR	_DLN_AVG_MAX_T[1]	s	
6444	uint	RD/WR	_DLN_AVG_MAX_T[2]	s	
6446	uint	RD/WR	_DLN_AVG_MAX_T[3]	s	
6448	uint	RD/WR	_KFACT_AVG_MAX_T[0]	s	
6450	uint	RD/WR	_KFACT_AVG_MAX_T[1]	s	
6452	uint	RD/WR	_KFACT_AVG_MAX_T[2]	s	
6454	uint	RD/WR	_KFACT_AVG_MAX_T[3]	s	
6456	uint	RD/WR	_S0_POWER_AVG_MAX_T[0]	s	
6458	uint	RD/WR	_S0_POWER_AVG_MAX_T[1]	s	
6460	uint	RD/WR	_EXT_TEMPERATUR_AVG_MAX_T	s	

Other values

Address	Format	RD/WR	Designation	Unit	Note
6628	float	RD	_SPU012	V	Star connection voltage
6630	short	RD/WR	_DIGOUT_STAT[0]	n	Status Digital Output 1, 0=not active, 1=active
6631	short	RD/WR	_DIGOUT_STAT[1]	n	Status Digital Output 2, 0=not active, 1=active
6632	short	RD	_DIGIN_STAT[0]	n	Status Digital Input 1, 0=not active, 1=active
6633	short	RD	_DIGIN_STAT[1]	n	Status Digital Input 2, 0=not active, 1=active
6634	uint	RD/WR	_EVT_COUNT	n	Event counter
6636	uint	RD/WR	_FLAG_COUNT	n	Flag counter
6638	uint	RD/WR	_TRANS_COUNT	n	Error counter, transients
6640	uint	RD/WR	_HWW_COUNT	n	Error counter, half-cycle effective values
6642	uint	RD/WR	_RX232_COUNT	n	Error counter, receive RS232
6644	uint	RD/WR	_TX232_COUNT	n	Error counter, send RS232
6646	uint	RD/WR	_ERR232_COUNT	n	Error counter, RS232
6648	uint	RD/WR	_RX485_COUNT	n	Error counter, receive RS485
6650	uint	RD/WR	_TX485_COUNT	n	Error counter, send RS485
6652	uint	RD/WR	_ERR485_COUNT	n	Error counter, RS485
6654	short	RD/WR	_DEL_WH		1= Delete all real energy counters
6655	short	RD/WR	_DEL_QH		1= Delete all reactive energy counters
6656	short	RD/WR	_INIT_MAX		Only for internal use
6657	string	RD/WR	_RUN 64		Only for internal use
6689	float	RD/WR	_CTPRIM[0]	A	L1; Current transformer, primary
6691	floatat	RD/WR	_CTPRIM[1]	A	L2; Current transformer, primary
6693	floatat	RD/WR	_CTPRIM[2]	A	L3; Current transformer, primary
6695	floatat	RD/WR	_CTPRIM[3]	A	L4; Current transformer, primary
6697	floatat	RD/WR	_CTSEC[0]	A	L1; Current transformer, secondary
6699	floatat	RD/WR	_CTSEC[1]	A	L2; Current transformer, secondary
6701	floatat	RD/WR	_CTSEC[2]	A	L3; Current transformer, secondary
6703	floatat	RD/WR	_CTSEC[3]	A	L4; Current transformer, secondary
6705	floatat	RD/WR	_VTPRIM[0]	V	L1; Voltage transformer, primary
6707	floatat	RD/WR	_VTPRIM[1]	V	L2; Voltage transformer, primary
6709	floatat	RD/WR	_VTPRIM[2]	V	L3; Voltage transformer, primary
6711	floatat	RD/WR	_VTPRIM[3]	V	L4; Voltage transformer, primary
6713	floatat	RD/WR	_VTSEC[0]	V	L1; Voltage transformer, sekundär
6715	floatat	RD/WR	_VTSEC[1]	V	L2; Voltage transformer, sekundär
6717	floatat	RD/WR	_VTSEC[2]	V	L3; Voltage transformer, sekundär
6719	floatat	RD/WR	_VTSEC[3]	V	L4; Voltage transformer, sekundär
6721	floatat	RD/WR	_IRATED[0]	A	Nominal current transformer; I L1
6723	floatat	RD/WR	_IRATED[1]	A	Nominal current transformer; I L2
6725	floatat	RD/WR	_IRATED[2]	A	Nominal current transformer; I L3
6727	floatat	RD/WR	_IRATED[3]	A	Nominal current transformer; I L4
6729	floatat	RD/WR	_NOMINAL_U[0]	V	Nominal voltage; L1
6731	floatat	RD/WR	_NOMINAL_U[1]	V	Nominal voltage; L2
6733	floatat	RD/WR	_NOMINAL_U[2]	V	Nominal voltage; L3
6735	floatat	RD/WR	_NOMINAL_U[3]	V	Nominal voltage; L4
6737	floatat	RD/WR	_NOMINAL_I[0]	A	Nominal current; L1
6739	floatat	RD/WR	_NOMINAL_I[1]	A	Nominal current; L2
6741	floatat	RD/WR	_NOMINAL_I[2]	A	Nominal current; L3
6743	floatat	RD/WR	_NOMINAL_I[3]	A	Nominal current; L4
6745	floatat	RD/WR	_TRNS_DELTA[0]	%	Only for internal use
6747	floatat	RD/WR	_TRNS_DELTA[1]	%	Only for internal use
6749	floatat	RD/WR	_TRNS_DELTA[2]	%	Only for internal use
6751	floatat	RD/WR	_TRNS_DELTA[3]	%	Only for internal use
6753	floatat	RD/WR	_TRNS_I_ABS[0]	%	Only for internal use
6755	floatat	RD/WR	_TRNS_I_ABS[1]	%	Only for internal use
6757	floatat	RD/WR	_TRNS_I_ABS[2]	%	Only for internal use

Adresse	Format	RD/WR	Designation	Unit	Note
6759	float	RD/WR	_TRNS_I_ABS[3]	%	Only for internal use
6761	float	RD/WR	_TRNS_U_ABS[0]	%	Only for internal use
6763	float	RD/WR	_TRNS_U_ABS[1]	%	Only for internal use
6765	float	RD/WR	_TRNS_U_ABS[2]	%	Only for internal use
6767	float	RD/WR	_TRNS_U_ABS[3]	%	Only for internal use
6769	float	RD/WR	_I_EVT_MAX[0]	%	Only for internal use
6771	float	RD/WR	_I_EVT_MAX[1]	%	Only for internal use
6773	float	RD/WR	_I_EVT_MAX[2]	%	Only for internal use
6775	float	RD/WR	_I_EVT_MAX[3]	%	Only for internal use
6777	float	RD/WR	_U_EVT_MAX[0]	%	Only for internal use
6779	float	RD/WR	_U_EVT_MAX[1]	%	Only for internal use
6781	float	RD/WR	_U_EVT_MAX[2]	%	Only for internal use
6783	float	RD/WR	_U_EVT_MAX[3]	%	Only for internal use
6785	float	RD/WR	_U_EVT_MIN[0]	%	Only for internal use
6787	float	RD/WR	_U_EVT_MIN[1]	%	Only for internal use
6789	float	RD/WR	_U_EVT_MIN[2]	%	Only for internal use
6791	float	RD/WR	_U_EVT_MIN[3]	%	Only for internal use
6793	float	RD/WR	_U_EVT_OFF[0]	%	Only for internal use
6795	float	RD/WR	_U_EVT_OFF[1]	%	Only for internal use
6797	float	RD/WR	_U_EVT_OFF[2]	%	Only for internal use
6799	float	RD/WR	_U_EVT_OFF[3]	%	Only for internal use
6801	float	RD/WR	_NOMINAL_F	Hz	Nominal frequency 50Hz or 60Hz
6803	short	RD/WR	_FLICKER_SYSTEM		Only for internal use
6804	short	RD/WR	_TRNS_PRE	n	Only for internal use
6805	short	RD/WR	_TRNS_POST	n	Only for internal use
6806	string	RD/WR	_DEV_NAME	64	Only for internal use
6838	string	RD/WR	_DEV_DESC	128	Only for internal use
6902	string	RD/WR	_LANGUAGE	16	Only for internal use
6912	uint	RD	_SERNR		Only for internal use
6914	uint	RD	_PRODNR		Only for internal use
6916	int	RD/WR	_MBUSADDR		Only for internal use
6918	int	RD/WR	_MODE485		Only for internal use
6920	int	RD/WR	_BAUD485		Only for internal use
6922	int	RD/WR	_BAUD232		Only for internal use
6924	int	RD/WR	_MODE232		Only for internal use
6926	uint	RD/WR	_IP_ADDR		Network address
6928	uint	RD/WR	_IP_MASK		Network mask
6930	uint	RD/WR	_IP_GATE		Gateway
6932	int	RD/WR	_DHCPMODE		1=DHCP on, 0=DHCP off
6934	int	RD/WR	_CONTRAST		Contrast
6936	int	RD/WR	_THERMOELEMENT		Type temperature sensor
6938	int	RD/WR	_KEY1		Status, button 1
6940	int	RD/WR	_KEY2		Status, button 2
6942	int	RD/WR	_KEY3		Status, button 3
6944	uint	RD/WR	_DEBUG_IP		Only for internal use
6946	int	RD/WR	_TIME_ZONE	s	Time zone
6948	int	RD/WR	_STIME	s	Only for internal use
6950	short	RD/WR	_SDAY		Start day of summer/winter switchover (spring)
6951	short	RD/WR	_SHOUR	h	Start hour of summer/winter switchover
6952	short	RD/WR	_SMON		Start month of summer/winter switchover
6953	short	RD/WR	_SMIN	min	Start minute of summer/winter switchover
6954	short	RD/WR	_SDOW		Summer/winter switchover (spring)
6955	short	RD/WR	_EDAY		Start day of summer/winter switchover (autumn)
6956	short	RD/WR	_EHOUR	h	Start hour of summer/winter switchover
6957	short	RD/WR	_EMON		Start month of summer/winter switchover

Address	Format	RD/WR	Designation	Unit	Note
6958	short	RD/WR	_EMIN	min	Start minute of summer/winter switchover
6959	short	RD/WR	_EDOW		Only for internal use
6960	short		_KALIB_I[0]		Only for internal use
6961	short		_KALIB_I[1]		Only for internal use
6962	short		_KALIB_I[2]		Only for internal use
6963	short		_KALIB_I[3]		Only for internal use
6964	short		_KALIB_U[0]		Only for internal use
6965	short		_KALIB_U[1]		Only for internal use
6966	short		_KALIB_U[2]		Only for internal use
6967	short		_KALIB_U[3]		Only for internal use
6968	short	RD/WR	_EVT_VAL_PRE	n	Only for internal use
6969	short	RD/WR	_EVT_VAL_POST	n	Only for internal use
6970	int	RD/WR	_TRNS_MODE	n	Only for internal use
6972	int	RD/WR	_EVT_MODE	n	Only for internal use
6974	int	RD/WR	_CON_AUX_MODE	n	Only for internal use
6976	int	RD/WR	_CON_MODE	n	Only for internal use
6978	int	RD/WR	_PHASE_MODE	n	Only for internal use
6980	short	RD/WR	_COLOR[0]	n	Only for internal use
6981	short	RD/WR	_COLOR[1]	n	Only for internal use
6982	short	RD/WR	_COLOR[2]	n	Only for internal use
6983	short	RD/WR	_COLOR[3]	n	Only for internal use
6984	short	RD/WR	_COLOR[4]	n	Only for internal use
6985	short	RD/WR	_COLOR[5]	n	Only for internal use
6986	short	RD/WR	_COLOR[6]	n	Only for internal use
6987	short	RD/WR	_COLOR[7]	n	Only for internal use
6988	string	RD/WR	_GUEST_PASSWD	64	Only for internal use
7020	string	RD/WR	_USER_PASSWD	64	Only for internal use
7052	string	RD/WR	_ADMIN_PASSWD	64	Only for internal use
7084	float	RD/WR	_PULSWERT[0]	Wh/n	Only for internal use
7086	float	RD/WR	_PULSWERT[1]	Wh/n	Only for internal use
7088	float	RD/WR	_MAXSIZE_REC	%	Only for internal use
7090	float	RD/WR	_MAXSIZE_TRNS	%	Only for internal use
7092	float	RD/WR	_MAXSIZE_VWW	%	Only for internal use
7094	float	RD/WR	_MAXSIZE_EVT	%	Only for internal use
7096	float	RD/WR	_MAXSIZE_FLAGS	%	Only for internal use
7098	int	RD/WR	_TFTP_FILE_NR	n	Only for internal use
7100	int	RD/WR	_TFTP_NEWFILE	n	Only for internal use
7102	int	RD/WR	_DIGOUTEVT[0]	bin	Only for internal use
7104	int	RD/WR	_DIGOUTEVT[1]	bin	Only for internal use
7106	int	RD/WR	_DIGOUTEVT_TIME[0]	0.01s	Only for internal use
7108	int	RD/WR	_DIGOUTEVT_TIME[1]	0.01s	Only for internal use
7110	short	RD/WR	_INVERT_DIGOUT[0]	bool	Only for internal use
7111	short	RD/WR	_INVERT_DIGOUT[1]	bool	Only for internal use
8914	int	RD	_KORR_INT		Only for internal use
8916	int	RD/WR	_QUARZ_KORR_NTP ppm		Only for internal use
8918	int	RD/WR	_BACNET_SENDIAM_TIME	s	Only for internal use
8920	short	RD/WR	_HTML_PORT		Only for internal use
8921	string	RD/WR	_IP_ADDR_STR	32	Only for internal use
8937	string	RD/WR	_IP_GATEWAY_STR	32	Only for internal use
8953	string	RD/WR	_IP_MASK_STR	32	Only for internal use
8969	string	RD/WR	_NAMESRV_IP	32	Only for internal use
8985	string	RD/WR	_NTPSRV_IP	128	Only for internal use
9049	string	RD/WR	_HOSTNAME	64	Only for internal use
9081	string	RD/WR	_EVT_NAME	16	Only for internal use
9089	string	RD/WR	_FL_NAME	16	Only for internal use
9097	string	RD/WR	_TR_NAME	16	Only for internal use
9105	string	RD/WR	_HWW_NAME	16	Only for internal use
9113	int	RD/WR	___FILEMAGIC		Only for internal use

Adresse	Format	RD/WR	Designation	Unit	Note
9115	int	RD/WR	_MODE_NTP		Only for internal use
9117	int	RD/WR	_QUARZ_KORR	ppm	Only for internal use
9119	float	RD/WR	_RC_FREQ	Hz	Only for internal use
9121	string	RD/WR	_TFTP_PRG1	256	Only for internal use
9249	string	RD/WR	_TFTP_PRG2	256	Only for internal use
9377	string	RD/WR	_TFTP_PRG3	256	Only for internal use
9505	string	RD/WR	_TFTP_PRG4	256	Only for internal use
9633	string	RD/WR	_TFTP_PRG5	256	Only for internal use
9761	string	RD/WR	_TFTP_PRG6	256	Only for internal use
9889	string	RD/WR	_TFTP_REC	256	Only for internal use
10017	string	RD/WR	_TFTP_DISPLAY	256	Only for internal use
10145	string	RD	_RELEASE	16	Only for internal use
10153	string	RD/WR	_DOWNLOAD	64	Only for internal use
10185	int		_DUMMY		Only for internal use
10187	uint	RD/WR	_MASTER_TIMEOUT	msec	Only for internal use
10189	int	RD/WR	_ED_PASSWD		Only for internal use
10191	int	RD/WR	_HTML_PASSWD		Only for internal use
10193	int	RD/WR	_PASSWD_MODE		Only for internal use
10195	float	RD	_CHALLENGE		Only for internal use
10197	uint	RD	_EMAX_PASSWORD		Only for internal use
10199	uint	RD	_BACNET_PASSWORD		Only for internal use
10201	short	RD	_FORBID_HTML		Only for internal use
10202	short	RD	_FORBID_CFG_HTML		Only for internal use
10203	short	RD	_FORBID_FTP		Only for internal use
10204	short	RD	_FORBID_CFG_FTP		Only for internal use
10205	short	RD	_FORBID_MODETH		Only for internal use
10206	short	RD	_FORBID_CFG_MODETH		Only for internal use
10207	short	RD	_FORBID_BACNET		Only for internal use
10208	short	RD	_IP_UP		Only for internal use
10209	short	RD	_SYSVAR_CNT		Only for internal use
10210	string	RD/WR	_SEQ_IP0	32	Only for internal use
10226	string	RD/WR	_SEQ_IP1	32	Only for internal use
10242	string	RD/WR	_SEQ_IP2	32	Only for internal use
10258	string	RD/WR	_SEQ_IP3	32	Only for internal use
10274	string	RD/WR	_SEQ_IP4	32	Only for internal use
10290	string	RD/WR	_SEQ_IP5	32	Only for internal use
10306	string	RD/WR	_SEQ_IP6	32	Only for internal use
10322	string	RD/WR	_SEQ_IP7	32	Only for internal use
10338	short	RD/WR	_CH_MAP[0]		Only for internal use
10339	short	RD/WR	_CH_MAP[1]		Only for internal use
10340	short	RD/WR	_CH_MAP[2]		Only for internal use
10341	short	RD/WR	_CH_MAP[3]		Only for internal use
10342	short	RD/WR	_CH_MAP[4]		Only for internal use
10343	short	RD/WR	_CH_MAP[5]		Only for internal use
10344	short	RD/WR	_CH_MAP[6]		Only for internal use
10345	short	RD/WR	_CH_MAP[7]		Only for internal use
10346	float	RD	_NTP_DIV	s	Only for internal use
10348	float	RD	_NTP_TURNAROUND	s	Only for internal use
10350	float	RD	_NTP_KORR	ppm	Only for internal use
10352	long64	RD/WR	_RX_ETH_COUNT		Only for internal use
10356	long64	RD/WR	_TX_ETH_COUNT		Only for internal use
10360	long64	RD/WR	_ERR_ETH_COUNT		Only for internal use
10364	long64	RD/WR	_RX_NTP_COUNT		Only for internal use
10368	long64	RD/WR	_TX_NTP_COUNT		Only for internal use
10372	long64	RD/WR	_ERR_NTP_COUNT		Only for internal use
10376	long64	RD/WR	_RX_DNS_COUNT		Only for internal use
10380	long64	RD/WR	_TX_DNS_COUNT		Only for internal use
10384	long64	RD/WR	_ERR_DNS_COUNT		Only for internal use

Address	Format	RD/WR	Designation	Unit	Note
10388	long64	RD/WR	_RX_DHCP_COUNT		Only for internal use
10392	long64	RD/WR	_TX_DHCP_COUNT		Only for internal use
10396	long64	RD/WR	_ERR_DHCP_COUNT		Only for internal use
10400	long64	RD/WR	_TX_EMAIL_COUNT		Only for internal use
10404	long64	RD/WR	_ERR_EMAIL_COUNT		Only for internal use
10408	int	RD/WR	_MTU_SIZE		Only for internal use
10410	long64	RD	_SYSTIMEUP	10ms	Only for internal use
10414	dfloat	RD	_WH_V_T3[0]	Wh	Real energy, consumption, tariff 3, L1
10416	dfloat	RD	_WH_V_T3[1]	Wh	Real energy, consumption, tariff 3, L2
10418	dfloat	RD	_WH_V_T3[2]	Wh	Real energy, consumption, tariff 3, L3
10420	dfloat	RD	_WH_V_T3[3]	Wh	Real energy, consumption, tariff 3, L4
10422	dfloat	RD	_WH_V_T3[4]	Wh	Real energy, consump., tariff 3, L1..L3
10424	dfloat	RD	_WH_V_T3[5]	Wh	Real energy, consump., tariff 3, L1..L4
10426	dfloat	RD	_WH_V_T4[0]	Wh	Real energy, consumption, tariff 4, L1
10428	dfloat	RD	_WH_V_T4[1]	Wh	Real energy, consumption, tariff 4, L2
10430	dfloat	RD	_WH_V_T4[2]	Wh	Real energy, consumption, tariff 4, L3
10432	dfloat	RD	_WH_V_T4[3]	Wh	Real energy, consumption, tariff 4, L4
10434	dfloat	RD	_WH_V_T4[4]	Wh	Real energy, consump., tariff 4, L1..L3
10436	dfloat	RD	_WH_V_T4[5]	Wh	Real energy, consump., tariff 4, L1..L4
10438	dfloat	RD	_WH_Z_T3[0]	Wh	Real energy, supply, tariff 3, L1
10440	dfloat	RD	_WH_Z_T3[1]	Wh	Real energy, supply, tariff 3, L2
10442	dfloat	RD	_WH_Z_T3[2]	Wh	Real energy, supply, tariff 3, L3
10444	dfloat	RD	_WH_Z_T3[3]	Wh	Real energy, supply, tariff 3, L4
10446	dfloat	RD	_WH_Z_T3[4]	Wh	Real energy, supply, tariff 3, L1..L3
10448	dfloat	RD	_WH_Z_T3[5]	Wh	Real energy, supply, tariff 3, L1..L4
10450	dfloat	RD	_WH_Z_T4[0]	Wh	Real energy, supply, tariff 4, L1
10452	dfloat	RD	_WH_Z_T4[1]	Wh	Real energy, supply, tariff 4, L2
10454	dfloat	RD	_WH_Z_T4[2]	Wh	Real energy, supply, tariff 4, L3
10456	dfloat	RD	_WH_Z_T4[3]	Wh	Real energy, supply, tariff 4, L4
10458	dfloat	RD	_WH_Z_T4[4]	Wh	Real energy, supply, tariff 4, L1..L3
10460	dfloat	RD	_WH_Z_T4[5]	Wh	Real energy, supply, tariff 4, L1..L4
10462	dfloat	RD	_IQH_T3[0]	varh	Reactive energy, induktiv, tariff 3, L1
10464	dfloat	RD	_IQH_T3[1]	varh	Reactive energy, induktiv, tariff 3, L2
10466	dfloat	RD	_IQH_T3[2]	varh	Reactive energy, induktiv, tariff 3, L3
10468	dfloat	RD	_IQH_T3[3]	varh	Reactive energy, induktiv, tariff 3, L4
10470	dfloat	RD	_IQH_T3[4]	varh	Reactive energy, induktiv, tariff 3, L1..L3
10472	dfloat	RD	_IQH_T3[5]	varh	Reactive energy, induktiv, tariff 3, L1..L4
10474	dfloat	RD	_IQH_T4[0]	varh	Reactive energy, induktiv, tariff 4, L1
10476	dfloat	RD	_IQH_T4[1]	varh	Reactive energy, induktiv, tariff 4, L2
10478	dfloat	RD	_IQH_T4[2]	varh	Reactive energy, induktiv, tariff 4, L3
10480	dfloat	RD	_IQH_T4[3]	varh	Reactive energy, induktiv, tariff 4, L4
10482	dfloat	RD	_IQH_T4[4]	varh	Reactive energy, induktiv, tariff 3, L1..L3
10484	dfloat	RD	_IQH_T4[5]	varh	Reactive energy, induktiv, tariff 3, L1..L4
10486	float	RD/WR	_SNMP_USERVAR[0]		Only for internal use
10488	float	RD/WR	_SNMP_USERVAR[1]		Only for internal use
10490	float	RD/WR	_SNMP_USERVAR[2]		Only for internal use
10492	float	RD/WR	_SNMP_USERVAR[3]		Only for internal use
10494	float	RD/WR	_SNMP_USERVAR[4]		Only for internal use
10496	float	RD/WR	_SNMP_USERVAR[5]		Only for internal use
10498	float	RD/WR	_SNMP_USERVAR[6]		Only for internal use
10500	float	RD/WR	_SNMP_USERVAR[7]		Only for internal use
10502	float	RD/WR	_SNMP_USERVAR[8]		Only for internal use
10504	float	RD/WR	_SNMP_USERVAR[9]		Only for internal use
10506	float	RD/WR	_SNMP_USERVAR[10]		Only for internal use
10508	float	RD/WR	_SNMP_USERVAR[11]		Only for internal use
10510	float	RD/WR	_SNMP_USERVAR[12]		Only for internal use
10512	float	RD/WR	_SNMP_USERVAR[13]		Only for internal use
10514	float	RD/WR	_SNMP_USERVAR[14]		Only for internal use

Adresse	Format	RD/WR	Designation	Unit	Note
10516	float	RD/WR	_SNMP_USERVAR[15]		Only for internal use
10518	double	RD	_AKT_EVT_START[0]	s	Only for internal use
10522	double	RD	_AKT_EVT_START[1]	s	Only for internal use
10526	double	RD	_AKT_EVT_START[2]	s	Only for internal use
10530	double	RD	_AKT_EVT_START[3]	s	Only for internal use
10534	double	RD	_AKT_EVT_START[4]	s	Only for internal use
10538	double	RD	_AKT_EVT_START[5]	s	Only for internal use
10542	double	RD	_AKT_EVT_START[6]	s	Only for internal use
10546	double	RD	_AKT_EVT_START[7]	s	Only for internal use
10550	double	RD	_AKT_EVT_STOP[0]	s	Only for internal use
10554	double	RD	_AKT_EVT_STOP[1]	s	Only for internal use
10558	double	RD	_AKT_EVT_STOP[2]	s	Only for internal use
10562	double	RD	_AKT_EVT_STOP[3]	s	Only for internal use
10566	double	RD	_AKT_EVT_STOP[4]	s	Only for internal use
10570	double	RD	_AKT_EVT_STOP[5]	s	Only for internal use
10574	double	RD	_AKT_EVT_STOP[6]	s	Only for internal use
10578	double	RD	_AKT_EVT_STOP[7]	s	Only for internal use
10582	float	RD	_AKT_EVT_BOUND[0]		Only for internal use
10584	float	RD	_AKT_EVT_BOUND[1]		Only for internal use
10586	float	RD	_AKT_EVT_BOUND[2]		Only for internal use
10588	float	RD	_AKT_EVT_BOUND[3]		Only for internal use
10590	float	RD	_AKT_EVT_BOUND[4]		Only for internal use
10592	float	RD	_AKT_EVT_BOUND[5]		Only for internal use
10594	float	RD	_AKT_EVT_BOUND[6]		Only for internal use
10596	float	RD	_AKT_EVT_BOUND[7]		Only for internal use
10598	float	RD	_AKT_EVT_MAXVAL[0]		Only for internal use
10600	float	RD	_AKT_EVT_MAXVAL[1]		Only for internal use
10602	float	RD	_AKT_EVT_MAXVAL[2]		Only for internal use
10604	float	RD	_AKT_EVT_MAXVAL[3]		Only for internal use
10606	float	RD	_AKT_EVT_MAXVAL[4]		Only for internal use
10608	float	RD	_AKT_EVT_MAXVAL[5]		Only for internal use
10610	float	RD	_AKT_EVT_MAXVAL[6]		Only for internal use
10612	float	RD	_AKT_EVT_MAXVAL[7]		Only for internal use
10614	float	RD	_AKT_EVT_MINVAL[0]		Only for internal use
10616	float	RD	_AKT_EVT_MINVAL[1]		Only for internal use
10618	float	RD	_AKT_EVT_MINVAL[2]		Only for internal use
10620	float	RD	_AKT_EVT_MINVAL[3]		Only for internal use
10622	float	RD	_AKT_EVT_MINVAL[4]		Only for internal use
10624	float	RD	_AKT_EVT_MINVAL[5]		Only for internal use
10626	float	RD	_AKT_EVT_MINVAL[6]		Only for internal use
10628	float	RD	_AKT_EVT_MINVAL[7]		Only for internal use
10630	float	RD	_AKT_EVT_AVG[0]		Only for internal use
10632	float	RD	_AKT_EVT_AVG[1]		Only for internal use
10634	float	RD	_AKT_EVT_AVG[2]		Only for internal use
10636	float	RD	_AKT_EVT_AVG[3]		Only for internal use
10638	float	RD	_AKT_EVT_AVG[4]		Only for internal use
10640	float	RD	_AKT_EVT_AVG[5]		Only for internal use
10642	float	RD	_AKT_EVT_AVG[6]		Only for internal use
10644	float	RD	_AKT_EVT_AVG[7]		Only for internal use
10646	int	RD	_AKT_EVT_REASON[0]		Only for internal use
10648	int	RD	_AKT_EVT_REASON[1]		Only for internal use
10650	int	RD	_AKT_EVT_REASON[2]		Only for internal use
10652	int	RD	_AKT_EVT_REASON[3]		Only for internal use
10654	int	RD	_AKT_EVT_REASON[4]		Only for internal use
10656	int	RD	_AKT_EVT_REASON[5]		Only for internal use
10658	int	RD	_AKT_EVT_REASON[6]		Only for internal use
10660	int	RD	_AKT_EVT_REASON[7]		Only for internal use
10662	int	RD	_AKT_EVT_CNT[0]		Only for internal use

Address	Format	RD/WR	Designation	Unit	Note
10664	int	RD	_AKT_EVT_CNT[1]		Only for internal use
10666	int	RD	_AKT_EVT_CNT[2]		Only for internal use
10668	int	RD	_AKT_EVT_CNT[3]		Only for internal use
10670	int	RD	_AKT_EVT_CNT[4]		Only for internal use
10672	int	RD	_AKT_EVT_CNT[5]		Only for internal use
10674	int	RD	_AKT_EVT_CNT[6]		Only for internal use
10676	int	RD	_AKT_EVT_CNT[7]		Only for internal use
10678	int	RD/WR	_HW_INDEX		Only for internal use
10944	int	RD/WR	_SET_BACNAME_INSTACE		Only for internal use
10946	short	RD/WR	_PULS_WIDTH		Impulse output, impulse width (1=10ms...)
10947	float	RD/WR	_TRNS_ENVELOPE[0]	%	Transients (envelope), threshold value, L1
10949	float	RD/WR	_TRNS_ENVELOPE[1]	%	Transients (envelope), threshold value, L2
10951	float	RD/WR	_TRNS_ENVELOPE[2]	%	Transients (envelope), threshold value, L3
10953	float	RD/WR	_TRNS_ENVELOPE[3]	%	Transients (envelope), threshold value, L4
10971	int	RD/WR	_LCD_LED_MAX		LCD, backlight, max. brightness
10973	int	RD/WR	_LCD_LED_MIN		LCD, backlight, min. brightness
10975	int	RD/WR	_LCD_LED_ONTIME		LCD, backlight, ontime
10988	uint	RD/WR	_MB_STATUS	n	Status measuring range, overrange
10990	int	RD/WR	_SET_SYSTIME	sec	System time (UTC)
10992	string	RD	_SNMP_OID	32	SNMP ID
11008	ushort	RD/WR	_SMTP_PORT	n	SMTP, port number
11009	float	RD/WR	_I_EVT_MAX_HYST[0]	%	Event, hysteresis, overcurrent, IL1
11011	float	RD/WR	_I_EVT_MAX_HYST[1]	%	Event, hysteresis, overcurrent, IL2
11013	float	RD/WR	_I_EVT_MAX_HYST[2]	%	Event, hysteresis, overcurrent, IL3
11015	float	RD/WR	_I_EVT_MAX_HYST[3]	%	Event, hysteresis, overcurrent, IL4
11017	float	RD/WR	_U_EVT_MAX_HYST[0]	%	Event, hysteresis, max. voltage, UL1
11019	float	RD/WR	_U_EVT_MAX_HYST[1]	%	Event, hysteresis, max. voltage, UL2
11021	float	RD/WR	_U_EVT_MAX_HYST[2]	%	Event, hysteresis, max. voltage, UL3
11023	float	RD/WR	_U_EVT_MAX_HYST[3]	%	Event, hysteresis, max. voltage, UL4
11025	float	RD/WR	_U_EVT_MIN_HYST[0]	%	Event, hysteresis, min. voltage, UL1
11027	float	RD/WR	_U_EVT_MIN_HYST[1]	%	Event, hysteresis, min. voltage, UL2
11029	float	RD/WR	_U_EVT_MIN_HYST[2]	%	Event, hysteresis, min. voltage, UL3
11031	float	RD/WR	_U_EVT_MIN_HYST[3]	%	Event, hysteresis, min. voltage, UL4
11033	float	RD/WR	_U_EVT_OFF_HYST[0]	%	Event monitoring off L1
11035	float	RD/WR	_U_EVT_OFF_HYST[1]	%	Event monitoring off L2
11037	float	RD/WR	_U_EVT_OFF_HYST[2]	%	Event monitoring off L3
11039	float	RD/WR	_U_EVT_OFF_HYST[3]	%	Event monitoring off L4

Adresse	Format	RD/WR	Designation	Unit	Note

Energy

Address	Format	RD/WR	Designation	Unit	Note
6462	short	RD/WR	_W_TARIF		Current rate, real/apparent energy
6463	short	RD/WR	_Q_TARIF		Current rate, reactive energy
6464	dfloat	RD	_WH_S[0]	VAh	Apparent energy L1
6466	dfloat	RD	_WH_S[1]	VAh	Apparent energy L2
6468	dfloat	RD	_WH_S[2]	VAh	Apparent energy L3
6470	dfloat	RD	_WH_S[3]	VAh	Apparent energy L4
6472	dfloat	RD	_WH_S[4]	VAh	Apparent energy L1+L2+L3
6474	dfloat	RD	_WH_S[5]	VAh	Apparent energy L1+L2+L3+L4
6476	dfloat	RD	_WH[0]	Wh	Real energy L1
6478	dfloat	RD	_WH[1]	Wh	Real energy L2
6480	dfloat	RD	_WH[2]	Wh	Real energy L3
6482	dfloat	RD	_WH[3]	Wh	Real energy L4
6484	dfloat	RD	_WH[4]	Wh	Real energy L1+L2+L3
6486	dfloat	RD	_WH[5]	Wh	Real energy L1+L2+L3+L4
6488	dfloat	RD	_QH[0]	varh	Reaktive energy L1
6490	dfloat	RD	_QH[1]	varh	Reaktive energy L2
6492	dfloat	RD	_QH[2]	varh	Reaktive energy L3
6494	dfloat	RD	_QH[3]	varh	Reaktive energy L4
6496	dfloat	RD	_QH[4]	varh	Reaktive energy L1+L2+L3
6498	dfloat	RD	_QH[5]	varh	Reaktive energy L1+L2+L3+L4
6500	dfloat	RD	_WH_V[0]	Wh	Real energy L1, consumed
6502	dfloat	RD	_WH_V[1]	Wh	Real energy L2, consumed
6504	dfloat	RD	_WH_V[2]	Wh	Real energy L3, consumed
6506	dfloat	RD	_WH_V[3]	Wh	Real energy L4, consumed
6508	dfloat	RD	_WH_V[4]	Wh	Real energy L1+L2+L3, consumed
6510	dfloat	RD	_WH_V[5]	Wh	Real energy L1+L2+L3+L4, consumed
6512	dfloat	RD	_WH_Z[0]	Wh	Real energy L1, delivered
6514	dfloat	RD	_WH_Z[1]	Wh	Real energy L2 delivered
6516	dfloat	RD	_WH_Z[2]	Wh	Real energy L3, delivered
6518	dfloat	RD	_WH_Z[3]	Wh	Real energy L4, delivered
6520	dfloat	RD	_WH_Z[4]	Wh	Real energy L1+L2+L3, delivered
6522	dfloat	RD	_WH_Z[5]	Wh	Real energy L1+L2+L3+L4, delivered
6524	dfloat	RD	_WH_V_HT[0]	Wh	Real energy L, consumed, HT (tariff 1), rate 1
6526	dfloat	RD	_WH_V_HT[1]	Wh	Real energy L, consumed, HT (tariff 1), rate 1
6528	dfloat	RD	_WH_V_HT[2]	Wh	Real energy L, consumed, HT (tariff 1), rate 1
6530	dfloat	RD	_WH_V_HT[3]	Wh	Real energy L, consumed, HT (tariff 1), rate 1
6532	dfloat	RD	_WH_V_HT[4]	Wh	Real energy L, consumed, HT (tariff 1), rate 1
6534	dfloat	RD	_WH_V_HT[5]	Wh	Real energy L, consumed, HT (tariff 1), rate 1
6536	dfloat	RD	_WH_V_NT[0]	Wh	Real energy L, consumed, NT (tariff 1), rate 2
6538	dfloat	RD	_WH_V_NT[1]	Wh	Real energy L, consumed, NT (tariff 1), rate 2
6540	dfloat	RD	_WH_V_NT[2]	Wh	Real energy L, consumed, NT (tariff 1), rate 2
6542	dfloat	RD	_WH_V_NT[3]	Wh	Real energy L, consumed, NT (tariff 1), rate 2
6544	dfloat	RD	_WH_V_NT[4]	Wh	Real energy L, consumed, NT (tariff 1), rate 2
6546	dfloat	RD	_WH_V_NT[5]	Wh	Real energy L, consumed, NT (tariff 1), rate 2
6548	dfloat	RD	_WH_Z_HT[0]	Wh	Real energy L, delivered, HT (tariff 2), rate 1
6550	dfloat	RD	_WH_Z_HT[1]	Wh	Real energy L, delivered, HT (tariff 2), rate 1
6552	dfloat	RD	_WH_Z_HT[2]	Wh	Real energy L, delivered, HT (tariff 2), rate 1
6554	dfloat	RD	_WH_Z_HT[3]	Wh	Real energy L, delivered, HT (tariff 2), rate 1
6556	dfloat	RD	_WH_Z_HT[4]	Wh	Real energy L, delivered, HT (tariff 2), rate 1
6558	dfloat	RD	_WH_Z_HT[5]	Wh	Real energy L, delivered, HT (tariff 2), rate 1
6560	dfloat	RD	_WH_Z_NT[0]	Wh	Real energy L, delivered, NT (tariff 2), rate 2
6562	dfloat	RD	_WH_Z_NT[1]	Wh	Real energy L, delivered, NT (tariff 2), rate 2
6564	dfloat	RD	_WH_Z_NT[2]	Wh	Real energy L, delivered, NT (tariff 2), rate 2
6566	dfloat	RD	_WH_Z_NT[3]	Wh	Real energy L, delivered, NT (tariff 2), rate 2
6568	dfloat	RD	_WH_Z_NT[4]	Wh	Real energy L, delivered, NT (tariff 2), rate 2
6570	dfloat	RD	_WH_Z_NT[5]	Wh	Real energy L, delivered, NT (tariff 2), rate 2
6572	dfloat	RD	_IQH[0]	varh	Reactive energy L, inductive
6574	dfloat	RD	_IQH[1]	varh	Reactive energy L, inductive

Adresse	Format	RD/WR	Designation	Unit	Note
6576	dfloat	RD	_IQH[2]	varh	Reactive energy L, inductive
6578	dfloat	RD	_IQH[3]	varh	Reactive energy L, inductive
6580	dfloat	RD	_IQH[4]	varh	Reactive energy L, inductive
6582	dfloat	RD	_IQH[5]	varh	Reactive energy L, inductive
6584	dfloat	RD	_CQH[0]	varh	Reactive energy L, capacitive
6586	dfloat	RD	_CQH[1]	varh	Reactive energy L, capacitive
6588	dfloat	RD	_CQH[2]	varh	Reactive energy L, capacitive
6590	dfloat	RD	_CQH[3]	varh	Reactive energy L, capacitive
6592	dfloat	RD	_CQH[4]	varh	Reactive energy L, capacitive
6594	dfloat	RD	_CQH[5]	varh	Reactive energy L, capacitive
6596	dfloat	RD	_IQH_HT[0]	varh	Reactive energy L, inductive, rate 1
6598	dfloat	RD	_IQH_HT[1]	varh	Reactive energy L, inductive, rate 1
6600	dfloat	RD	_IQH_HT[2]	varh	Reactive energy L, inductive, rate 1
6602	dfloat	RD	_IQH_HT[3]	varh	Reactive energy L, inductive, rate 1
6604	dfloat	RD	_IQH_HT[4]	varh	Reactive energy L, inductive, rate 1
6606	dfloat	RD	_IQH_HT[5]	varh	Reactive energy L, inductive, rate 1
6608	dfloat	RD	_IQH_NT[0]	varh	Reactive energy L, inductive, rate 2
6610	dfloat	RD	_IQH_NT[1]	varh	Reactive energy L, inductive, rate 2
6612	dfloat	RD	_IQH_NT[2]	varh	Reactive energy L, inductive, rate 2
6614	dfloat	RD	_IQH_NT[3]	varh	Reactive energy L, inductive, rate 2
6616	dfloat	RD	_IQH_NT[4]	varh	Reactive energy L, inductive, rate 2
6618	dfloat	RD	_IQH_NT[5]	varh	Reactive energy L, inductive, rate 2
6620	dfloat	RD	_S0_CNT[0]	n	Energy meter (counter, not scaled), impulse input 1
6622	dfloat	RD	_S0_CNT[1]	n	Energy meter (counter, not scaled), impulse input 2
6624	dfloat	RD	_TIME_WH	s	Runtime of real and apparent energy meas.
6626	dfloat	RD	_TIME_QH	s	Runtime of real and reactive energy meas.
10680	dfloat	RD	_VWH_MONTH[0]	Wh	Real energy, month high, jan., even year
10682	dfloat	RD	_VWH_MONTH[1]	Wh	Real energy, month high, feb., even year
10684	dfloat	RD	_VWH_MONTH[2]	Wh	Real energy, month high, march, even year
10686	dfloat	RD	_VWH_MONTH[3]	Wh	Real energy, month high, april, even year
10688	dfloat	RD	_VWH_MONTH[4]	Wh	Real energy, month high, may, even year
10690	dfloat	RD	_VWH_MONTH[5]	Wh	Real energy, month high, june, even year
10692	dfloat	RD	_VWH_MONTH[6]	Wh	Real energy, month high, july, even year
10694	dfloat	RD	_VWH_MONTH[7]	Wh	Real energy, month high, aug., even year
10696	dfloat	RD	_VWH_MONTH[8]	Wh	Real energy, month high, sep., even year
10698	dfloat	RD	_VWH_MONTH[9]	Wh	Real energy, month high, oct. even year
10700	dfloat	RD	_VWH_MONTH[10]	Wh	Real energy, month high, nov., even year
10702	dfloat	RD	_VWH_MONTH[11]	Wh	Real energy, month high, dec., even year
10704	dfloat	RD	_VWH_MONTH[12]	Wh	Real energy, month high, jan., uneven year
10706	dfloat	RD	_VWH_MONTH[13]	Wh	Real energy, month high, feb., uneven year
10708	dfloat	RD	_VWH_MONTH[14]	Wh	Real energy, month high, march, uneven year
10710	dfloat	RD	_VWH_MONTH[15]	Wh	Real energy, month high, april, uneven year
10712	dfloat	RD	_VWH_MONTH[16]	Wh	Real energy, month high, may, uneven year
10714	dfloat	RD	_VWH_MONTH[17]	Wh	Real energy, month high, june, uneven year
10716	dfloat	RD	_VWH_MONTH[18]	Wh	Real energy, month high, july, uneven year
10718	dfloat	RD	_VWH_MONTH[19]	Wh	Real energy, month high, aug., uneven year
10720	dfloat	RD	_VWH_MONTH[20]	Wh	Real energy, month high, sep., uneven year
10722	dfloat	RD	_VWH_MONTH[21]	Wh	Real energy, month high, oct.., uneven year
10724	dfloat	RD	_VWH_MONTH[22]	Wh	Real energy, month high, nov., uneven year
10726	dfloat	RD	_VWH_MONTH[23]	Wh	Real energy, month high, dec., uneven year
10728	dfloat	RD	_SH_MONTH[0]	VAh	Apparent energy, month high, jan., even year
10730	dfloat	RD	_SH_MONTH[1]	VAh	Apparent energy, month high, feb., even year
10732	dfloat	RD	_SH_MONTH[2]	VAh	Apparent energy, month high, march, even year
10734	dfloat	RD	_SH_MONTH[3]	VAh	Apparent energy, month high, april, even year
10736	dfloat	RD	_SH_MONTH[4]	VAh.	Apparent energy, month high, may, even year
10738	dfloat	RD	_SH_MONTH[5]	VAh	Apparent energy, month high, june, even year
10740	dfloat	RD	_SH_MONTH[6]	VAh	Apparent energy, month high, july, even year
10742	dfloat	RD	_SH_MONTH[7]	VAh	Apparent energy, month high, aug., even year

Address	Format	RD/WR	Designation	Unit	Note
10744	dfloat	RD	_SH_MONTH[8]	VAh	Apparent energy, month high, sep., even year
10746	dfloat	RD	_SH_MONTH[9]	VAh	Apparent energy, month high, oct., even year
10748	dfloat	RD	_SH_MONTH[10]	VAh	Apparent energy, month high, nov., even year
10750	dfloat	RD	_SH_MONTH[11]	VAh	Apparent energy, month high, dec., even year
10752	dfloat	RD	_SH_MONTH[12]	VAh	Apparent energy, month high, jan., uneven year
10754	dfloat	RD	_SH_MONTH[13]	VAh	Apparent energy, month high, feb., uneven year
10756	dfloat	RD	_SH_MONTH[14]	VAh	Apparent energy, month high, march, uneven year
10758	dfloat	RD	_SH_MONTH[15]	VAh	Apparent energy, month high, april, uneven year
10760	dfloat	RD	_SH_MONTH[16]	VAh	Apparent energy, month high, may, uneven year
10762	dfloat	RD	_SH_MONTH[17]	VAh	Apparent energy, month high, june, uneven year
10764	dfloat	RD	_SH_MONTH[18]	VAh	Apparent energy, month high, july, uneven year
10766	dfloat	RD	_SH_MONTH[19]	VAh	Apparent energy, month high, aug., uneven year
10768	dfloat	RD	_SH_MONTH[20]	VAh	Apparent energy, month high, sep., uneven year
10770	dfloat	RD	_SH_MONTH[21]	VAh	Apparent energy, month high, oct., uneven year
10772	dfloat	RD	_SH_MONTH[22]	VAh	Apparent energy, month high, nov., uneven year
10774	dfloat	RD	_SH_MONTH[23]	VAh	Apparent energy, month high, dec., uneven year
10776	dfloat	RD	_IQH_MONTH[0]	VArh	Reactive energy, month high, jan., even year
10778	dfloat	RD	_IQH_MONTH[1]	VArh	Reactive energy, month high, feb., even year
10780	dfloat	RD	_IQH_MONTH[2]	VArh	Reactive energy, month high, march, even year
10782	dfloat	RD	_IQH_MONTH[3]	VArh	Reactive energy, month high, april, even year
10784	dfloat	RD	_IQH_MONTH[4]	VArh	Reactive energy, month high, may even year
10786	dfloat	RD	_IQH_MONTH[5]	VArh	Reactive energy, month high, june, even year
10788	dfloat	RD	_IQH_MONTH[6]	VArh	Reactive energy, month high, july, even year
10790	dfloat	RD	_IQH_MONTH[7]	VArh	Reactive energy, month high, aug., even year
10792	dfloat	RD	_IQH_MONTH[8]	VArh	Reactive energy, month high, sep., even year
10794	dfloat	RD	_IQH_MONTH[9]	VArh	Reactive energy, month high, oct., even year
10796	dfloat	RD	_IQH_MONTH[10]	VArh	Reactive energy, month high, nov., even year
10798	dfloat	RD	_IQH_MONTH[11]	VArh	Reactive energy, month high, dec., even year
10800	dfloat	RD	_IQH_MONTH[12]	VArh	Reactive energy, month high, jan., uneven year
10802	dfloat	RD	_IQH_MONTH[13]	VArh	Reactive energy, month high, feb., uneven year
10804	dfloat	RD	_IQH_MONTH[14]	VArh	Reactive energy, month high, march, uneven year
10806	dfloat	RD	_IQH_MONTH[15]	VArh	Reactive energy, month high, april, uneven year
10808	dfloat	RD	_IQH_MONTH[16]	VArh	Reactive energy, month high, may, uneven year
10810	dfloat	RD	_IQH_MONTH[17]	VArh	Reactive energy, month high, june., uneven year
10812	dfloat	RD	_IQH_MONTH[18]	VArh	Reactive energy, month high, july., uneven year
10814	dfloat	RD	_IQH_MONTH[19]	VArh	Reactive energy, month high, aug., uneven year
10816	dfloat	RD	_IQH_MONTH[20]	VArh	Reactive energy, month high, sep., uneven year
10818	dfloat	RD	_IQH_MONTH[21]	VArh	Reactive energy, month high, oct., uneven year
10820	dfloat	RD	_IQH_MONTH[22]	VArh	Reactive energy, month high, nov., uneven year
10822	dfloat	RD	_IQH_MONTH[23]	VArh	Reactive energy, month high, dez., uneven year
10824	float	RD	_P15_MONTH[0]	W	EMAX, 15minutes month high, jan., even year
10826	float	RD	_P15_MONTH[1]	W	EMAX, 15minutes month high, feb., even year
10828	float	RD	_P15_MONTH[2]	W	EMAX, 15minutes month high, march, even year
10830	float	RD	_P15_MONTH[3]	W	EMAX, 15minutes month high, april., even year
10832	float	RD	_P15_MONTH[4]	W	EMAX, 15minutes month high, may, even year
10834	float	RD	_P15_MONTH[5]	W	EMAX, 15minutes month high, june, even year
10836	float	RD	_P15_MONTH[6]	W	EMAX, 15minutes month high, july, even year
10838	float	RD	_P15_MONTH[7]	W	EMAX, 15minutes month high, aug., even year
10840	float	RD	_P15_MONTH[8]	W	EMAX, 15minutes month high, sep., even year
10842	float	RD	_P15_MONTH[9]	W	EMAX, 15minutes month high, oct., even year
10844	float	RD	_P15_MONTH[10]	W	EMAX, 15minutes month high, nov., even year
10846	float	RD	_P15_MONTH[11]	W	EMAX, 15minutes month high, dez., even year
10848	float	RD	_P15_MONTH[12]	W	EMAX, 15minutes month high, jan., uneven year
10850	float	RD	_P15_MONTH[13]	W	EMAX, 15minutes month high, feb., uneven year
10852	float	RD	_P15_MONTH[14]	W	EMAX, 15minutes month high, march, uneven year
10854	float	RD	_P15_MONTH[15]	W	EMAX, 15minutes month high, april, uneven year
10856	float	RD	_P15_MONTH[16]	W	EMAX, 15minutes month high, may, uneven year
10858	float	RD	_P15_MONTH[17]	W	EMAX, 15minutes month high, june, uneven year

Adresse	Format	RD/WR	Designation	Unit	Note
10860	float	RD	_P15_MONTH[18]	W	EMAX, 15minutes month high, july, uneven year
10862	float	RD	_P15_MONTH[19]	W	EMAX, 15minutes month high, aug., uneven year
10864	float	RD	_P15_MONTH[20]	W	EMAX, 15minutes month high, sep., uneven year
10866	float	RD	_P15_MONTH[21]	W	EMAX, 15minutes month high, oct., uneven year
10868	float	RD	_P15_MONTH[22]	W	EMAX, 15minutes month high, nov., uneven year
10870	float	RD	_P15_MONTH[23]	W	EMAX, 15minutes month high, dec., uneven year
10872	uint	RD	_P15_T_MONTH[0]	s	Time of 15minutes month high, jan., even year
10874	uint	RD	_P15_T_MONTH[1]	s	Time of 15minutes month high, feb., even year
10876	uint	RD	_P15_T_MONTH[2]	s	Time of 15minutes month high, march, even year
10878	uint	RD	_P15_T_MONTH[3]	s	Time of 15minutes month high, april, even year
10880	uint	RD	_P15_T_MONTH[4]	s	Time of 15minutes month high, may, even year
10882	uint	RD	_P15_T_MONTH[5]	s	Time of 15minutes month high, june, even year
10884	uint	RD	_P15_T_MONTH[6]	s	Time of 15minutes month high, july, even year
10886	uint	RD	_P15_T_MONTH[7]	s	Time of 15minutes month high, aug., even year
10888	uint	RD	_P15_T_MONTH[8]	s	Time of 15minutes month high, sep., even year
10890	uint	RD	_P15_T_MONTH[9]	s	Time of 15minutes month high, oct., even year
10892	uint	RD	_P15_T_MONTH[10]	s	Time of 15minutes month high, nov., even year
10894	uint	RD	_P15_T_MONTH[11]	s	Time of 15minutes month high, dez., even year
10896	uint	RD	_P15_T_MONTH[12]	s	Time of 15minutes month high, jan., uneven year
10898	uint	RD	_P15_T_MONTH[13]	s	Time of 15minutes month high, feb., uneven year
10900	uint	RD	_P15_T_MONTH[14]	s	Time of 15minutes month high, march, uneven year
10902	uint	RD	_P15_T_MONTH[15]	s	Time of 15minutes month high, april, uneven year
10904	uint	RD	_P15_T_MONTH[16]	s	Time of 15minutes month high, may, uneven year
10906	uint	RD	_P15_T_MONTH[17]	s	Time of 15minutes month high, june, uneven year
10908	uint	RD	_P15_T_MONTH[18]	s	Time of 15minutes month high, july, uneven year
10910	uint	RD	_P15_T_MONTH[19]	s	Time of 15minutes month high, aug., uneven year
10912	uint	RD	_P15_T_MONTH[20]	s	Time of 15minutes month high, sep., uneven year
10914	uint	RD	_P15_T_MONTH[21]	s	Time of 15minutes month high, oct., uneven year
10916	uint	RD	_P15_T_MONTH[22]	s	Time of 15minutes month high, nov., uneven year
10918	uint	RD	_P15_T_MONTH[23]	s	Time of 15minutes month high, dec., uneven year
10920	short	RD	_MONTHLY_YEAR[0]		Year, real energy, bar graph, jan., even year
10921	short	RD	_MONTHLY_YEAR[1]		Year, real energy, bar graph, feb., even year
10922	short	RD	_MONTHLY_YEAR[2]		Year, real energy, bar graph, march, even year
10923	short	RD	_MONTHLY_YEAR[3]		Year, real energy, bar graph, april, even year
10924	short	RD	_MONTHLY_YEAR[4]		Year, real energy, bar graph, may, even year
10925	short	RD	_MONTHLY_YEAR[5]		Year, real energy, bar graph, june, even year
10926	short	RD	_MONTHLY_YEAR[6]		Year, real energy, bar graph, july, even year
10927	short	RD	_MONTHLY_YEAR[7]		Year, real energy, bar graph, aug., even year
10928	short	RD	_MONTHLY_YEAR[8]		Year, real energy, bar graph, sep., even year
10929	short	RD	_MONTHLY_YEAR[9]		Year, real energy, bar graph, oct., even year
10930	short	RD	_MONTHLY_YEAR[10]		Year, real energy, bar graph, nov., even year
10931	short	RD	_MONTHLY_YEAR[11]		Year, real energy, bar graph, dez., even year
10932	short	RD	_MONTHLY_YEAR[12]		Year, real energy, bar graph, jan., uneven year
10933	short	RD	_MONTHLY_YEAR[13]		Year, real energy, bar graph, feb., uneven year
10934	short	RD	_MONTHLY_YEAR[14]		Year, real energy, bar graph, march, uneven year
10935	short	RD	_MONTHLY_YEAR[15]		Year, real energy, bar graph, april, uneven year
10936	short	RD	_MONTHLY_YEAR[16]		Year, real energy, bar graph, may, uneven year
10937	short	RD	_MONTHLY_YEAR[17]		Year, real energy, bar graph, june, uneven year
10938	short	RD	_MONTHLY_YEAR[18]		Year, real energy, bar graph, july, uneven year
10939	short	RD	_MONTHLY_YEAR[19]		Year, real energy, bar graph, aug., uneven year
10940	short	RD	_MONTHLY_YEAR[20]		Year, real energy, bar graph, sep., uneven year
10941	short	RD	_MONTHLY_YEAR[21]		Year, real energy, bar graph, oct., uneven year
10942	short	RD	_MONTHLY_YEAR[22]		Year, real energy, bar graph, nov., uneven year
10943	short	RD	_MONTHLY_YEAR[23]		Year, real energy, bar graph, dec., uneven year

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Address	Format	RD/WR	Designation	Unit	Note
7112	short	RD/WR	_EMAX_SPERRZEIT	s	
7113	short	RD/WR	_EMAX_PAUSENZEIT	s	
7114	float	RD/WR	_EMAX_LEISTUNG	W	
7116	float	RD/WR	_EMAX_MAX	W	
7118	float	RD/WR	_EMAX_TRENDWERT	W	
7120	uint	RD/WR	_EMAX_MAX_T		
7122	short	RD/WR	_EMAX_D_STATUS[0]	n	
7123	short	RD/WR	_EMAX_D_STATUS[1]	n	
7124	short	RD/WR	_EMAX_D_STATUS[2]	n	
7125	short	RD/WR	_EMAX_D_STATUS[3]	n	
7126	short	RD/WR	_EMAX_D_STATUS[4]	n	
7127	short	RD/WR	_EMAX_D_STATUS[5]	n	
7128	short	RD/WR	_EMAX_D_STATUS[6]	n	
7129	short	RD/WR	_EMAX_D_STATUS[7]	n	
7130	short	RD/WR	_EMAX_D_STATUS[8]	n	
7131	short	RD/WR	_EMAX_D_STATUS[9]	n	
7132	short	RD/WR	_EMAX_D_STATUS[10]	n	
7133	short	RD/WR	_EMAX_D_STATUS[11]	n	
7134	short	RD/WR	_EMAX_D_STATUS[12]	n	
7135	short	RD/WR	_EMAX_D_STATUS[13]	n	
7136	short	RD/WR	_EMAX_D_STATUS[14]	n	
7137	short	RD/WR	_EMAX_D_STATUS[15]	n	
7138	short	RD/WR	_EMAX_D_STATUS[16]	n	
7139	short	RD/WR	_EMAX_D_STATUS[17]	n	
7140	short	RD/WR	_EMAX_D_STATUS[18]	n	
7141	short	RD/WR	_EMAX_D_STATUS[19]	n	
7142	short	RD/WR	_EMAX_D_STATUS[20]	n	
7143	short	RD/WR	_EMAX_D_STATUS[21]	n	
7144	short	RD/WR	_EMAX_D_STATUS[22]	n	
7145	short	RD/WR	_EMAX_D_STATUS[23]	n	
7146	short	RD/WR	_EMAX_D_STATUS[24]	n	
7147	short	RD/WR	_EMAX_D_STATUS[25]	n	
7148	short	RD/WR	_EMAX_D_STATUS[26]	n	
7149	short	RD/WR	_EMAX_D_STATUS[27]	n	
7150	short	RD/WR	_EMAX_D_STATUS[28]	n	
7151	short	RD/WR	_EMAX_D_STATUS[29]	n	
7152	short	RD/WR	_EMAX_D_STATUS[30]	n	
7153	short	RD/WR	_EMAX_D_STATUS[31]	n	
7154	short	RD/WR	_EMAX_D_STATUS[32]	n	
7155	short	RD/WR	_EMAX_D_STATUS[33]	n	
7156	short	RD/WR	_EMAX_D_STATUS[34]	n	
7157	short	RD/WR	_EMAX_D_STATUS[35]	n	
7158	short	RD/WR	_EMAX_D_STATUS[36]	n	
7159	short	RD/WR	_EMAX_D_STATUS[37]	n	
7160	short	RD/WR	_EMAX_D_STATUS[38]	n	
7161	short	RD/WR	_EMAX_D_STATUS[39]	n	
7162	short	RD/WR	_EMAX_D_STATUS[40]	n	
7163	short	RD/WR	_EMAX_D_STATUS[41]	n	
7164	short	RD/WR	_EMAX_D_STATUS[42]	n	
7165	short	RD/WR	_EMAX_D_STATUS[43]	n	
7166	short	RD/WR	_EMAX_D_STATUS[44]	n	
7167	short	RD/WR	_EMAX_D_STATUS[45]	n	
7168	short	RD/WR	_EMAX_D_STATUS[46]	n	
7169	short	RD/WR	_EMAX_D_STATUS[47]	n	
7170	short	RD/WR	_EMAX_D_STATUS[48]	n	
7171	short	RD/WR	_EMAX_D_STATUS[49]	n	
7172	short	RD/WR	_EMAX_D_STATUS[50]	n	
7173	short	RD/WR	_EMAX_D_STATUS[51]	n	

Adresse	Format	RD/WR	Designation	Unit	Note
7174	short	RD/WR	_EMAX_D_STATUS[52]	n	
7175	short	RD/WR	_EMAX_D_STATUS[53]	n	
7176	short	RD/WR	_EMAX_D_STATUS[54]	n	
7177	short	RD/WR	_EMAX_D_STATUS[55]	n	
7178	short	RD/WR	_EMAX_D_STATUS[56]	n	
7179	short	RD/WR	_EMAX_D_STATUS[57]	n	
7180	short	RD/WR	_EMAX_D_STATUS[58]	n	
7181	short	RD/WR	_EMAX_D_STATUS[59]	n	
7182	short	RD/WR	_EMAX_D_STATUS[60]	n	
7183	short	RD/WR	_EMAX_D_STATUS[61]	n	
7184	short	RD/WR	_EMAX_D_STATUS[62]	n	
7185	short	RD/WR	_EMAX_D_STATUS[63]	n	
7186	float	RD/WR	_EMAX_A_STATUS[0]	%	
7188	float	RD/WR	_EMAX_A_STATUS[1]	%	
7190	float	RD/WR	_EMAX_A_STATUS[2]	%	
7192	float	RD/WR	_EMAX_A_STATUS[3]	%	
7194	short	RD/WR	_EMAX_D_ACTIVE[0]		
7195	short	RD/WR	_EMAX_D_ACTIVE[1]		
7196	short	RD/WR	_EMAX_D_ACTIVE[2]		
7197	short	RD/WR	_EMAX_D_ACTIVE[3]		
7198	short	RD/WR	_EMAX_D_ACTIVE[4]		
7199	short	RD/WR	_EMAX_D_ACTIVE[5]		
7200	short	RD/WR	_EMAX_D_ACTIVE[6]		
7201	short	RD/WR	_EMAX_D_ACTIVE[7]		
7202	short	RD/WR	_EMAX_D_ACTIVE[8]		
7203	short	RD/WR	_EMAX_D_ACTIVE[9]		
7204	short	RD/WR	_EMAX_D_ACTIVE[10]		
7205	short	RD/WR	_EMAX_D_ACTIVE[11]		
7206	short	RD/WR	_EMAX_D_ACTIVE[12]		
7207	short	RD/WR	_EMAX_D_ACTIVE[13]		
7208	short	RD/WR	_EMAX_D_ACTIVE[14]		
7209	short	RD/WR	_EMAX_D_ACTIVE[15]		
7210	short	RD/WR	_EMAX_D_ACTIVE[16]		
7211	short	RD/WR	_EMAX_D_ACTIVE[17]		
7212	short	RD/WR	_EMAX_D_ACTIVE[18]		
7213	short	RD/WR	_EMAX_D_ACTIVE[19]		
7214	short	RD/WR	_EMAX_D_ACTIVE[20]		
7215	short	RD/WR	_EMAX_D_ACTIVE[21]		
7216	short	RD/WR	_EMAX_D_ACTIVE[22]		
7217	short	RD/WR	_EMAX_D_ACTIVE[23]		
7218	short	RD/WR	_EMAX_D_ACTIVE[24]		
7219	short	RD/WR	_EMAX_D_ACTIVE[25]		
7220	short	RD/WR	_EMAX_D_ACTIVE[26]		
7221	short	RD/WR	_EMAX_D_ACTIVE[27]		
7222	short	RD/WR	_EMAX_D_ACTIVE[28]		
7223	short	RD/WR	_EMAX_D_ACTIVE[29]		
7224	short	RD/WR	_EMAX_D_ACTIVE[30]		
7225	short	RD/WR	_EMAX_D_ACTIVE[31]		
7226	short	RD/WR	_EMAX_D_ACTIVE[32]		
7227	short	RD/WR	_EMAX_D_ACTIVE[33]		
7228	short	RD/WR	_EMAX_D_ACTIVE[34]		
7229	short	RD/WR	_EMAX_D_ACTIVE[35]		
7230	short	RD/WR	_EMAX_D_ACTIVE[36]		
7231	short	RD/WR	_EMAX_D_ACTIVE[37]		
7232	short	RD/WR	_EMAX_D_ACTIVE[38]		
7233	short	RD/WR	_EMAX_D_ACTIVE[39]		
7234	short	RD/WR	_EMAX_D_ACTIVE[40]		
7235	short	RD/WR	_EMAX_D_ACTIVE[41]		

Address	Format	RD/WR	Designation	Unit	Note
7236	short	RD/WR	_EMAX_D_ACTIVE[42]		
7237	short	RD/WR	_EMAX_D_ACTIVE[43]		
7238	short	RD/WR	_EMAX_D_ACTIVE[44]		
7239	short	RD/WR	_EMAX_D_ACTIVE[45]		
7240	short	RD/WR	_EMAX_D_ACTIVE[46]		
7241	short	RD/WR	_EMAX_D_ACTIVE[47]		
7242	short	RD/WR	_EMAX_D_ACTIVE[48]		
7243	short	RD/WR	_EMAX_D_ACTIVE[49]		
7244	short	RD/WR	_EMAX_D_ACTIVE[50]		
7245	short	RD/WR	_EMAX_D_ACTIVE[51]		
7246	short	RD/WR	_EMAX_D_ACTIVE[52]		
7247	short	RD/WR	_EMAX_D_ACTIVE[53]		
7248	short	RD/WR	_EMAX_D_ACTIVE[54]		
7249	short	RD/WR	_EMAX_D_ACTIVE[55]		
7250	short	RD/WR	_EMAX_D_ACTIVE[56]		
7251	short	RD/WR	_EMAX_D_ACTIVE[57]		
7252	short	RD/WR	_EMAX_D_ACTIVE[58]		
7253	short	RD/WR	_EMAX_D_ACTIVE[59]		
7254	short	RD/WR	_EMAX_D_ACTIVE[60]		
7255	short	RD/WR	_EMAX_D_ACTIVE[61]		
7256	short	RD/WR	_EMAX_D_ACTIVE[62]		
7257	short	RD/WR	_EMAX_D_ACTIVE[63]		
7258	short	RD/WR	_EMAX_A_ACTIVE[0]		
7259	short	RD/WR	_EMAX_A_ACTIVE[1]		
7260	short	RD/WR	_EMAX_A_ACTIVE[2]		
7261	short	RD/WR	_EMAX_A_ACTIVE[3]		
7262	string	RD/WR	_EMAX_D_DESC0	32	
7278	string	RD/WR	_EMAX_D_DESC1	32	
7294	string	RD/WR	_EMAX_D_DESC2	32	
7310	string	RD/WR	_EMAX_D_DESC3	32	
7326	string	RD/WR	_EMAX_D_DESC4	32	
7342	string	RD/WR	_EMAX_D_DESC5	32	
7358	string	RD/WR	_EMAX_D_DESC6	32	
7374	string	RD/WR	_EMAX_D_DESC7	32	
7390	string	RD/WR	_EMAX_D_DESC8	32	
7406	string	RD/WR	_EMAX_D_DESC9	32	
7422	string	RD/WR	_EMAX_D_DESC10	32	
7438	string	RD/WR	_EMAX_D_DESC11	32	
7454	string	RD/WR	_EMAX_D_DESC12	32	
7470	string	RD/WR	_EMAX_D_DESC13	32	
7486	string	RD/WR	_EMAX_D_DESC14	32	
7502	string	RD/WR	_EMAX_D_DESC15	32	
7518	string	RD/WR	_EMAX_D_DESC16	32	
7534	string	RD/WR	_EMAX_D_DESC17	32	
7550	string	RD/WR	_EMAX_D_DESC18	32	
7566	string	RD/WR	_EMAX_D_DESC19	32	
7582	string	RD/WR	_EMAX_D_DESC20	32	
7598	string	RD/WR	_EMAX_D_DESC21	32	
7614	string	RD/WR	_EMAX_D_DESC22	32	
7630	string	RD/WR	_EMAX_D_DESC23	32	
7646	string	RD/WR	_EMAX_D_DESC24	32	
7662	string	RD/WR	_EMAX_D_DESC25	32	
7678	string	RD/WR	_EMAX_D_DESC26	32	
7694	string	RD/WR	_EMAX_D_DESC27	32	
7710	string	RD/WR	_EMAX_D_DESC28	32	
7726	string	RD/WR	_EMAX_D_DESC29	32	
7742	string	RD/WR	_EMAX_D_DESC30	32	
7758	string	RD/WR	_EMAX_D_DESC31	32	

Adresse	Format	RD/WR	Designation	Unit	Note
7774	string	RD/WR	_EMAX_D_DESC32	32	
7790	string	RD/WR	_EMAX_D_DESC33	32	
7806	string	RD/WR	_EMAX_D_DESC34	32	
7822	string	RD/WR	_EMAX_D_DESC35	32	
7838	string	RD/WR	_EMAX_D_DESC36	32	
7854	string	RD/WR	_EMAX_D_DESC37	32	
7870	string	RD/WR	_EMAX_D_DESC38	32	
7886	string	RD/WR	_EMAX_D_DESC39	32	
7902	string	RD/WR	_EMAX_D_DESC40	32	
7918	string	RD/WR	_EMAX_D_DESC41	32	
7934	string	RD/WR	_EMAX_D_DESC42	32	
7950	string	RD/WR	_EMAX_D_DESC43	32	
7966	string	RD/WR	_EMAX_D_DESC44	32	
7982	string	RD/WR	_EMAX_D_DESC45	32	
7998	string	RD/WR	_EMAX_D_DESC46	32	
8014	string	RD/WR	_EMAX_D_DESC47	32	
8030	string	RD/WR	_EMAX_D_DESC48	32	
8046	string	RD/WR	_EMAX_D_DESC49	32	
8062	string	RD/WR	_EMAX_D_DESC50	32	
8078	string	RD/WR	_EMAX_D_DESC51	32	
8094	string	RD/WR	_EMAX_D_DESC52	32	
8110	string	RD/WR	_EMAX_D_DESC53	32	
8126	string	RD/WR	_EMAX_D_DESC54	32	
8142	string	RD/WR	_EMAX_D_DESC55	32	
8158	string	RD/WR	_EMAX_D_DESC56	32	
8174	string	RD/WR	_EMAX_D_DESC57	32	
8190	string	RD/WR	_EMAX_D_DESC58	32	
8206	string	RD/WR	_EMAX_D_DESC59	32	
8222	string	RD/WR	_EMAX_D_DESC60	32	
8238	string	RD/WR	_EMAX_D_DESC61	32	
8254	string	RD/WR	_EMAX_D_DESC62	32	
8270	string	RD/WR	_EMAX_D_DESC63	32	
8286	string	RD/WR	_EMAX_A_DESC0	32	
8302	string	RD/WR	_EMAX_A_DESC1	32	
8318	string	RD/WR	_EMAX_A_DESC2	32	
8334	string	RD/WR	_EMAX_A_DESC3	32	
8350	short	RD/WR	_EMAX_D_PRIORITY[0]		
8351	short	RD/WR	_EMAX_D_PRIORITY[1]		
8352	short	RD/WR	_EMAX_D_PRIORITY[2]		
8353	short	RD/WR	_EMAX_D_PRIORITY[3]		
8354	short	RD/WR	_EMAX_D_PRIORITY[4]		
8355	short	RD/WR	_EMAX_D_PRIORITY[5]		
8356	short	RD/WR	_EMAX_D_PRIORITY[6]		
8357	short	RD/WR	_EMAX_D_PRIORITY[7]		
8358	short	RD/WR	_EMAX_D_PRIORITY[8]		
8359	short	RD/WR	_EMAX_D_PRIORITY[9]		
8360	short	RD/WR	_EMAX_D_PRIORITY[10]		
8361	short	RD/WR	_EMAX_D_PRIORITY[11]		
8362	short	RD/WR	_EMAX_D_PRIORITY[12]		
8363	short	RD/WR	_EMAX_D_PRIORITY[13]		
8364	short	RD/WR	_EMAX_D_PRIORITY[14]		
8365	short	RD/WR	_EMAX_D_PRIORITY[15]		
8366	short	RD/WR	_EMAX_D_PRIORITY[16]		
8367	short	RD/WR	_EMAX_D_PRIORITY[17]		
8368	short	RD/WR	_EMAX_D_PRIORITY[18]		
8369	short	RD/WR	_EMAX_D_PRIORITY[19]		
8370	short	RD/WR	_EMAX_D_PRIORITY[20]		
8371	short	RD/WR	_EMAX_D_PRIORITY[21]		

Address	Format	RD/WR	Designation	Unit	Note
8372	short	RD/WR	_EMAX_D_PRIORITY[22]		
8373	short	RD/WR	_EMAX_D_PRIORITY[23]		
8374	short	RD/WR	_EMAX_D_PRIORITY[24]		
8375	short	RD/WR	_EMAX_D_PRIORITY[25]		
8376	short	RD/WR	_EMAX_D_PRIORITY[26]		
8377	short	RD/WR	_EMAX_D_PRIORITY[27]		
8378	short	RD/WR	_EMAX_D_PRIORITY[28]		
8379	short	RD/WR	_EMAX_D_PRIORITY[29]		
8380	short	RD/WR	_EMAX_D_PRIORITY[30]		
8381	short	RD/WR	_EMAX_D_PRIORITY[31]		
8382	short	RD/WR	_EMAX_D_PRIORITY[32]		
8383	short	RD/WR	_EMAX_D_PRIORITY[33]		
8384	short	RD/WR	_EMAX_D_PRIORITY[34]		
8385	short	RD/WR	_EMAX_D_PRIORITY[35]		
8386	short	RD/WR	_EMAX_D_PRIORITY[36]		
8387	short	RD/WR	_EMAX_D_PRIORITY[37]		
8388	short	RD/WR	_EMAX_D_PRIORITY[38]		
8389	short	RD/WR	_EMAX_D_PRIORITY[39]		
8390	short	RD/WR	_EMAX_D_PRIORITY[40]		
8391	short	RD/WR	_EMAX_D_PRIORITY[41]		
8392	short	RD/WR	_EMAX_D_PRIORITY[42]		
8393	short	RD/WR	_EMAX_D_PRIORITY[43]		
8394	short	RD/WR	_EMAX_D_PRIORITY[44]		
8395	short	RD/WR	_EMAX_D_PRIORITY[45]		
8396	short	RD/WR	_EMAX_D_PRIORITY[46]		
8397	short	RD/WR	_EMAX_D_PRIORITY[47]		
8398	short	RD/WR	_EMAX_D_PRIORITY[48]		
8399	short	RD/WR	_EMAX_D_PRIORITY[49]		
8400	short	RD/WR	_EMAX_D_PRIORITY[50]		
8401	short	RD/WR	_EMAX_D_PRIORITY[51]		
8402	short	RD/WR	_EMAX_D_PRIORITY[52]		
8403	short	RD/WR	_EMAX_D_PRIORITY[53]		
8404	short	RD/WR	_EMAX_D_PRIORITY[54]		
8405	short	RD/WR	_EMAX_D_PRIORITY[55]		
8406	short	RD/WR	_EMAX_D_PRIORITY[56]		
8407	short	RD/WR	_EMAX_D_PRIORITY[57]		
8408	short	RD/WR	_EMAX_D_PRIORITY[58]		
8409	short	RD/WR	_EMAX_D_PRIORITY[59]		
8410	short	RD/WR	_EMAX_D_PRIORITY[60]		
8411	short	RD/WR	_EMAX_D_PRIORITY[61]		
8412	short	RD/WR	_EMAX_D_PRIORITY[62]		
8413	short	RD/WR	_EMAX_D_PRIORITY[63]		
8414	short	RD/WR	_EMAX_A_PRIORITY[0]		
8415	short	RD/WR	_EMAX_A_PRIORITY[1]		
8416	short	RD/WR	_EMAX_A_PRIORITY[2]		
8417	short	RD/WR	_EMAX_A_PRIORITY[3]		
8418	float	RD/WR	_EMAX_D_POWER[0]	W	
8420	float	RD/WR	_EMAX_D_POWER[1]	W	
8422	float	RD/WR	_EMAX_D_POWER[2]	W	
8424	float	RD/WR	_EMAX_D_POWER[3]	W	
8426	float	RD/WR	_EMAX_D_POWER[4]	W	
8428	float	RD/WR	_EMAX_D_POWER[5]	W	
8430	float	RD/WR	_EMAX_D_POWER[6]	W	
8432	float	RD/WR	_EMAX_D_POWER[7]	W	
8434	float	RD/WR	_EMAX_D_POWER[8]	W	
8436	float	RD/WR	_EMAX_D_POWER[9]	W	
8438	float	RD/WR	_EMAX_D_POWER[10]	W	
8440	float	RD/WR	_EMAX_D_POWER[11]	W	

Adresse	Format	RD/WR	Designation	Unit	Note
8442	float	RD/WR	_EMAX_D_POWER[12]	W	
8444	float	RD/WR	_EMAX_D_POWER[13]	W	
8446	float	RD/WR	_EMAX_D_POWER[14]	W	
8448	float	RD/WR	_EMAX_D_POWER[15]	W	
8450	float	RD/WR	_EMAX_D_POWER[16]	W	
8452	float	RD/WR	_EMAX_D_POWER[17]	W	
8454	float	RD/WR	_EMAX_D_POWER[18]	W	
8456	float	RD/WR	_EMAX_D_POWER[19]	W	
8458	float	RD/WR	_EMAX_D_POWER[20]	W	
8460	float	RD/WR	_EMAX_D_POWER[21]	W	
8462	float	RD/WR	_EMAX_D_POWER[22]	W	
8464	float	RD/WR	_EMAX_D_POWER[23]	W	
8466	float	RD/WR	_EMAX_D_POWER[24]	W	
8468	float	RD/WR	_EMAX_D_POWER[25]	W	
8470	float	RD/WR	_EMAX_D_POWER[26]	W	
8472	float	RD/WR	_EMAX_D_POWER[27]	W	
8474	float	RD/WR	_EMAX_D_POWER[28]	W	
8476	float	RD/WR	_EMAX_D_POWER[29]	W	
8478	float	RD/WR	_EMAX_D_POWER[30]	W	
8480	float	RD/WR	_EMAX_D_POWER[31]	W	
8482	float	RD/WR	_EMAX_D_POWER[32]	W	
8484	float	RD/WR	_EMAX_D_POWER[33]	W	
8486	float	RD/WR	_EMAX_D_POWER[34]	W	
8488	float	RD/WR	_EMAX_D_POWER[35]	W	
8490	float	RD/WR	_EMAX_D_POWER[36]	W	
8492	float	RD/WR	_EMAX_D_POWER[37]	W	
8494	float	RD/WR	_EMAX_D_POWER[38]	W	
8496	float	RD/WR	_EMAX_D_POWER[39]	W	
8498	float	RD/WR	_EMAX_D_POWER[40]	W	
8500	float	RD/WR	_EMAX_D_POWER[41]	W	
8502	float	RD/WR	_EMAX_D_POWER[42]	W	
8504	float	RD/WR	_EMAX_D_POWER[43]	W	
8506	float	RD/WR	_EMAX_D_POWER[44]	W	
8508	float	RD/WR	_EMAX_D_POWER[45]	W	
8510	float	RD/WR	_EMAX_D_POWER[46]	W	
8512	float	RD/WR	_EMAX_D_POWER[47]	W	
8514	float	RD/WR	_EMAX_D_POWER[48]	W	
8516	float	RD/WR	_EMAX_D_POWER[49]	W	
8518	float	RD/WR	_EMAX_D_POWER[50]	W	
8520	float	RD/WR	_EMAX_D_POWER[51]	W	
8522	float	RD/WR	_EMAX_D_POWER[52]	W	
8524	float	RD/WR	_EMAX_D_POWER[53]	W	
8526	float	RD/WR	_EMAX_D_POWER[54]	W	
8528	float	RD/WR	_EMAX_D_POWER[55]	W	
8530	float	RD/WR	_EMAX_D_POWER[56]	W	
8532	float	RD/WR	_EMAX_D_POWER[57]	W	
8534	float	RD/WR	_EMAX_D_POWER[58]	W	
8536	float	RD/WR	_EMAX_D_POWER[59]	W	
8538	float	RD/WR	_EMAX_D_POWER[60]	W	
8540	float	RD/WR	_EMAX_D_POWER[61]	W	
8542	float	RD/WR	_EMAX_D_POWER[62]	W	
8544	float	RD/WR	_EMAX_D_POWER[63]	W	
8546	short	RD/WR	_EMAX_D_MIN_ONDURATION[0]	s	
8547	short	RD/WR	_EMAX_D_MIN_ONDURATION[1]	s	
8548	short	RD/WR	_EMAX_D_MIN_ONDURATION[2]	s	
8549	short	RD/WR	_EMAX_D_MIN_ONDURATION[3]	s	
8550	short	RD/WR	_EMAX_D_MIN_ONDURATION[4]	s	
8551	short	RD/WR	_EMAX_D_MIN_ONDURATION[5]	s	

Address	Format	RD/WR	Designation	Unit	Note
8552	short	RD/WR	_EMAX_D_MIN_ONDURATION[6]	s	
8553	short	RD/WR	_EMAX_D_MIN_ONDURATION[7]	s	
8554	short	RD/WR	_EMAX_D_MIN_ONDURATION[8]	s	
8555	short	RD/WR	_EMAX_D_MIN_ONDURATION[9]	s	
8556	short	RD/WR	_EMAX_D_MIN_ONDURATION[10]	s	
8557	short	RD/WR	_EMAX_D_MIN_ONDURATION[11]	s	
8558	short	RD/WR	_EMAX_D_MIN_ONDURATION[12]	s	
8559	short	RD/WR	_EMAX_D_MIN_ONDURATION[13]	s	
8560	short	RD/WR	_EMAX_D_MIN_ONDURATION[14]	s	
8561	short	RD/WR	_EMAX_D_MIN_ONDURATION[15]	s	
8562	short	RD/WR	_EMAX_D_MIN_ONDURATION[16]	s	
8563	short	RD/WR	_EMAX_D_MIN_ONDURATION[17]	s	
8564	short	RD/WR	_EMAX_D_MIN_ONDURATION[18]	s	
8565	short	RD/WR	_EMAX_D_MIN_ONDURATION[19]	s	
8566	short	RD/WR	_EMAX_D_MIN_ONDURATION[20]	s	
8567	short	RD/WR	_EMAX_D_MIN_ONDURATION[21]	s	
8568	short	RD/WR	_EMAX_D_MIN_ONDURATION[22]	s	
8569	short	RD/WR	_EMAX_D_MIN_ONDURATION[23]	s	
8570	short	RD/WR	_EMAX_D_MIN_ONDURATION[24]	s	
8571	short	RD/WR	_EMAX_D_MIN_ONDURATION[25]	s	
8572	short	RD/WR	_EMAX_D_MIN_ONDURATION[26]	s	
8573	short	RD/WR	_EMAX_D_MIN_ONDURATION[27]	s	
8574	short	RD/WR	_EMAX_D_MIN_ONDURATION[28]	s	
8575	short	RD/WR	_EMAX_D_MIN_ONDURATION[29]	s	
8576	short	RD/WR	_EMAX_D_MIN_ONDURATION[30]	s	
8577	short	RD/WR	_EMAX_D_MIN_ONDURATION[31]	s	
8578	short	RD/WR	_EMAX_D_MIN_ONDURATION[32]	s	
8579	short	RD/WR	_EMAX_D_MIN_ONDURATION[33]	s	
8580	short	RD/WR	_EMAX_D_MIN_ONDURATION[34]	s	
8581	short	RD/WR	_EMAX_D_MIN_ONDURATION[35]	s	
8582	short	RD/WR	_EMAX_D_MIN_ONDURATION[36]	s	
8583	short	RD/WR	_EMAX_D_MIN_ONDURATION[37]	s	
8584	short	RD/WR	_EMAX_D_MIN_ONDURATION[38]	s	
8585	short	RD/WR	_EMAX_D_MIN_ONDURATION[39]	s	
8586	short	RD/WR	_EMAX_D_MIN_ONDURATION[40]	s	
8587	short	RD/WR	_EMAX_D_MIN_ONDURATION[41]	s	
8588	short	RD/WR	_EMAX_D_MIN_ONDURATION[42]	s	
8589	short	RD/WR	_EMAX_D_MIN_ONDURATION[43]	s	
8590	short	RD/WR	_EMAX_D_MIN_ONDURATION[44]	s	
8591	short	RD/WR	_EMAX_D_MIN_ONDURATION[45]	s	
8592	short	RD/WR	_EMAX_D_MIN_ONDURATION[46]	s	
8593	short	RD/WR	_EMAX_D_MIN_ONDURATION[47]	s	
8594	short	RD/WR	_EMAX_D_MIN_ONDURATION[48]	s	
8595	short	RD/WR	_EMAX_D_MIN_ONDURATION[49]	s	
8596	short	RD/WR	_EMAX_D_MIN_ONDURATION[50]	s	
8597	short	RD/WR	_EMAX_D_MIN_ONDURATION[51]	s	
8598	short	RD/WR	_EMAX_D_MIN_ONDURATION[52]	s	
8599	short	RD/WR	_EMAX_D_MIN_ONDURATION[53]	s	
8600	short	RD/WR	_EMAX_D_MIN_ONDURATION[54]	s	
8601	short	RD/WR	_EMAX_D_MIN_ONDURATION[55]	s	
8602	short	RD/WR	_EMAX_D_MIN_ONDURATION[56]	s	
8603	short	RD/WR	_EMAX_D_MIN_ONDURATION[57]	s	
8604	short	RD/WR	_EMAX_D_MIN_ONDURATION[58]	s	
8605	short	RD/WR	_EMAX_D_MIN_ONDURATION[59]	s	
8606	short	RD/WR	_EMAX_D_MIN_ONDURATION[60]	s	
8607	short	RD/WR	_EMAX_D_MIN_ONDURATION[61]	s	
8608	short	RD/WR	_EMAX_D_MIN_ONDURATION[62]	s	
8609	short	RD/WR	_EMAX_D_MIN_ONDURATION[63]	s	

Adresse	Format	RD/WR	Designation	Unit	Note
8610	short	RD/WR	_EMAX_D_MAX_OFFDURATION[0]	s	
8611	short	RD/WR	_EMAX_D_MAX_OFFDURATION[1]	s	
8612	short	RD/WR	_EMAX_D_MAX_OFFDURATION[2]	s	
8613	short	RD/WR	_EMAX_D_MAX_OFFDURATION[3]	s	
8614	short	RD/WR	_EMAX_D_MAX_OFFDURATION[4]	s	
8615	short	RD/WR	_EMAX_D_MAX_OFFDURATION[5]	s	
8616	short	RD/WR	_EMAX_D_MAX_OFFDURATION[6]	s	
8617	short	RD/WR	_EMAX_D_MAX_OFFDURATION[7]	s	
8618	short	RD/WR	_EMAX_D_MAX_OFFDURATION[8]	s	
8619	short	RD/WR	_EMAX_D_MAX_OFFDURATION[9]	s	
8620	short	RD/WR	_EMAX_D_MAX_OFFDURATION[10]	s	
8621	short	RD/WR	_EMAX_D_MAX_OFFDURATION[11]	s	
8622	short	RD/WR	_EMAX_D_MAX_OFFDURATION[12]	s	
8623	short	RD/WR	_EMAX_D_MAX_OFFDURATION[13]	s	
8624	short	RD/WR	_EMAX_D_MAX_OFFDURATION[14]	s	
8625	short	RD/WR	_EMAX_D_MAX_OFFDURATION[15]	s	
8626	short	RD/WR	_EMAX_D_MAX_OFFDURATION[16]	s	
8627	short	RD/WR	_EMAX_D_MAX_OFFDURATION[17]	s	
8628	short	RD/WR	_EMAX_D_MAX_OFFDURATION[18]	s	
8629	short	RD/WR	_EMAX_D_MAX_OFFDURATION[19]	s	
8630	short	RD/WR	_EMAX_D_MAX_OFFDURATION[20]	s	
8631	short	RD/WR	_EMAX_D_MAX_OFFDURATION[21]	s	
8632	short	RD/WR	_EMAX_D_MAX_OFFDURATION[22]	s	
8633	short	RD/WR	_EMAX_D_MAX_OFFDURATION[23]	s	
8634	short	RD/WR	_EMAX_D_MAX_OFFDURATION[24]	s	
8635	short	RD/WR	_EMAX_D_MAX_OFFDURATION[25]	s	
8636	short	RD/WR	_EMAX_D_MAX_OFFDURATION[26]	s	
8637	short	RD/WR	_EMAX_D_MAX_OFFDURATION[27]	s	
8638	short	RD/WR	_EMAX_D_MAX_OFFDURATION[28]	s	
8639	short	RD/WR	_EMAX_D_MAX_OFFDURATION[29]	s	
8640	short	RD/WR	_EMAX_D_MAX_OFFDURATION[30]	s	
8641	short	RD/WR	_EMAX_D_MAX_OFFDURATION[31]	s	
8642	short	RD/WR	_EMAX_D_MAX_OFFDURATION[32]	s	
8643	short	RD/WR	_EMAX_D_MAX_OFFDURATION[33]	s	
8644	short	RD/WR	_EMAX_D_MAX_OFFDURATION[34]	s	
8645	short	RD/WR	_EMAX_D_MAX_OFFDURATION[35]	s	
8646	short	RD/WR	_EMAX_D_MAX_OFFDURATION[36]	s	
8647	short	RD/WR	_EMAX_D_MAX_OFFDURATION[37]	s	
8648	short	RD/WR	_EMAX_D_MAX_OFFDURATION[38]	s	
8649	short	RD/WR	_EMAX_D_MAX_OFFDURATION[39]	s	
8650	short	RD/WR	_EMAX_D_MAX_OFFDURATION[40]	s	
8651	short	RD/WR	_EMAX_D_MAX_OFFDURATION[41]	s	
8652	short	RD/WR	_EMAX_D_MAX_OFFDURATION[42]	s	
8653	short	RD/WR	_EMAX_D_MAX_OFFDURATION[43]	s	
8654	short	RD/WR	_EMAX_D_MAX_OFFDURATION[44]	s	
8655	short	RD/WR	_EMAX_D_MAX_OFFDURATION[45]	s	
8656	short	RD/WR	_EMAX_D_MAX_OFFDURATION[46]	s	
8657	short	RD/WR	_EMAX_D_MAX_OFFDURATION[47]	s	
8658	short	RD/WR	_EMAX_D_MAX_OFFDURATION[48]	s	
8659	short	RD/WR	_EMAX_D_MAX_OFFDURATION[49]	s	
8660	short	RD/WR	_EMAX_D_MAX_OFFDURATION[50]	s	
8661	short	RD/WR	_EMAX_D_MAX_OFFDURATION[51]	s	
8662	short	RD/WR	_EMAX_D_MAX_OFFDURATION[52]	s	
8663	short	RD/WR	_EMAX_D_MAX_OFFDURATION[53]	s	
8664	short	RD/WR	_EMAX_D_MAX_OFFDURATION[54]	s	
8665	short	RD/WR	_EMAX_D_MAX_OFFDURATION[55]	s	
8666	short	RD/WR	_EMAX_D_MAX_OFFDURATION[56]	s	
8667	short	RD/WR	_EMAX_D_MAX_OFFDURATION[57]	s	

Address	Format	RD/WR	Designation	Unit	Note
8668	short	RD/WR	_EMAX_D_MAX_OFFDURATION[58]	s	
8669	short	RD/WR	_EMAX_D_MAX_OFFDURATION[59]	s	
8670	short	RD/WR	_EMAX_D_MAX_OFFDURATION[60]	s	
8671	short	RD/WR	_EMAX_D_MAX_OFFDURATION[61]	s	
8672	short	RD/WR	_EMAX_D_MAX_OFFDURATION[62]	s	
8673	short	RD/WR	_EMAX_D_MAX_OFFDURATION[63]	s	
8674	short	RD/WR	_EMAX_D_MIN_OFFDURATION[0]	s	
8675	short	RD/WR	_EMAX_D_MIN_OFFDURATION[1]	s	
8676	short	RD/WR	_EMAX_D_MIN_OFFDURATION[2]	s	
8677	short	RD/WR	_EMAX_D_MIN_OFFDURATION[3]	s	
8678	short	RD/WR	_EMAX_D_MIN_OFFDURATION[4]	s	
8679	short	RD/WR	_EMAX_D_MIN_OFFDURATION[5]	s	
8680	short	RD/WR	_EMAX_D_MIN_OFFDURATION[6]	s	
8681	short	RD/WR	_EMAX_D_MIN_OFFDURATION[7]	s	
8682	short	RD/WR	_EMAX_D_MIN_OFFDURATION[8]	s	
8683	short	RD/WR	_EMAX_D_MIN_OFFDURATION[9]	s	
8684	short	RD/WR	_EMAX_D_MIN_OFFDURATION[10]	s	
8685	short	RD/WR	_EMAX_D_MIN_OFFDURATION[11]	s	
8686	short	RD/WR	_EMAX_D_MIN_OFFDURATION[12]	s	
8687	short	RD/WR	_EMAX_D_MIN_OFFDURATION[13]	s	
8688	short	RD/WR	_EMAX_D_MIN_OFFDURATION[14]	s	
8689	short	RD/WR	_EMAX_D_MIN_OFFDURATION[15]	s	
8690	short	RD/WR	_EMAX_D_MIN_OFFDURATION[16]	s	
8691	short	RD/WR	_EMAX_D_MIN_OFFDURATION[17]	s	
8692	short	RD/WR	_EMAX_D_MIN_OFFDURATION[18]	s	
8693	short	RD/WR	_EMAX_D_MIN_OFFDURATION[19]	s	
8694	short	RD/WR	_EMAX_D_MIN_OFFDURATION[20]	s	
8695	short	RD/WR	_EMAX_D_MIN_OFFDURATION[21]	s	
8696	short	RD/WR	_EMAX_D_MIN_OFFDURATION[22]	s	
8697	short	RD/WR	_EMAX_D_MIN_OFFDURATION[23]	s	
8698	short	RD/WR	_EMAX_D_MIN_OFFDURATION[24]	s	
8699	short	RD/WR	_EMAX_D_MIN_OFFDURATION[25]	s	
8700	short	RD/WR	_EMAX_D_MIN_OFFDURATION[26]	s	
8701	short	RD/WR	_EMAX_D_MIN_OFFDURATION[27]	s	
8702	short	RD/WR	_EMAX_D_MIN_OFFDURATION[28]	s	
8703	short	RD/WR	_EMAX_D_MIN_OFFDURATION[29]	s	
8704	short	RD/WR	_EMAX_D_MIN_OFFDURATION[30]	s	
8705	short	RD/WR	_EMAX_D_MIN_OFFDURATION[31]	s	
8706	short	RD/WR	_EMAX_D_MIN_OFFDURATION[32]	s	
8707	short	RD/WR	_EMAX_D_MIN_OFFDURATION[33]	s	
8708	short	RD/WR	_EMAX_D_MIN_OFFDURATION[34]	s	
8709	short	RD/WR	_EMAX_D_MIN_OFFDURATION[35]	s	
8710	short	RD/WR	_EMAX_D_MIN_OFFDURATION[36]	s	
8711	short	RD/WR	_EMAX_D_MIN_OFFDURATION[37]	s	
8712	short	RD/WR	_EMAX_D_MIN_OFFDURATION[38]	s	
8713	short	RD/WR	_EMAX_D_MIN_OFFDURATION[39]	s	
8714	short	RD/WR	_EMAX_D_MIN_OFFDURATION[40]	s	
8715	short	RD/WR	_EMAX_D_MIN_OFFDURATION[41]	s	
8716	short	RD/WR	_EMAX_D_MIN_OFFDURATION[42]	s	
8717	short	RD/WR	_EMAX_D_MIN_OFFDURATION[43]	s	
8718	short	RD/WR	_EMAX_D_MIN_OFFDURATION[44]	s	
8719	short	RD/WR	_EMAX_D_MIN_OFFDURATION[45]	s	
8720	short	RD/WR	_EMAX_D_MIN_OFFDURATION[46]	s	
8721	short	RD/WR	_EMAX_D_MIN_OFFDURATION[47]	s	
8722	short	RD/WR	_EMAX_D_MIN_OFFDURATION[48]	s	
8723	short	RD/WR	_EMAX_D_MIN_OFFDURATION[49]	s	
8724	short	RD/WR	_EMAX_D_MIN_OFFDURATION[50]	s	
8725	short	RD/WR	_EMAX_D_MIN_OFFDURATION[51]	s	

Adresse	Format	RD/WR	Designation	Unit	Note
8726	short	RD/WR	_EMAX_D_MIN_OFFDURATION[52]	s	
8727	short	RD/WR	_EMAX_D_MIN_OFFDURATION[53]	s	
8728	short	RD/WR	_EMAX_D_MIN_OFFDURATION[54]	s	
8729	short	RD/WR	_EMAX_D_MIN_OFFDURATION[55]	s	
8730	short	RD/WR	_EMAX_D_MIN_OFFDURATION[56]	s	
8731	short	RD/WR	_EMAX_D_MIN_OFFDURATION[57]	s	
8732	short	RD/WR	_EMAX_D_MIN_OFFDURATION[58]	s	
8733	short	RD/WR	_EMAX_D_MIN_OFFDURATION[59]	s	
8734	short	RD/WR	_EMAX_D_MIN_OFFDURATION[60]	s	
8735	short	RD/WR	_EMAX_D_MIN_OFFDURATION[61]	s	
8736	short	RD/WR	_EMAX_D_MIN_OFFDURATION[62]	s	
8737	short	RD/WR	_EMAX_D_MIN_OFFDURATION[63]	s	
8738	float	RD/WR	_EMAX_D_PROBABILITY[0]	%	
8740	float	RD/WR	_EMAX_D_PROBABILITY[1]	%	
8742	float	RD/WR	_EMAX_D_PROBABILITY[2]	%	
8744	float	RD/WR	_EMAX_D_PROBABILITY[3]	%	
8746	float	RD/WR	_EMAX_D_PROBABILITY[4]	%	
8748	float	RD/WR	_EMAX_D_PROBABILITY[5]	%	
8750	float	RD/WR	_EMAX_D_PROBABILITY[6]	%	
8752	float	RD/WR	_EMAX_D_PROBABILITY[7]	%	
8754	float	RD/WR	_EMAX_D_PROBABILITY[8]	%	
8756	float	RD/WR	_EMAX_D_PROBABILITY[9]	%	
8758	float	RD/WR	_EMAX_D_PROBABILITY[10]	%	
8760	float	RD/WR	_EMAX_D_PROBABILITY[11]	%	
8762	float	RD/WR	_EMAX_D_PROBABILITY[12]	%	
8764	float	RD/WR	_EMAX_D_PROBABILITY[13]	%	
8766	float	RD/WR	_EMAX_D_PROBABILITY[14]	%	
8768	float	RD/WR	_EMAX_D_PROBABILITY[15]	%	
8770	float	RD/WR	_EMAX_D_PROBABILITY[16]	%	
8772	float	RD/WR	_EMAX_D_PROBABILITY[17]	%	
8774	float	RD/WR	_EMAX_D_PROBABILITY[18]	%	
8776	float	RD/WR	_EMAX_D_PROBABILITY[19]	%	
8778	float	RD/WR	_EMAX_D_PROBABILITY[20]	%	
8780	float	RD/WR	_EMAX_D_PROBABILITY[21]	%	
8782	float	RD/WR	_EMAX_D_PROBABILITY[22]	%	
8784	float	RD/WR	_EMAX_D_PROBABILITY[23]	%	
8786	float	RD/WR	_EMAX_D_PROBABILITY[24]	%	
8788	float	RD/WR	_EMAX_D_PROBABILITY[25]	%	
8790	float	RD/WR	_EMAX_D_PROBABILITY[26]	%	
8792	float	RD/WR	_EMAX_D_PROBABILITY[27]	%	
8794	float	RD/WR	_EMAX_D_PROBABILITY[28]	%	
8796	float	RD/WR	_EMAX_D_PROBABILITY[29]	%	
8798	float	RD/WR	_EMAX_D_PROBABILITY[30]	%	
8800	float	RD/WR	_EMAX_D_PROBABILITY[31]	%	
8802	float	RD/WR	_EMAX_D_PROBABILITY[32]	%	
8804	float	RD/WR	_EMAX_D_PROBABILITY[33]	%	
8806	float	RD/WR	_EMAX_D_PROBABILITY[34]	%	
8808	float	RD/WR	_EMAX_D_PROBABILITY[35]	%	
8810	float	RD/WR	_EMAX_D_PROBABILITY[36]	%	
8812	float	RD/WR	_EMAX_D_PROBABILITY[37]	%	
8814	float	RD/WR	_EMAX_D_PROBABILITY[38]	%	
8816	float	RD/WR	_EMAX_D_PROBABILITY[39]	%	
8818	float	RD/WR	_EMAX_D_PROBABILITY[40]	%	
8820	float	RD/WR	_EMAX_D_PROBABILITY[41]	%	
8822	float	RD/WR	_EMAX_D_PROBABILITY[42]	%	
8824	float	RD/WR	_EMAX_D_PROBABILITY[43]	%	
8826	float	RD/WR	_EMAX_D_PROBABILITY[44]	%	
8828	float	RD/WR	_EMAX_D_PROBABILITY[45]	%	

Address	Format	RD/WR	Designation	Unit	Note
8830	float	RD/WR	_EMAX_D_PROBABILITY[46]	%	
8832	float	RD/WR	_EMAX_D_PROBABILITY[47]	%	
8834	float	RD/WR	_EMAX_D_PROBABILITY[48]	%	
8836	float	RD/WR	_EMAX_D_PROBABILITY[49]	%	
8838	float	RD/WR	_EMAX_D_PROBABILITY[50]	%	
8840	float	RD/WR	_EMAX_D_PROBABILITY[51]	%	
8842	float	RD/WR	_EMAX_D_PROBABILITY[52]	%	
8844	float	RD/WR	_EMAX_D_PROBABILITY[53]	%	
8846	float	RD/WR	_EMAX_D_PROBABILITY[54]	%	
8848	float	RD/WR	_EMAX_D_PROBABILITY[55]	%	
8850	float	RD/WR	_EMAX_D_PROBABILITY[56]	%	
8852	float	RD/WR	_EMAX_D_PROBABILITY[57]	%	
8854	float	RD/WR	_EMAX_D_PROBABILITY[58]	%	
8856	float	RD/WR	_EMAX_D_PROBABILITY[59]	%	
8858	float	RD/WR	_EMAX_D_PROBABILITY[60]	%	
8860	float	RD/WR	_EMAX_D_PROBABILITY[61]	%	
8862	float	RD/WR	_EMAX_D_PROBABILITY[62]	%	
8864	float	RD/WR	_EMAX_D_PROBABILITY[63]	%	
8866	short	RD/WR	_EMAX_A_GENERATOR[0]		
8867	short	RD/WR	_EMAX_A_GENERATOR[1]		
8868	short	RD/WR	_EMAX_A_GENERATOR[2]		
8869	short	RD/WR	_EMAX_A_GENERATOR[3]		
8870	float	RD/WR	_EMAX_A_MAXPOWER[0]	W	
8872	float	RD/WR	_EMAX_A_MAXPOWER[1]	W	
8874	float	RD/WR	_EMAX_A_MAXPOWER[2]	W	
8876	float	RD/WR	_EMAX_A_MAXPOWER[3]	W	
8878	float	RD/WR	_EMAX_A_MINPOWER[0]	W	
8880	float	RD/WR	_EMAX_A_MINPOWER[1]	W	
8882	float	RD/WR	_EMAX_A_MINPOWER[2]	W	
8884	float	RD/WR	_EMAX_A_MINPOWER[3]	W	
8886	float	RD/WR	_EMAX_A_MAXSAVE[0]	W	
8888	float	RD/WR	_EMAX_A_MAXSAVE[1]	W	
8890	float	RD/WR	_EMAX_A_MAXSAVE[2]	W	
8892	float	RD/WR	_EMAX_A_MAXSAVE[3]	W	
8894	short	RD/WR	_EMAX_A_STARTUP[0]	s	
8895	short	RD/WR	_EMAX_A_STARTUP[1]	s	
8896	short	RD/WR	_EMAX_A_STARTUP[2]	s	
8897	short	RD/WR	_EMAX_A_STARTUP[3]	s	
8898	int	RD/WR	_EMAX_A_MINACTIVE[0]	s	
8900	int	RD/WR	_EMAX_A_MINACTIVE[1]	s	
8902	int	RD/WR	_EMAX_A_MINACTIVE[2]	s	
8904	int	RD/WR	_EMAX_A_MINACTIVE[3]	s	
8906	int	RD/WR	_EMAX_PERIOD	s	
8908	int	RD/WR	_EMAX_T_REMAIN	s	
8910	float	RD	_EMAX_TREND	W	
8912	float	RD	_EMAX_C_TREND	W	

Adresse	Format	RD/WR	Designation	Unit	Note

FFT Fourier analysis

Address	Format	RD/WR	Designation	Unit	Note
13	float	RD	_FFT_ULL1[0]	V	1st harmonic
15	float	RD	_FFT_ULL1[1]	V	
17	float	RD	_FFT_ULL1[2]	V	
19	float	RD	_FFT_ULL1[3]	V	
21	float	RD	_FFT_ULL1[4]	V	
23	float	RD	_FFT_ULL1[5]	V	
25	float	RD	_FFT_ULL1[6]	V	
27	float	RD	_FFT_ULL1[7]	V	
29	float	RD	_FFT_ULL1[8]	V	
31	float	RD	_FFT_ULL1[9]	V	
33	float	RD	_FFT_ULL1[10]	V	
35	float	RD	_FFT_ULL1[11]	V	
37	float	RD	_FFT_ULL1[12]	V	
39	float	RD	_FFT_ULL1[13]	V	
41	float	RD	_FFT_ULL1[14]	V	
43	float	RD	_FFT_ULL1[15]	V	
45	float	RD	_FFT_ULL1[16]	V	
47	float	RD	_FFT_ULL1[17]	V	
49	float	RD	_FFT_ULL1[18]	V	
51	float	RD	_FFT_ULL1[19]	V	
53	float	RD	_FFT_ULL1[20]	V	
55	float	RD	_FFT_ULL1[21]	V	
57	float	RD	_FFT_ULL1[22]	V	
59	float	RD	_FFT_ULL1[23]	V	
61	float	RD	_FFT_ULL1[24]	V	
63	float	RD	_FFT_ULL1[25]	V	
65	float	RD	_FFT_ULL1[26]	V	
67	float	RD	_FFT_ULL1[27]	V	
69	float	RD	_FFT_ULL1[28]	V	
71	float	RD	_FFT_ULL1[29]	V	
73	float	RD	_FFT_ULL1[30]	V	
75	float	RD	_FFT_ULL1[31]	V	
77	float	RD	_FFT_ULL1[32]	V	
79	float	RD	_FFT_ULL1[33]	V	
81	float	RD	_FFT_ULL1[34]	V	
83	float	RD	_FFT_ULL1[35]	V	
85	float	RD	_FFT_ULL1[36]	V	
87	float	RD	_FFT_ULL1[37]	V	
89	float	RD	_FFT_ULL1[38]	V	
91	float	RD	_FFT_ULL1[39]	V	
93	float	RD	_FFT_ULL1[40]	V	
95	float	RD	_FFT_ULL1[41]	V	
97	float	RD	_FFT_ULL1[42]	V	
99	float	RD	_FFT_ULL1[43]	V	
101	float	RD	_FFT_ULL1[44]	V	
103	float	RD	_FFT_ULL1[45]	V	
105	float	RD	_FFT_ULL1[46]	V	
107	float	RD	_FFT_ULL1[47]	V	
109	float	RD	_FFT_ULL1[48]	V	
111	float	RD	_FFT_ULL1[49]	V	
113	float	RD	_FFT_ULL1[50]	V	
115	float	RD	_FFT_ULL1[51]	V	
117	float	RD	_FFT_ULL1[52]	V	
119	float	RD	_FFT_ULL1[53]	V	
121	float	RD	_FFT_ULL1[54]	V	
123	float	RD	_FFT_ULL1[55]	V	
125	float	RD	_FFT_ULL1[56]	V	
127	float	RD	_FFT_ULL1[57]	V	

Adresse	Format	RD/WR	Designation	Unit	Note
129	float	RD	_FFT_ULL1[58]	V	
131	float	RD	_FFT_ULL1[59]	V	
133	float	RD	_FFT_ULL1[60]	V	
135	float	RD	_FFT_ULL1[61]	V	
137	float	RD	_FFT_ULL1[62]	V	63rd harmonic
139	float	RD	_FFT_ULL2[0]	V	
141	float	RD	_FFT_ULL2[1]	V	
143	float	RD	_FFT_ULL2[2]	V	
145	float	RD	_FFT_ULL2[3]	V	
147	float	RD	_FFT_ULL2[4]	V	
149	float	RD	_FFT_ULL2[5]	V	
151	float	RD	_FFT_ULL2[6]	V	
153	float	RD	_FFT_ULL2[7]	V	
155	float	RD	_FFT_ULL2[8]	V	
157	float	RD	_FFT_ULL2[9]	V	
159	float	RD	_FFT_ULL2[10]	V	
161	float	RD	_FFT_ULL2[11]	V	
163	float	RD	_FFT_ULL2[12]	V	
165	float	RD	_FFT_ULL2[13]	V	
167	float	RD	_FFT_ULL2[14]	V	
169	float	RD	_FFT_ULL2[15]	V	
171	float	RD	_FFT_ULL2[16]	V	
173	float	RD	_FFT_ULL2[17]	V	
175	float	RD	_FFT_ULL2[18]	V	
177	float	RD	_FFT_ULL2[19]	V	
179	float	RD	_FFT_ULL2[20]	V	
181	float	RD	_FFT_ULL2[21]	V	
183	float	RD	_FFT_ULL2[22]	V	
185	float	RD	_FFT_ULL2[23]	V	
187	float	RD	_FFT_ULL2[24]	V	
189	float	RD	_FFT_ULL2[25]	V	
191	float	RD	_FFT_ULL2[26]	V	
193	float	RD	_FFT_ULL2[27]	V	
195	float	RD	_FFT_ULL2[28]	V	
197	float	RD	_FFT_ULL2[29]	V	
199	float	RD	_FFT_ULL2[30]	V	
201	float	RD	_FFT_ULL2[31]	V	
203	float	RD	_FFT_ULL2[32]	V	
205	float	RD	_FFT_ULL2[33]	V	
207	float	RD	_FFT_ULL2[34]	V	
209	float	RD	_FFT_ULL2[35]	V	
211	float	RD	_FFT_ULL2[36]	V	
213	float	RD	_FFT_ULL2[37]	V	
215	float	RD	_FFT_ULL2[38]	V	
217	float	RD	_FFT_ULL2[39]	V	
219	float	RD	_FFT_ULL2[40]	V	
221	float	RD	_FFT_ULL2[41]	V	
223	float	RD	_FFT_ULL2[42]	V	
225	float	RD	_FFT_ULL2[43]	V	
227	float	RD	_FFT_ULL2[44]	V	
229	float	RD	_FFT_ULL2[45]	V	
231	float	RD	_FFT_ULL2[46]	V	
233	float	RD	_FFT_ULL2[47]	V	
235	float	RD	_FFT_ULL2[48]	V	
237	float	RD	_FFT_ULL2[49]	V	
239	float	RD	_FFT_ULL2[50]	V	
241	float	RD	_FFT_ULL2[51]	V	
243	float	RD	_FFT_ULL2[52]	V	

Address	Format	RD/WR	Designation	Unit	Note
245	float	RDD	_FFT_ULL2[53]	V	
247	float	RDD	_FFT_ULL2[54]	V	
249	float	RDD	_FFT_ULL2[55]	V	
251	float	RDD	_FFT_ULL2[56]	V	
253	float	RDD	_FFT_ULL2[57]	V	
255	float	RDD	_FFT_ULL2[58]	V	
257	float	RDD	_FFT_ULL2[59]	V	
259	float	RDD	_FFT_ULL2[60]	V	
261	float	RDD	_FFT_ULL2[61]	V	
263	float	RDD	_FFT_ULL2[62]	V	
265	float	RDD	_FFT_ULL3[0]	V	
267	float	RDD	_FFT_ULL3[1]	V	
269	float	RDD	_FFT_ULL3[2]	V	
271	float	RDD	_FFT_ULL3[3]	V	
273	float	RDD	_FFT_ULL3[4]	V	
275	float	RDD	_FFT_ULL3[5]	V	
277	float	RDD	_FFT_ULL3[6]	V	
279	float	RDD	_FFT_ULL3[7]	V	
281	float	RDD	_FFT_ULL3[8]	V	
283	float	RDD	_FFT_ULL3[9]	V	
285	float	RDD	_FFT_ULL3[10]	V	
287	float	RDD	_FFT_ULL3[11]	V	
289	float	RDD	_FFT_ULL3[12]	V	
291	float	RDD	_FFT_ULL3[13]	V	
293	float	RDD	_FFT_ULL3[14]	V	
295	float	RDD	_FFT_ULL3[15]	V	
297	float	RDD	_FFT_ULL3[16]	V	
299	float	RDD	_FFT_ULL3[17]	V	
301	float	RDD	_FFT_ULL3[18]	V	
303	float	RDD	_FFT_ULL3[19]	V	
305	float	RDD	_FFT_ULL3[20]	V	
307	float	RDD	_FFT_ULL3[21]	V	
309	float	RDD	_FFT_ULL3[22]	V	
311	float	RDD	_FFT_ULL3[23]	V	
313	float	RDD	_FFT_ULL3[24]	V	
315	float	RDD	_FFT_ULL3[25]	V	
317	float	RDD	_FFT_ULL3[26]	V	
319	float	RDD	_FFT_ULL3[27]	V	
321	float	RDD	_FFT_ULL3[28]	V	
323	float	RDD	_FFT_ULL3[29]	V	
325	float	RDD	_FFT_ULL3[30]	V	
327	float	RDD	_FFT_ULL3[31]	V	
329	float	RDD	_FFT_ULL3[32]	V	
331	float	RDD	_FFT_ULL3[33]	V	
333	float	RDD	_FFT_ULL3[34]	V	
335	float	RDD	_FFT_ULL3[35]	V	
337	float	RDD	_FFT_ULL3[36]	V	
339	float	RDD	_FFT_ULL3[37]	V	
341	float	RDD	_FFT_ULL3[38]	V	
343	float	RDD	_FFT_ULL3[39]	V	
345	float	RDD	_FFT_ULL3[40]	V	
347	float	RDD	_FFT_ULL3[41]	V	
349	float	RDD	_FFT_ULL3[42]	V	
351	float	RDD	_FFT_ULL3[43]	V	
353	float	RDD	_FFT_ULL3[44]	V	
355	float	RDD	_FFT_ULL3[45]	V	
357	float	RDD	_FFT_ULL3[46]	V	
359	float	RDD	_FFT_ULL3[47]	V	

Adresse	Format	RD/WR	Designation	Unit	Note
361	float	RDD	_FFT_ULL3[48]	V	
363	float	RDD	_FFT_ULL3[49]	V	
365	float	RDD	_FFT_ULL3[50]	V	
367	float	RDD	_FFT_ULL3[51]	V	
369	float	RDD	_FFT_ULL3[52]	V	
371	float	RDD	_FFT_ULL3[53]	V	
373	float	RDD	_FFT_ULL3[54]	V	
375	float	RDD	_FFT_ULL3[55]	V	
377	float	RDD	_FFT_ULL3[56]	V	
379	float	RDD	_FFT_ULL3[57]	V	
381	float	RDD	_FFT_ULL3[58]	V	
383	float	RDD	_FFT_ULL3[59]	V	
385	float	RDD	_FFT_ULL3[60]	V	
387	float	RDD	_FFT_ULL3[61]	V	
389	float	RDD	_FFT_ULL3[62]	V	
391	float	RDD	_FFT_UL1[0]	V	
393	float	RDD	_FFT_UL1[1]	V	
395	float	RDD	_FFT_UL1[2]	V	
397	float	RDD	_FFT_UL1[3]	V	
399	float	RDD	_FFT_UL1[4]	V	
401	float	RDD	_FFT_UL1[5]	V	
403	float	RDD	_FFT_UL1[6]	V	
405	float	RDD	_FFT_UL1[7]	V	
407	float	RDD	_FFT_UL1[8]	V	
409	float	RDD	_FFT_UL1[9]	V	
411	float	RDD	_FFT_UL1[10]	V	
413	float	RDD	_FFT_UL1[11]	V	
415	float	RDD	_FFT_UL1[12]	V	
417	float	RDD	_FFT_UL1[13]	V	
419	float	RDD	_FFT_UL1[14]	V	
421	float	RDD	_FFT_UL1[15]	V	
423	float	RDD	_FFT_UL1[16]	V	
425	float	RDD	_FFT_UL1[17]	V	
427	float	RDD	_FFT_UL1[18]	V	
429	float	RDD	_FFT_UL1[19]	V	
431	float	RDD	_FFT_UL1[20]	V	
433	float	RDD	_FFT_UL1[21]	V	
435	float	RDD	_FFT_UL1[22]	V	
437	float	RDD	_FFT_UL1[23]	V	
439	float	RDD	_FFT_UL1[24]	V	
441	float	RDD	_FFT_UL1[25]	V	
443	float	RDD	_FFT_UL1[26]	V	
445	float	RDD	_FFT_UL1[27]	V	
447	float	RDD	_FFT_UL1[28]	V	
449	float	RDD	_FFT_UL1[29]	V	
451	float	RDD	_FFT_UL1[30]	V	
453	float	RDD	_FFT_UL1[31]	V	
455	float	RDD	_FFT_UL1[32]	V	
457	float	RDD	_FFT_UL1[33]	V	
459	float	RDD	_FFT_UL1[34]	V	
461	float	RDD	_FFT_UL1[35]	V	
463	float	RDD	_FFT_UL1[36]	V	
465	float	RDD	_FFT_UL1[37]	V	
467	float	RDD	_FFT_UL1[38]	V	
469	float	RDD	_FFT_UL1[39]	V	
471	float	RDD	_FFT_UL1[40]	V	
473	float	RDD	_FFT_UL1[41]	V	
475	float	RDD	_FFT_UL1[42]	V	

Address	Format	RD/WR	Designation	Unit	Note
477	float	RD	_FFT_UL1[43]	V	
479	float	RD	_FFT_UL1[44]	V	
481	float	RD	_FFT_UL1[45]	V	
483	float	RD	_FFT_UL1[46]	V	
485	float	RD	_FFT_UL1[47]	V	
487	float	RD	_FFT_UL1[48]	V	
489	float	RD	_FFT_UL1[49]	V	
491	float	RD	_FFT_UL1[50]	V	
493	float	RD	_FFT_UL1[51]	V	
495	float	RD	_FFT_UL1[52]	V	
497	float	RD	_FFT_UL1[53]	V	
499	float	RD	_FFT_UL1[54]	V	
501	float	RD	_FFT_UL1[55]	V	
503	float	RD	_FFT_UL1[56]	V	
505	float	RD	_FFT_UL1[57]	V	
507	float	RD	_FFT_UL1[58]	V	
509	float	RD	_FFT_UL1[59]	V	
511	float	RD	_FFT_UL1[60]	V	
513	float	RD	_FFT_UL1[61]	V	
515	float	RD	_FFT_UL1[62]	V	
517	float	RD	_FFT_UL2[0]	V	
519	float	RD	_FFT_UL2[1]	V	
521	float	RD	_FFT_UL2[2]	V	
523	float	RD	_FFT_UL2[3]	V	
525	float	RD	_FFT_UL2[4]	V	
527	float	RD	_FFT_UL2[5]	V	
529	float	RD	_FFT_UL2[6]	V	
531	float	RD	_FFT_UL2[7]	V	
533	float	RD	_FFT_UL2[8]	V	
535	float	RD	_FFT_UL2[9]	V	
537	float	RD	_FFT_UL2[10]	V	
539	float	RD	_FFT_UL2[11]	V	
541	float	RD	_FFT_UL2[12]	V	
543	float	RD	_FFT_UL2[13]	V	
545	float	RD	_FFT_UL2[14]	V	
547	float	RD	_FFT_UL2[15]	V	
549	float	RD	_FFT_UL2[16]	V	
551	float	RD	_FFT_UL2[17]	V	
553	float	RD	_FFT_UL2[18]	V	
555	float	RD	_FFT_UL2[19]	V	
557	float	RD	_FFT_UL2[20]	V	
559	float	RD	_FFT_UL2[21]	V	
561	float	RD	_FFT_UL2[22]	V	
563	float	RD	_FFT_UL2[23]	V	
565	float	RD	_FFT_UL2[24]	V	
567	float	RD	_FFT_UL2[25]	V	
569	float	RD	_FFT_UL2[26]	V	
571	float	RD	_FFT_UL2[27]	V	
573	float	RD	_FFT_UL2[28]	V	
575	float	RD	_FFT_UL2[29]	V	
577	float	RD	_FFT_UL2[30]	V	
579	float	RD	_FFT_UL2[31]	V	
581	float	RD	_FFT_UL2[32]	V	
583	float	RD	_FFT_UL2[33]	V	
585	float	RD	_FFT_UL2[34]	V	
587	float	RD	_FFT_UL2[35]	V	
589	float	RD	_FFT_UL2[36]	V	
591	float	RD	_FFT_UL2[37]	V	

Adresse	Format	RD/WR	Designation	Unit	Note
593	float	RD	_FFT_UL2[38]	V	
595	float	RD	_FFT_UL2[39]	V	
597	float	RD	_FFT_UL2[40]	V	
599	float	RD	_FFT_UL2[41]	V	
601	float	RD	_FFT_UL2[42]	V	
603	float	RD	_FFT_UL2[43]	V	
605	float	RD	_FFT_UL2[44]	V	
607	float	RD	_FFT_UL2[45]	V	
609	float	RD	_FFT_UL2[46]	V	
611	float	RD	_FFT_UL2[47]	V	
613	float	RD	_FFT_UL2[48]	V	
615	float	RD	_FFT_UL2[49]	V	
617	float	RD	_FFT_UL2[50]	V	
619	float	RD	_FFT_UL2[51]	V	
621	float	RD	_FFT_UL2[52]	V	
623	float	RD	_FFT_UL2[53]	V	
625	float	RD	_FFT_UL2[54]	V	
627	float	RD	_FFT_UL2[55]	V	
629	float	RD	_FFT_UL2[56]	V	
631	float	RD	_FFT_UL2[57]	V	
633	float	RD	_FFT_UL2[58]	V	
635	float	RD	_FFT_UL2[59]	V	
637	float	RD	_FFT_UL2[60]	V	
639	float	RD	_FFT_UL2[61]	V	
641	float	RD	_FFT_UL2[62]	V	
643	float	RD	_FFT_UL3[0]	V	
645	float	RD	_FFT_UL3[1]	V	
647	float	RD	_FFT_UL3[2]	V	
649	float	RD	_FFT_UL3[3]	V	
651	float	RD	_FFT_UL3[4]	V	
653	float	RD	_FFT_UL3[5]	V	
655	float	RD	_FFT_UL3[6]	V	
657	float	RD	_FFT_UL3[7]	V	
659	float	RD	_FFT_UL3[8]	V	
661	float	RD	_FFT_UL3[9]	V	
663	float	RD	_FFT_UL3[10]	V	
665	float	RD	_FFT_UL3[11]	V	
667	float	RD	_FFT_UL3[12]	V	
669	float	RD	_FFT_UL3[13]	V	
671	float	RD	_FFT_UL3[14]	V	
673	float	RD	_FFT_UL3[15]	V	
675	float	RD	_FFT_UL3[16]	V	
677	float	RD	_FFT_UL3[17]	V	
679	float	RD	_FFT_UL3[18]	V	
681	float	RD	_FFT_UL3[19]	V	
683	float	RD	_FFT_UL3[20]	V	
685	float	RD	_FFT_UL3[21]	V	
687	float	RD	_FFT_UL3[22]	V	
689	float	RD	_FFT_UL3[23]	V	
691	float	RD	_FFT_UL3[24]	V	
693	float	RD	_FFT_UL3[25]	V	
695	float	RD	_FFT_UL3[26]	V	
697	float	RD	_FFT_UL3[27]	V	
699	float	RD	_FFT_UL3[28]	V	
701	float	RD	_FFT_UL3[29]	V	
703	float	RD	_FFT_UL3[30]	V	
705	float	RD	_FFT_UL3[31]	V	
707	float	RD	_FFT_UL3[32]	V	

Address	Format	RD/WR	Designation	Unit	Note
709	float	RD	_FFT_UL3[33]	V	
711	float	RD	_FFT_UL3[34]	V	
713	float	RD	_FFT_UL3[35]	V	
715	float	RD	_FFT_UL3[36]	V	
717	float	RD	_FFT_UL3[37]	V	
719	float	RD	_FFT_UL3[38]	V	
721	float	RD	_FFT_UL3[39]	V	
723	float	RD	_FFT_UL3[40]	V	
725	float	RD	_FFT_UL3[41]	V	
727	float	RD	_FFT_UL3[42]	V	
729	float	RD	_FFT_UL3[43]	V	
731	float	RD	_FFT_UL3[44]	V	
733	float	RD	_FFT_UL3[45]	V	
735	float	RD	_FFT_UL3[46]	V	
737	float	RD	_FFT_UL3[47]	V	
739	float	RD	_FFT_UL3[48]	V	
741	float	RD	_FFT_UL3[49]	V	
743	float	RD	_FFT_UL3[50]	V	
745	float	RD	_FFT_UL3[51]	V	
747	float	RD	_FFT_UL3[52]	V	
749	float	RD	_FFT_UL3[53]	V	
751	float	RD	_FFT_UL3[54]	V	
753	float	RD	_FFT_UL3[55]	V	
755	float	RD	_FFT_UL3[56]	V	
757	float	RD	_FFT_UL3[57]	V	
759	float	RD	_FFT_UL3[58]	V	
761	float	RD	_FFT_UL3[59]	V	
763	float	RD	_FFT_UL3[60]	V	
765	float	RD	_FFT_UL3[61]	V	
767	float	RD	_FFT_UL3[62]	V	
769	float	RD	_FFT_UL4[0]	V	
771	float	RD	_FFT_UL4[1]	V	
773	float	RD	_FFT_UL4[2]	V	
775	float	RD	_FFT_UL4[3]	V	
777	float	RD	_FFT_UL4[4]	V	
779	float	RD	_FFT_UL4[5]	V	
781	float	RD	_FFT_UL4[6]	V	
783	float	RD	_FFT_UL4[7]	V	
785	float	RD	_FFT_UL4[8]	V	
787	float	RD	_FFT_UL4[9]	V	
789	float	RD	_FFT_UL4[10]	V	
791	float	RD	_FFT_UL4[11]	V	
793	float	RD	_FFT_UL4[12]	V	
795	float	RD	_FFT_UL4[13]	V	
797	float	RD	_FFT_UL4[14]	V	
799	float	RD	_FFT_UL4[15]	V	
801	float	RD	_FFT_UL4[16]	V	
803	float	RD	_FFT_UL4[17]	V	
805	float	RD	_FFT_UL4[18]	V	
807	float	RD	_FFT_UL4[19]	V	
809	float	RD	_FFT_UL4[20]	V	
811	float	RD	_FFT_UL4[21]	V	
813	float	RD	_FFT_UL4[22]	V	
815	float	RD	_FFT_UL4[23]	V	
817	float	RD	_FFT_UL4[24]	V	
819	float	RD	_FFT_UL4[25]	V	
821	float	RD	_FFT_UL4[26]	V	
823	float	RD	_FFT_UL4[27]	V	

Adresse	Format	RD/WR	Designation	Unit	Note
825	float	RD	_FFT_UL4[28]	V	
827	float	RD	_FFT_UL4[29]	V	
829	float	RD	_FFT_UL4[30]	V	
831	float	RD	_FFT_UL4[31]	V	
833	float	RD	_FFT_UL4[32]	V	
835	float	RD	_FFT_UL4[33]	V	
837	float	RD	_FFT_UL4[34]	V	
839	float	RD	_FFT_UL4[35]	V	
841	float	RD	_FFT_UL4[36]	V	
843	float	RD	_FFT_UL4[37]	V	
845	float	RD	_FFT_UL4[38]	V	
847	float	RD	_FFT_UL4[39]	V	
849	float	RD	_FFT_UL4[40]	V	
851	float	RD	_FFT_UL4[41]	V	
853	float	RD	_FFT_UL4[42]	V	
855	float	RD	_FFT_UL4[43]	V	
857	float	RD	_FFT_UL4[44]	V	
859	float	RD	_FFT_UL4[45]	V	
861	float	RD	_FFT_UL4[46]	V	
863	float	RD	_FFT_UL4[47]	V	
865	float	RD	_FFT_UL4[48]	V	
867	float	RD	_FFT_UL4[49]	V	
869	float	RD	_FFT_UL4[50]	V	
871	float	RD	_FFT_UL4[51]	V	
873	float	RD	_FFT_UL4[52]	V	
875	float	RD	_FFT_UL4[53]	V	
877	float	RD	_FFT_UL4[54]	V	
879	float	RD	_FFT_UL4[55]	V	
881	float	RD	_FFT_UL4[56]	V	
883	float	RD	_FFT_UL4[57]	V	
885	float	RD	_FFT_UL4[58]	V	
887	float	RD	_FFT_UL4[59]	V	
889	float	RD	_FFT_UL4[60]	V	
891	float	RD	_FFT_UL4[61]	V	
893	float	RD	_FFT_UL4[62]	V	
895	float	RD	_FFT_IL1[0]	A	
897	float	RD	_FFT_IL1[1]	A	
899	float	RD	_FFT_IL1[2]	A	
901	float	RD	_FFT_IL1[3]	A	
903	float	RD	_FFT_IL1[4]	A	
905	float	RD	_FFT_IL1[5]	A	
907	float	RD	_FFT_IL1[6]	A	
909	float	RD	_FFT_IL1[7]	A	
911	float	RD	_FFT_IL1[8]	A	
913	float	RD	_FFT_IL1[9]	A	
915	float	RD	_FFT_IL1[10]	A	
917	float	RD	_FFT_IL1[11]	A	
919	float	RD	_FFT_IL1[12]	A	
921	float	RD	_FFT_IL1[13]	A	
923	float	RD	_FFT_IL1[14]	A	
925	float	RD	_FFT_IL1[15]	A	
927	float	RD	_FFT_IL1[16]	A	
929	float	RD	_FFT_IL1[17]	A	
931	float	RD	_FFT_IL1[18]	A	
933	float	RD	_FFT_IL1[19]	A	
935	float	RD	_FFT_IL1[20]	A	
937	float	RD	_FFT_IL1[21]	A	
939	float	RD	_FFT_IL1[22]	A	

Address	Format	RD/WR	Designation	Unit	Note
941	float	RD	_FFT_IL1[23]	A	
943	float	RD	_FFT_IL1[24]	A	
945	float	RD	_FFT_IL1[25]	A	
947	float	RD	_FFT_IL1[26]	A	
949	float	RD	_FFT_IL1[27]	A	
951	float	RD	_FFT_IL1[28]	A	
953	float	RD	_FFT_IL1[29]	A	
955	float	RD	_FFT_IL1[30]	A	
957	float	RD	_FFT_IL1[31]	A	
959	float	RD	_FFT_IL1[32]	A	
961	float	RD	_FFT_IL1[33]	A	
963	float	RD	_FFT_IL1[34]	A	
965	float	RD	_FFT_IL1[35]	A	
967	float	RD	_FFT_IL1[36]	A	
969	float	RD	_FFT_IL1[37]	A	
971	float	RD	_FFT_IL1[38]	A	
973	float	RD	_FFT_IL1[39]	A	
975	float	RD	_FFT_IL1[40]	A	
977	float	RD	_FFT_IL1[41]	A	
979	float	RD	_FFT_IL1[42]	A	
981	float	RD	_FFT_IL1[43]	A	
983	float	RD	_FFT_IL1[44]	A	
985	float	RD	_FFT_IL1[45]	A	
987	float	RD	_FFT_IL1[46]	A	
989	float	RD	_FFT_IL1[47]	A	
991	float	RD	_FFT_IL1[48]	A	
993	float	RD	_FFT_IL1[49]	A	
995	float	RD	_FFT_IL1[50]	A	
997	float	RD	_FFT_IL1[51]	A	
999	float	RD	_FFT_IL1[52]	A	
1001	float	RD	_FFT_IL1[53]	A	
1003	float	RD	_FFT_IL1[54]	A	
1005	float	RD	_FFT_IL1[55]	A	
1007	float	RD	_FFT_IL1[56]	A	
1009	float	RD	_FFT_IL1[57]	A	
1011	float	RD	_FFT_IL1[58]	A	
1013	float	RD	_FFT_IL1[59]	A	
1015	float	RD	_FFT_IL1[60]	A	
1017	float	RD	_FFT_IL1[61]	A	
1019	float	RD	_FFT_IL1[62]	A	
1021	float	RD	_FFT_IL2[0]	A	
1023	float	RD	_FFT_IL2[1]	A	
1025	float	RD	_FFT_IL2[2]	A	
1027	float	RD	_FFT_IL2[3]	A	
1029	float	RD	_FFT_IL2[4]	A	
1031	float	RD	_FFT_IL2[5]	A	
1033	float	RD	_FFT_IL2[6]	A	
1035	float	RD	_FFT_IL2[7]	A	
1037	float	RD	_FFT_IL2[8]	A	
1039	float	RD	_FFT_IL2[9]	A	
1041	float	RD	_FFT_IL2[10]	A	
1043	float	RD	_FFT_IL2[11]	A	
1045	float	RD	_FFT_IL2[12]	A	
1047	float	RD	_FFT_IL2[13]	A	
1049	float	RD	_FFT_IL2[14]	A	
1051	float	RD	_FFT_IL2[15]	A	
1053	float	RD	_FFT_IL2[16]	A	
1055	float	RD	_FFT_IL2[17]	A	

Adresse	Format	RD/WR	Designation	Unit	Note
1057	float	RD	_FFT_IL2[18]	A	
1059	float	RD	_FFT_IL2[19]	A	
1061	float	RD	_FFT_IL2[20]	A	
1063	float	RD	_FFT_IL2[21]	A	
1065	float	RD	_FFT_IL2[22]	A	
1067	float	RD	_FFT_IL2[23]	A	
1069	float	RD	_FFT_IL2[24]	A	
1071	float	RD	_FFT_IL2[25]	A	
1073	float	RD	_FFT_IL2[26]	A	
1075	float	RD	_FFT_IL2[27]	A	
1077	float	RD	_FFT_IL2[28]	A	
1079	float	RD	_FFT_IL2[29]	A	
1081	float	RD	_FFT_IL2[30]	A	
1083	float	RD	_FFT_IL2[31]	A	
1085	float	RD	_FFT_IL2[32]	A	
1087	float	RD	_FFT_IL2[33]	A	
1089	float	RD	_FFT_IL2[34]	A	
1091	float	RD	_FFT_IL2[35]	A	
1093	float	RD	_FFT_IL2[36]	A	
1095	float	RD	_FFT_IL2[37]	A	
1097	float	RD	_FFT_IL2[38]	A	
1099	float	RD	_FFT_IL2[39]	A	
1101	float	RD	_FFT_IL2[40]	A	
1103	float	RD	_FFT_IL2[41]	A	
1105	float	RD	_FFT_IL2[42]	A	
1107	float	RD	_FFT_IL2[43]	A	
1109	float	RD	_FFT_IL2[44]	A	
1111	float	RD	_FFT_IL2[45]	A	
1113	float	RD	_FFT_IL2[46]	A	
1115	float	RD	_FFT_IL2[47]	A	
1117	float	RD	_FFT_IL2[48]	A	
1119	float	RD	_FFT_IL2[49]	A	
1121	float	RD	_FFT_IL2[50]	A	
1123	float	RD	_FFT_IL2[51]	A	
1125	float	RD	_FFT_IL2[52]	A	
1127	float	RD	_FFT_IL2[53]	A	
1129	float	RD	_FFT_IL2[54]	A	
1131	float	RD	_FFT_IL2[55]	A	
1133	float	RD	_FFT_IL2[56]	A	
1135	float	RD	_FFT_IL2[57]	A	
1137	float	RD	_FFT_IL2[58]	A	
1139	float	RD	_FFT_IL2[59]	A	
1141	float	RD	_FFT_IL2[60]	A	
1143	float	RD	_FFT_IL2[61]	A	
1145	float	RD	_FFT_IL2[62]	A	
1147	float	RD	_FFT_IL3[0]	A	
1149	float	RD	_FFT_IL3[1]	A	
1151	float	RD	_FFT_IL3[2]	A	
1153	float	RD	_FFT_IL3[3]	A	
1155	float	RD	_FFT_IL3[4]	A	
1157	float	RD	_FFT_IL3[5]	A	
1159	float	RD	_FFT_IL3[6]	A	
1161	float	RD	_FFT_IL3[7]	A	
1163	float	RD	_FFT_IL3[8]	A	
1165	float	RD	_FFT_IL3[9]	A	
1167	float	RD	_FFT_IL3[10]	A	
1169	float	RD	_FFT_IL3[11]	A	
1171	float	RD	_FFT_IL3[12]	A	

Address	Format	RD/WR	Designation	Unit	Note
1173	float	RD	_FFT_IL3[13]	A	
1175	float	RD	_FFT_IL3[14]	A	
1177	float	RD	_FFT_IL3[15]	A	
1179	float	RD	_FFT_IL3[16]	A	
1181	float	RD	_FFT_IL3[17]	A	
1183	float	RD	_FFT_IL3[18]	A	
1185	float	RD	_FFT_IL3[19]	A	
1187	float	RD	_FFT_IL3[20]	A	
1189	float	RD	_FFT_IL3[21]	A	
1191	float	RD	_FFT_IL3[22]	A	
1193	float	RD	_FFT_IL3[23]	A	
1195	float	RD	_FFT_IL3[24]	A	
1197	float	RD	_FFT_IL3[25]	A	
1199	float	RD	_FFT_IL3[26]	A	
1201	float	RD	_FFT_IL3[27]	A	
1203	float	RD	_FFT_IL3[28]	A	
1205	float	RD	_FFT_IL3[29]	A	
1207	float	RD	_FFT_IL3[30]	A	
1209	float	RD	_FFT_IL3[31]	A	
1211	float	RD	_FFT_IL3[32]	A	
1213	float	RD	_FFT_IL3[33]	A	
1215	float	RD	_FFT_IL3[34]	A	
1217	float	RD	_FFT_IL3[35]	A	
1219	float	RD	_FFT_IL3[36]	A	
1221	float	RD	_FFT_IL3[37]	A	
1223	float	RD	_FFT_IL3[38]	A	
1225	float	RD	_FFT_IL3[39]	A	
1227	float	RD	_FFT_IL3[40]	A	
1229	float	RD	_FFT_IL3[41]	A	
1231	float	RD	_FFT_IL3[42]	A	
1233	float	RD	_FFT_IL3[43]	A	
1235	float	RD	_FFT_IL3[44]	A	
1237	float	RD	_FFT_IL3[45]	A	
1239	float	RD	_FFT_IL3[46]	A	
1241	float	RD	_FFT_IL3[47]	A	
1243	float	RD	_FFT_IL3[48]	A	
1245	float	RD	_FFT_IL3[49]	A	
1247	float	RD	_FFT_IL3[50]	A	
1249	float	RD	_FFT_IL3[51]	A	
1251	float	RD	_FFT_IL3[52]	A	
1253	float	RD	_FFT_IL3[53]	A	
1255	float	RD	_FFT_IL3[54]	A	
1257	float	RD	_FFT_IL3[55]	A	
1259	float	RD	_FFT_IL3[56]	A	
1261	float	RD	_FFT_IL3[57]	A	
1263	float	RD	_FFT_IL3[58]	A	
1265	float	RD	_FFT_IL3[59]	A	
1267	float	RD	_FFT_IL3[60]	A	
1269	float	RD	_FFT_IL3[61]	A	
1271	float	RD	_FFT_IL3[62]	A	
1273	float	RD	_FFT_IL4[0]	A	
1275	float	RD	_FFT_IL4[1]	A	
1277	float	RD	_FFT_IL4[2]	A	
1279	float	RD	_FFT_IL4[3]	A	
1281	float	RD	_FFT_IL4[4]	A	
1283	float	RD	_FFT_IL4[5]	A	
1285	float	RD	_FFT_IL4[6]	A	
1287	float	RD	_FFT_IL4[7]	A	

Adresse	Format	RD/WR	Designation	Unit	Note
1289	float	RD	_FFT_IL4[8]	A	
1291	float	RD	_FFT_IL4[9]	A	
1293	float	RD	_FFT_IL4[10]	A	
1295	float	RD	_FFT_IL4[11]	A	
1297	float	RD	_FFT_IL4[12]	A	
1299	float	RD	_FFT_IL4[13]	A	
1301	float	RD	_FFT_IL4[14]	A	
1303	float	RD	_FFT_IL4[15]	A	
1305	float	RD	_FFT_IL4[16]	A	
1307	float	RD	_FFT_IL4[17]	A	
1309	float	RD	_FFT_IL4[18]	A	
1311	float	RD	_FFT_IL4[19]	A	
1313	float	RD	_FFT_IL4[20]	A	
1315	float	RD	_FFT_IL4[21]	A	
1317	float	RD	_FFT_IL4[22]	A	
1319	float	RD	_FFT_IL4[23]	A	
1321	float	RD	_FFT_IL4[24]	A	
1323	float	RD	_FFT_IL4[25]	A	
1325	float	RD	_FFT_IL4[26]	A	
1327	float	RD	_FFT_IL4[27]	A	
1329	float	RD	_FFT_IL4[28]	A	
1331	float	RD	_FFT_IL4[29]	A	
1333	float	RD	_FFT_IL4[30]	A	
1335	float	RD	_FFT_IL4[31]	A	
1337	float	RD	_FFT_IL4[32]	A	
1339	float	RD	_FFT_IL4[33]	A	
1341	float	RD	_FFT_IL4[34]	A	
1343	float	RD	_FFT_IL4[35]	A	
1345	float	RD	_FFT_IL4[36]	A	
1347	float	RD	_FFT_IL4[37]	A	
1349	float	RD	_FFT_IL4[38]	A	
1351	float	RD	_FFT_IL4[39]	A	
1353	float	RD	_FFT_IL4[40]	A	
1355	float	RD	_FFT_IL4[41]	A	
1357	float	RD	_FFT_IL4[42]	A	
1359	float	RD	_FFT_IL4[43]	A	
1361	float	RD	_FFT_IL4[44]	A	
1363	float	RD	_FFT_IL4[45]	A	
1365	float	RD	_FFT_IL4[46]	A	
1367	float	RD	_FFT_IL4[47]	A	
1369	float	RD	_FFT_IL4[48]	A	
1371	float	RD	_FFT_IL4[49]	A	
1373	float	RD	_FFT_IL4[50]	A	
1375	float	RD	_FFT_IL4[51]	A	
1377	float	RD	_FFT_IL4[52]	A	
1379	float	RD	_FFT_IL4[53]	A	
1381	float	RD	_FFT_IL4[54]	A	
1383	float	RD	_FFT_IL4[55]	A	
1385	float	RD	_FFT_IL4[56]	A	
1387	float	RD	_FFT_IL4[57]	A	
1389	float	RD	_FFT_IL4[58]	A	
1391	float	RD	_FFT_IL4[59]	A	
1393	float	RD	_FFT_IL4[60]	A	
1395	float	RD	_FFT_IL4[61]	A	
1397	float	RD	_FFT_IL4[62]	A	
1399	float	RD	_FFT_PL1[0]	W	
1401	float	RD	_FFT_PL1[1]	W	
1403	float	RD	_FFT_PL1[2]	W	

Address	Format	RD/WR	Designation	Unit	Note
1405	float	RD	_FFT_PL1[3]	W	
1407	float	RD	_FFT_PL1[4]	W	
1409	float	RD	_FFT_PL1[5]	W	
1411	float	RD	_FFT_PL1[6]	W	
1413	float	RD	_FFT_PL1[7]	W	
1415	float	RD	_FFT_PL1[8]	W	
1417	float	RD	_FFT_PL1[9]	W	
1419	float	RD	_FFT_PL1[10]	W	
1421	float	RD	_FFT_PL1[11]	W	
1423	float	RD	_FFT_PL1[12]	W	
1425	float	RD	_FFT_PL1[13]	W	
1427	float	RD	_FFT_PL1[14]	W	
1429	float	RD	_FFT_PL1[15]	W	
1431	float	RD	_FFT_PL1[16]	W	
1433	float	RD	_FFT_PL1[17]	W	
1435	float	RD	_FFT_PL1[18]	W	
1437	float	RD	_FFT_PL1[19]	W	
1439	float	RD	_FFT_PL1[20]	W	
1441	float	RD	_FFT_PL1[21]	W	
1443	float	RD	_FFT_PL1[22]	W	
1445	float	RD	_FFT_PL1[23]	W	
1447	float	RD	_FFT_PL1[24]	W	
1449	float	RD	_FFT_PL1[25]	W	
1451	float	RD	_FFT_PL1[26]	W	
1453	float	RD	_FFT_PL1[27]	W	
1455	float	RD	_FFT_PL1[28]	W	
1457	float	RD	_FFT_PL1[29]	W	
1459	float	RD	_FFT_PL1[30]	W	
1461	float	RD	_FFT_PL1[31]	W	
1463	float	RD	_FFT_PL1[32]	W	
1465	float	RD	_FFT_PL1[33]	W	
1467	float	RD	_FFT_PL1[34]	W	
1469	float	RD	_FFT_PL1[35]	W	
1471	float	RD	_FFT_PL1[36]	W	
1473	float	RD	_FFT_PL1[37]	W	
1475	float	RD	_FFT_PL1[38]	W	
1477	float	RD	_FFT_PL1[39]	W	
1479	float	RD	_FFT_PL1[40]	W	
1481	float	RD	_FFT_PL1[41]	W	
1483	float	RD	_FFT_PL1[42]	W	
1485	float	RD	_FFT_PL1[43]	W	
1487	float	RD	_FFT_PL1[44]	W	
1489	float	RD	_FFT_PL1[45]	W	
1491	float	RD	_FFT_PL1[46]	W	
1493	float	RD	_FFT_PL1[47]	W	
1495	float	RD	_FFT_PL1[48]	W	
1497	float	RD	_FFT_PL1[49]	W	
1499	float	RD	_FFT_PL1[50]	W	
1501	float	RD	_FFT_PL1[51]	W	
1503	float	RD	_FFT_PL1[52]	W	
1505	float	RD	_FFT_PL1[53]	W	
1507	float	RD	_FFT_PL1[54]	W	
1509	float	RD	_FFT_PL1[55]	W	
1511	float	RD	_FFT_PL1[56]	W	
1513	float	RD	_FFT_PL1[57]	W	
1515	float	RD	_FFT_PL1[58]	W	
1517	float	RD	_FFT_PL1[59]	W	
1519	float	RD	_FFT_PL1[60]	W	

Adresse	Format	RD/WR	Designation	Unit	Note
1521	float	RD	_FFT_PL1[61]	W	
1523	float	RD	_FFT_PL1[62]	W	
1525	float	RD	_FFT_PL2[0]	W	
1527	float	RD	_FFT_PL2[1]	W	
1529	float	RD	_FFT_PL2[2]	W	
1531	float	RD	_FFT_PL2[3]	W	
1533	float	RD	_FFT_PL2[4]	W	
1535	float	RD	_FFT_PL2[5]	W	
1537	float	RD	_FFT_PL2[6]	W	
1539	float	RD	_FFT_PL2[7]	W	
1541	float	RD	_FFT_PL2[8]	W	
1543	float	RD	_FFT_PL2[9]	W	
1545	float	RD	_FFT_PL2[10]	W	
1547	float	RD	_FFT_PL2[11]	W	
1549	float	RD	_FFT_PL2[12]	W	
1551	float	RD	_FFT_PL2[13]	W	
1553	float	RD	_FFT_PL2[14]	W	
1555	float	RD	_FFT_PL2[15]	W	
1557	float	RD	_FFT_PL2[16]	W	
1559	float	RD	_FFT_PL2[17]	W	
1561	float	RD	_FFT_PL2[18]	W	
1563	float	RD	_FFT_PL2[19]	W	
1565	float	RD	_FFT_PL2[20]	W	
1567	float	RD	_FFT_PL2[21]	W	
1569	float	RD	_FFT_PL2[22]	W	
1571	float	RD	_FFT_PL2[23]	W	
1573	float	RD	_FFT_PL2[24]	W	
1575	float	RD	_FFT_PL2[25]	W	
1577	float	RD	_FFT_PL2[26]	W	
1579	float	RD	_FFT_PL2[27]	W	
1581	float	RD	_FFT_PL2[28]	W	
1583	float	RD	_FFT_PL2[29]	W	
1585	float	RD	_FFT_PL2[30]	W	
1587	float	RD	_FFT_PL2[31]	W	
1589	float	RD	_FFT_PL2[32]	W	
1591	float	RD	_FFT_PL2[33]	W	
1593	float	RD	_FFT_PL2[34]	W	
1595	float	RD	_FFT_PL2[35]	W	
1597	float	RD	_FFT_PL2[36]	W	
1599	float	RD	_FFT_PL2[37]	W	
1601	float	RD	_FFT_PL2[38]	W	
1603	float	RD	_FFT_PL2[39]	W	
1605	float	RD	_FFT_PL2[40]	W	
1607	float	RD	_FFT_PL2[41]	W	
1609	float	RD	_FFT_PL2[42]	W	
1611	float	RD	_FFT_PL2[43]	W	
1613	float	RD	_FFT_PL2[44]	W	
1615	float	RD	_FFT_PL2[45]	W	
1617	float	RD	_FFT_PL2[46]	W	
1619	float	RD	_FFT_PL2[47]	W	
1621	float	RD	_FFT_PL2[48]	W	
1623	float	RD	_FFT_PL2[49]	W	
1625	float	RD	_FFT_PL2[50]	W	
1627	float	RD	_FFT_PL2[51]	W	
1629	float	RD	_FFT_PL2[52]	W	
1631	float	RD	_FFT_PL2[53]	W	
1633	float	RD	_FFT_PL2[54]	W	
1635	float	RD	_FFT_PL2[55]	W	

Address	Format	RD/WR	Designation	Unit	Note
1637	float	RD	_FFT_PL2[56]	W	
1639	float	RD	_FFT_PL2[57]	W	
1641	float	RD	_FFT_PL2[58]	W	
1643	float	RD	_FFT_PL2[59]	W	
1645	float	RD	_FFT_PL2[60]	W	
1647	float	RD	_FFT_PL2[61]	W	
1649	float	RD	_FFT_PL2[62]	W	
1651	float	RD	_FFT_PL3[0]	W	
1653	float	RD	_FFT_PL3[1]	W	
1655	float	RD	_FFT_PL3[2]	W	
1657	float	RD	_FFT_PL3[3]	W	
1659	float	RD	_FFT_PL3[4]	W	
1661	float	RD	_FFT_PL3[5]	W	
1663	float	RD	_FFT_PL3[6]	W	
1665	float	RD	_FFT_PL3[7]	W	
1667	float	RD	_FFT_PL3[8]	W	
1669	float	RD	_FFT_PL3[9]	W	
1671	float	RD	_FFT_PL3[10]	W	
1673	float	RD	_FFT_PL3[11]	W	
1675	float	RD	_FFT_PL3[12]	W	
1677	float	RD	_FFT_PL3[13]	W	
1679	float	RD	_FFT_PL3[14]	W	
1681	float	RD	_FFT_PL3[15]	W	
1683	float	RD	_FFT_PL3[16]	W	
1685	float	RD	_FFT_PL3[17]	W	
1687	float	RD	_FFT_PL3[18]	W	
1689	float	RD	_FFT_PL3[19]	W	
1691	float	RD	_FFT_PL3[20]	W	
1693	float	RD	_FFT_PL3[21]	W	
1695	float	RD	_FFT_PL3[22]	W	
1697	float	RD	_FFT_PL3[23]	W	
1699	float	RD	_FFT_PL3[24]	W	
1701	float	RD	_FFT_PL3[25]	W	
1703	float	RD	_FFT_PL3[26]	W	
1705	float	RD	_FFT_PL3[27]	W	
1707	float	RD	_FFT_PL3[28]	W	
1709	float	RD	_FFT_PL3[29]	W	
1711	float	RD	_FFT_PL3[30]	W	
1713	float	RD	_FFT_PL3[31]	W	
1715	float	RD	_FFT_PL3[32]	W	
1717	float	RD	_FFT_PL3[33]	W	
1719	float	RD	_FFT_PL3[34]	W	
1721	float	RD	_FFT_PL3[35]	W	
1723	float	RD	_FFT_PL3[36]	W	
1725	float	RD	_FFT_PL3[37]	W	
1727	float	RD	_FFT_PL3[38]	W	
1729	float	RD	_FFT_PL3[39]	W	
1731	float	RD	_FFT_PL3[40]	W	
1733	float	RD	_FFT_PL3[41]	W	
1735	float	RD	_FFT_PL3[42]	W	
1737	float	RD	_FFT_PL3[43]	W	
1739	float	RD	_FFT_PL3[44]	W	
1741	float	RD	_FFT_PL3[45]	W	
1743	float	RD	_FFT_PL3[46]	W	
1745	float	RD	_FFT_PL3[47]	W	
1747	float	RD	_FFT_PL3[48]	W	
1749	float	RD	_FFT_PL3[49]	W	
1751	float	RD	_FFT_PL3[50]	W	

Adresse	Format	RD/WR	Designation	Unit	Note
1753	float	RD	_FFT_PL3[51]	W	
1755	float	RD	_FFT_PL3[52]	W	
1757	float	RD	_FFT_PL3[53]	W	
1759	float	RD	_FFT_PL3[54]	W	
1761	float	RD	_FFT_PL3[55]	W	
1763	float	RD	_FFT_PL3[56]	W	
1765	float	RD	_FFT_PL3[57]	W	
1767	float	RD	_FFT_PL3[58]	W	
1769	float	RD	_FFT_PL3[59]	W	
1771	float	RD	_FFT_PL3[60]	W	
1773	float	RD	_FFT_PL3[61]	W	
1775	float	RD	_FFT_PL3[62]	W	
1777	float	RD	_FFT_PL4[0]	W	
1779	float	RD	_FFT_PL4[1]	W	
1781	float	RD	_FFT_PL4[2]	W	
1783	float	RD	_FFT_PL4[3]	W	
1785	float	RD	_FFT_PL4[4]	W	
1787	float	RD	_FFT_PL4[5]	W	
1789	float	RD	_FFT_PL4[6]	W	
1791	float	RD	_FFT_PL4[7]	W	
1793	float	RD	_FFT_PL4[8]	W	
1795	float	RD	_FFT_PL4[9]	W	
1797	float	RD	_FFT_PL4[10]	W	
1799	float	RD	_FFT_PL4[11]	W	
1801	float	RD	_FFT_PL4[12]	W	
1803	float	RD	_FFT_PL4[13]	W	
1805	float	RD	_FFT_PL4[14]	W	
1807	float	RD	_FFT_PL4[15]	W	
1809	float	RD	_FFT_PL4[16]	W	
1811	float	RD	_FFT_PL4[17]	W	
1813	float	RD	_FFT_PL4[18]	W	
1815	float	RD	_FFT_PL4[19]	W	
1817	float	RD	_FFT_PL4[20]	W	
1819	float	RD	_FFT_PL4[21]	W	
1821	float	RD	_FFT_PL4[22]	W	
1823	float	RD	_FFT_PL4[23]	W	
1825	float	RD	_FFT_PL4[24]	W	
1827	float	RD	_FFT_PL4[25]	W	
1829	float	RD	_FFT_PL4[26]	W	
1831	float	RD	_FFT_PL4[27]	W	
1833	float	RD	_FFT_PL4[28]	W	
1835	float	RD	_FFT_PL4[29]	W	
1837	float	RD	_FFT_PL4[30]	W	
1839	float	RD	_FFT_PL4[31]	W	
1841	float	RD	_FFT_PL4[32]	W	
1843	float	RD	_FFT_PL4[33]	W	
1845	float	RD	_FFT_PL4[34]	W	
1847	float	RD	_FFT_PL4[35]	W	
1849	float	RD	_FFT_PL4[36]	W	
1851	float	RD	_FFT_PL4[37]	W	
1853	float	RD	_FFT_PL4[38]	W	
1855	float	RD	_FFT_PL4[39]	W	
1857	float	RD	_FFT_PL4[40]	W	
1859	float	RD	_FFT_PL4[41]	W	
1861	float	RD	_FFT_PL4[42]	W	
1863	float	RD	_FFT_PL4[43]	W	
1865	float	RD	_FFT_PL4[44]	W	
1867	float	RD	_FFT_PL4[45]	W	

Address	Format	RD/WR	Designation	Unit	Note
1869	float	RD	_FFT_PL4[46]	W	
1871	float	RD	_FFT_PL4[47]	W	
1873	float	RD	_FFT_PL4[48]	W	
1875	float	RD	_FFT_PL4[49]	W	
1877	float	RD	_FFT_PL4[50]	W	
1879	float	RD	_FFT_PL4[51]	W	
1881	float	RD	_FFT_PL4[52]	W	
1883	float	RD	_FFT_PL4[53]	W	
1885	float	RD	_FFT_PL4[54]	W	
1887	float	RD	_FFT_PL4[55]	W	
1889	float	RD	_FFT_PL4[56]	W	
1891	float	RD	_FFT_PL4[57]	W	
1893	float	RD	_FFT_PL4[58]	W	
1895	float	RD	_FFT_PL4[59]	W	
1897	float	RD	_FFT_PL4[60]	W	
1899	float	RD	_FFT_PL4[61]	W	
1901	float	RD	_FFT_PL4[62]	W	
1903	float	RD	_FFT_QL1[0]	var	
1905	float	RD	_FFT_QL1[1]	var	
1907	float	RD	_FFT_QL1[2]	var	
1909	float	RD	_FFT_QL1[3]	var	
1911	float	RD	_FFT_QL1[4]	var	
1913	float	RD	_FFT_QL1[5]	var	
1915	float	RD	_FFT_QL1[6]	var	
1917	float	RD	_FFT_QL1[7]	var	
1919	float	RD	_FFT_QL1[8]	var	
1921	float	RD	_FFT_QL1[9]	var	
1923	float	RD	_FFT_QL1[10]	var	
1925	float	RD	_FFT_QL1[11]	var	
1927	float	RD	_FFT_QL1[12]	var	
1929	float	RD	_FFT_QL1[13]	var	
1931	float	RD	_FFT_QL1[14]	var	
1933	float	RD	_FFT_QL1[15]	var	
1935	float	RD	_FFT_QL1[16]	var	
1937	float	RD	_FFT_QL1[17]	var	
1939	float	RD	_FFT_QL1[18]	var	
1941	float	RD	_FFT_QL1[19]	var	
1943	float	RD	_FFT_QL1[20]	var	
1945	float	RD	_FFT_QL1[21]	var	
1947	float	RD	_FFT_QL1[22]	var	
1949	float	RD	_FFT_QL1[23]	var	
1951	float	RD	_FFT_QL1[24]	var	
1953	float	RD	_FFT_QL1[25]	var	
1955	float	RD	_FFT_QL1[26]	var	
1957	float	RD	_FFT_QL1[27]	var	
1959	float	RD	_FFT_QL1[28]	var	
1961	float	RD	_FFT_QL1[29]	var	
1963	float	RD	_FFT_QL1[30]	var	
1965	float	RD	_FFT_QL1[31]	var	
1967	float	RD	_FFT_QL1[32]	var	
1969	float	RD	_FFT_QL1[33]	var	
1971	float	RD	_FFT_QL1[34]	var	
1973	float	RD	_FFT_QL1[35]	var	
1975	float	RD	_FFT_QL1[36]	var	
1977	float	RD	_FFT_QL1[37]	var	
1979	float	RD	_FFT_QL1[38]	var	
1981	float	RD	_FFT_QL1[39]	var	
1983	float	RD	_FFT_QL1[40]	var	

Adresse	Format	RD/WR	Designation	Unit	Note
1985	float	RD	_FFT_QL1[41]	var	
1987	float	RD	_FFT_QL1[42]	var	
1989	float	RD	_FFT_QL1[43]	var	
1991	float	RD	_FFT_QL1[44]	var	
1993	float	RD	_FFT_QL1[45]	var	
1995	float	RD	_FFT_QL1[46]	var	
1997	float	RD	_FFT_QL1[47]	var	
1999	float	RD	_FFT_QL1[48]	var	
2001	float	RD	_FFT_QL1[49]	var	
2003	float	RD	_FFT_QL1[50]	var	
2005	float	RD	_FFT_QL1[51]	var	
2007	float	RD	_FFT_QL1[52]	var	
2009	float	RD	_FFT_QL1[53]	var	
2011	float	RD	_FFT_QL1[54]	var	
2013	float	RD	_FFT_QL1[55]	var	
2015	float	RD	_FFT_QL1[56]	var	
2017	float	RD	_FFT_QL1[57]	var	
2019	float	RD	_FFT_QL1[58]	var	
2021	float	RD	_FFT_QL1[59]	var	
2023	float	RD	_FFT_QL1[60]	var	
2025	float	RD	_FFT_QL1[61]	var	
2027	float	RD	_FFT_QL1[62]	var	
2029	float	RD	_FFT_QL2[0]	var	
2031	float	RD	_FFT_QL2[1]	var	
2033	float	RD	_FFT_QL2[2]	var	
2035	float	RD	_FFT_QL2[3]	var	
2037	float	RD	_FFT_QL2[4]	var	
2039	float	RD	_FFT_QL2[5]	var	
2041	float	RD	_FFT_QL2[6]	var	
2043	float	RD	_FFT_QL2[7]	var	
2045	float	RD	_FFT_QL2[8]	var	
2047	float	RD	_FFT_QL2[9]	var	
2049	float	RD	_FFT_QL2[10]	var	
2051	float	RD	_FFT_QL2[11]	var	
2053	float	RD	_FFT_QL2[12]	var	
2055	float	RD	_FFT_QL2[13]	var	
2057	float	RD	_FFT_QL2[14]	var	
2059	float	RD	_FFT_QL2[15]	var	
2061	float	RD	_FFT_QL2[16]	var	
2063	float	RD	_FFT_QL2[17]	var	
2065	float	RD	_FFT_QL2[18]	var	
2067	float	RD	_FFT_QL2[19]	var	
2069	float	RD	_FFT_QL2[20]	var	
2071	float	RD	_FFT_QL2[21]	var	
2073	float	RD	_FFT_QL2[22]	var	
2075	float	RD	_FFT_QL2[23]	var	
2077	float	RD	_FFT_QL2[24]	var	
2079	float	RD	_FFT_QL2[25]	var	
2081	float	RD	_FFT_QL2[26]	var	
2083	float	RD	_FFT_QL2[27]	var	
2085	float	RD	_FFT_QL2[28]	var	
2087	float	RD	_FFT_QL2[29]	var	
2089	float	RD	_FFT_QL2[30]	var	
2091	float	RD	_FFT_QL2[31]	var	
2093	float	RD	_FFT_QL2[32]	var	
2095	float	RD	_FFT_QL2[33]	var	
2097	float	RD	_FFT_QL2[34]	var	
2099	float	RD	_FFT_QL2[35]	var	

Address	Format	RD/WR	Designation	Unit	Note
2101	float	RD	_FFT_QL2[36]	var	
2103	float	RD	_FFT_QL2[37]	var	
2105	float	RD	_FFT_QL2[38]	var	
2107	float	RD	_FFT_QL2[39]	var	
2109	float	RD	_FFT_QL2[40]	var	
2111	float	RD	_FFT_QL2[41]	var	
2113	float	RD	_FFT_QL2[42]	var	
2115	float	RD	_FFT_QL2[43]	var	
2117	float	RD	_FFT_QL2[44]	var	
2119	float	RD	_FFT_QL2[45]	var	
2121	float	RD	_FFT_QL2[46]	var	
2123	float	RD	_FFT_QL2[47]	var	
2125	float	RD	_FFT_QL2[48]	var	
2127	float	RD	_FFT_QL2[49]	var	
2129	float	RD	_FFT_QL2[50]	var	
2131	float	RD	_FFT_QL2[51]	var	
2133	float	RD	_FFT_QL2[52]	var	
2135	float	RD	_FFT_QL2[53]	var	
2137	float	RD	_FFT_QL2[54]	var	
2139	float	RD	_FFT_QL2[55]	var	
2141	float	RD	_FFT_QL2[56]	var	
2143	float	RD	_FFT_QL2[57]	var	
2145	float	RD	_FFT_QL2[58]	var	
2147	float	RD	_FFT_QL2[59]	var	
2149	float	RD	_FFT_QL2[60]	var	
2151	float	RD	_FFT_QL2[61]	var	
2153	float	RD	_FFT_QL2[62]	var	
2155	float	RD	_FFT_QL3[0]	var	
2157	float	RD	_FFT_QL3[1]	var	
2159	float	RD	_FFT_QL3[2]	var	
2161	float	RD	_FFT_QL3[3]	var	
2163	float	RD	_FFT_QL3[4]	var	
2165	float	RD	_FFT_QL3[5]	var	
2167	float	RD	_FFT_QL3[6]	var	
2169	float	RD	_FFT_QL3[7]	var	
2171	float	RD	_FFT_QL3[8]	var	
2173	float	RD	_FFT_QL3[9]	var	
2175	float	RD	_FFT_QL3[10]	var	
2177	float	RD	_FFT_QL3[11]	var	
2179	float	RD	_FFT_QL3[12]	var	
2181	float	RD	_FFT_QL3[13]	var	
2183	float	RD	_FFT_QL3[14]	var	
2185	float	RD	_FFT_QL3[15]	var	
2187	float	RD	_FFT_QL3[16]	var	
2189	float	RD	_FFT_QL3[17]	var	
2191	float	RD	_FFT_QL3[18]	var	
2193	float	RD	_FFT_QL3[19]	var	
2195	float	RD	_FFT_QL3[20]	var	
2197	float	RD	_FFT_QL3[21]	var	
2199	float	RD	_FFT_QL3[22]	var	
2201	float	RD	_FFT_QL3[23]	var	
2203	float	RD	_FFT_QL3[24]	var	
2205	float	RD	_FFT_QL3[25]	var	
2207	float	RD	_FFT_QL3[26]	var	
2209	float	RD	_FFT_QL3[27]	var	
2211	float	RD	_FFT_QL3[28]	var	
2213	float	RD	_FFT_QL3[29]	var	
2215	float	RD	_FFT_QL3[30]	var	

Adresse	Format	RD/WR	Designation	Unit	Note
2217	float	RD	_FFT_QL3[31]	var	
2219	float	RD	_FFT_QL3[32]	var	
2221	float	RD	_FFT_QL3[33]	var	
2223	float	RD	_FFT_QL3[34]	var	
2225	float	RD	_FFT_QL3[35]	var	
2227	float	RD	_FFT_QL3[36]	var	
2229	float	RD	_FFT_QL3[37]	var	
2231	float	RD	_FFT_QL3[38]	var	
2233	float	RD	_FFT_QL3[39]	var	
2235	float	RD	_FFT_QL3[40]	var	
2237	float	RD	_FFT_QL3[41]	var	
2239	float	RD	_FFT_QL3[42]	var	
2241	float	RD	_FFT_QL3[43]	var	
2243	float	RD	_FFT_QL3[44]	var	
2245	float	RD	_FFT_QL3[45]	var	
2247	float	RD	_FFT_QL3[46]	var	
2249	float	RD	_FFT_QL3[47]	var	
2251	float	RD	_FFT_QL3[48]	var	
2253	float	RD	_FFT_QL3[49]	var	
2255	float	RD	_FFT_QL3[50]	var	
2257	float	RD	_FFT_QL3[51]	var	
2259	float	RD	_FFT_QL3[52]	var	
2261	float	RD	_FFT_QL3[53]	var	
2263	float	RD	_FFT_QL3[54]	var	
2265	float	RD	_FFT_QL3[55]	var	
2267	float	RD	_FFT_QL3[56]	var	
2269	float	RD	_FFT_QL3[57]	var	
2271	float	RD	_FFT_QL3[58]	var	
2273	float	RD	_FFT_QL3[59]	var	
2275	float	RD	_FFT_QL3[60]	var	
2277	float	RD	_FFT_QL3[61]	var	
2279	float	RD	_FFT_QL3[62]	var	
2281	float	RD	_FFT_QL4[0]	var	
2283	float	RD	_FFT_QL4[1]	var	
2285	float	RD	_FFT_QL4[2]	var	
2287	float	RD	_FFT_QL4[3]	var	
2289	float	RD	_FFT_QL4[4]	var	
2291	float	RD	_FFT_QL4[5]	var	
2293	float	RD	_FFT_QL4[6]	var	
2295	float	RD	_FFT_QL4[7]	var	
2297	float	RD	_FFT_QL4[8]	var	
2299	float	RD	_FFT_QL4[9]	var	
2301	float	RD	_FFT_QL4[10]	var	
2303	float	RD	_FFT_QL4[11]	var	
2305	float	RD	_FFT_QL4[12]	var	
2307	float	RD	_FFT_QL4[13]	var	
2309	float	RD	_FFT_QL4[14]	var	
2311	float	RD	_FFT_QL4[15]	var	
2313	float	RD	_FFT_QL4[16]	var	
2315	float	RD	_FFT_QL4[17]	var	
2317	float	RD	_FFT_QL4[18]	var	
2319	float	RD	_FFT_QL4[19]	var	
2321	float	RD	_FFT_QL4[20]	var	
2323	float	RD	_FFT_QL4[21]	var	
2325	float	RD	_FFT_QL4[22]	var	
2327	float	RD	_FFT_QL4[23]	var	
2329	float	RD	_FFT_QL4[24]	var	
2331	float	RD	_FFT_QL4[25]	var	

Address	Format	RD/WR	Designation	Unit	Note
2333	float	RD	_FFT_QL4[26]	var	
2335	float	RD	_FFT_QL4[27]	var	
2337	float	RD	_FFT_QL4[28]	var	
2339	float	RD	_FFT_QL4[29]	var	
2341	float	RD	_FFT_QL4[30]	var	
2343	float	RD	_FFT_QL4[31]	var	
2345	float	RD	_FFT_QL4[32]	var	
2347	float	RD	_FFT_QL4[33]	var	
2349	float	RD	_FFT_QL4[34]	var	
2351	float	RD	_FFT_QL4[35]	var	
2353	float	RD	_FFT_QL4[36]	var	
2355	float	RD	_FFT_QL4[37]	var	
2357	float	RD	_FFT_QL4[38]	var	
2359	float	RD	_FFT_QL4[39]	var	
2361	float	RD	_FFT_QL4[40]	var	
2363	float	RD	_FFT_QL4[41]	var	
2365	float	RD	_FFT_QL4[42]	var	
2367	float	RD	_FFT_QL4[43]	var	
2369	float	RD	_FFT_QL4[44]	var	
2371	float	RD	_FFT_QL4[45]	var	
2373	float	RD	_FFT_QL4[46]	var	
2375	float	RD	_FFT_QL4[47]	var	
2377	float	RD	_FFT_QL4[48]	var	
2379	float	RD	_FFT_QL4[49]	var	
2381	float	RD	_FFT_QL4[50]	var	
2383	float	RD	_FFT_QL4[51]	var	
2385	float	RD	_FFT_QL4[52]	var	
2387	float	RD	_FFT_QL4[53]	var	
2389	float	RD	_FFT_QL4[54]	var	
2391	float	RD	_FFT_QL4[55]	var	
2393	float	RD	_FFT_QL4[56]	var	
2395	float	RD	_FFT_QL4[57]	var	
2397	float	RD	_FFT_QL4[58]	var	
2399	float	RD	_FFT_QL4[59]	var	
2401	float	RD	_FFT_QL4[60]	var	
2403	float	RD	_FFT_QL4[61]	var	
2405	float	RD	_FFT_QL4[62]	var	
2407	float	RD	_FFT_ULLZ1[0]	V	
2409	float	RD	_FFT_ULLZ1[1]	V	
2411	float	RD	_FFT_ULLZ1[2]	V	
2413	float	RD	_FFT_ULLZ1[3]	V	
2415	float	RD	_FFT_ULLZ1[4]	V	
2417	float	RD	_FFT_ULLZ1[5]	V	
2419	float	RD	_FFT_ULLZ1[6]	V	
2421	float	RD	_FFT_ULLZ1[7]	V	
2423	float	RD	_FFT_ULLZ1[8]	V	
2425	float	RD	_FFT_ULLZ1[9]	V	
2427	float	RD	_FFT_ULLZ1[10]	V	
2429	float	RD	_FFT_ULLZ1[11]	V	
2431	float	RD	_FFT_ULLZ1[12]	V	
2433	float	RD	_FFT_ULLZ1[13]	V	
2435	float	RD	_FFT_ULLZ1[14]	V	
2437	float	RD	_FFT_ULLZ1[15]	V	
2439	float	RD	_FFT_ULLZ1[16]	V	
2441	float	RD	_FFT_ULLZ1[17]	V	
2443	float	RD	_FFT_ULLZ1[18]	V	
2445	float	RD	_FFT_ULLZ1[19]	V	
2447	float	RD	_FFT_ULLZ1[20]	V	

Adresse	Format	RD/WR	Designation	Unit	Note
2449	float	RD	_FFT_ULLZ1[21]	V	
2451	float	RD	_FFT_ULLZ1[22]	V	
2453	float	RD	_FFT_ULLZ1[23]	V	
2455	float	RD	_FFT_ULLZ1[24]	V	
2457	float	RD	_FFT_ULLZ1[25]	V	
2459	float	RD	_FFT_ULLZ1[26]	V	
2461	float	RD	_FFT_ULLZ1[27]	V	
2463	float	RD	_FFT_ULLZ1[28]	V	
2465	float	RD	_FFT_ULLZ1[29]	V	
2467	float	RD	_FFT_ULLZ1[30]	V	
2469	float	RD	_FFT_ULLZ1[31]	V	
2471	float	RD	_FFT_ULLZ1[32]	V	
2473	float	RD	_FFT_ULLZ1[33]	V	
2475	float	RD	_FFT_ULLZ1[34]	V	
2477	float	RD	_FFT_ULLZ1[35]	V	
2479	float	RD	_FFT_ULLZ1[36]	V	
2481	float	RD	_FFT_ULLZ1[37]	V	
2483	float	RD	_FFT_ULLZ1[38]	V	
2485	float	RD	_FFT_ULLZ1[39]	V	
2487	float	RD	_FFT_ULLZ1[40]	V	
2489	float	RD	_FFT_ULLZ1[41]	V	
2491	float	RD	_FFT_ULLZ1[42]	V	
2493	float	RD	_FFT_ULLZ1[43]	V	
2495	float	RD	_FFT_ULLZ1[44]	V	
2497	float	RD	_FFT_ULLZ1[45]	V	
2499	float	RD	_FFT_ULLZ1[46]	V	
2501	float	RD	_FFT_ULLZ1[47]	V	
2503	float	RD	_FFT_ULLZ1[48]	V	
2505	float	RD	_FFT_ULLZ1[49]	V	
2507	float	RD	_FFT_ULLZ1[50]	V	
2509	float	RD	_FFT_ULLZ1[51]	V	
2511	float	RD	_FFT_ULLZ1[52]	V	
2513	float	RD	_FFT_ULLZ1[53]	V	
2515	float	RD	_FFT_ULLZ1[54]	V	
2517	float	RD	_FFT_ULLZ1[55]	V	
2519	float	RD	_FFT_ULLZ1[56]	V	
2521	float	RD	_FFT_ULLZ1[57]	V	
2523	float	RD	_FFT_ULLZ1[58]	V	
2525	float	RD	_FFT_ULLZ1[59]	V	
2527	float	RD	_FFT_ULLZ1[60]	V	
2529	float	RD	_FFT_ULLZ1[61]	V	
2531	float	RD	_FFT_ULLZ1[62]	V	
2533	float	RD	_FFT_ULLZ2[0]	V	
2535	float	RD	_FFT_ULLZ2[1]	V	
2537	float	RD	_FFT_ULLZ2[2]	V	
2539	float	RD	_FFT_ULLZ2[3]	V	
2541	float	RD	_FFT_ULLZ2[4]	V	
2543	float	RD	_FFT_ULLZ2[5]	V	
2545	float	RD	_FFT_ULLZ2[6]	V	
2547	float	RD	_FFT_ULLZ2[7]	V	
2549	float	RD	_FFT_ULLZ2[8]	V	
2551	float	RD	_FFT_ULLZ2[9]	V	
2553	float	RD	_FFT_ULLZ2[10]	V	
2555	float	RD	_FFT_ULLZ2[11]	V	
2557	float	RD	_FFT_ULLZ2[12]	V	
2559	float	RD	_FFT_ULLZ2[13]	V	
2561	float	RD	_FFT_ULLZ2[14]	V	
2563	float	RD	_FFT_ULLZ2[15]	V	

Address	Format	RD/WR	Designation	Unit	Note
2565	float	RD	_FFT_ULLZ2[16]	V	
2567	float	RD	_FFT_ULLZ2[17]	V	
2569	float	RD	_FFT_ULLZ2[18]	V	
2571	float	RD	_FFT_ULLZ2[19]	V	
2573	float	RD	_FFT_ULLZ2[20]	V	
2575	float	RD	_FFT_ULLZ2[21]	V	
2577	float	RD	_FFT_ULLZ2[22]	V	
2579	float	RD	_FFT_ULLZ2[23]	V	
2581	float	RD	_FFT_ULLZ2[24]	V	
2583	float	RD	_FFT_ULLZ2[25]	V	
2585	float	RD	_FFT_ULLZ2[26]	V	
2587	float	RD	_FFT_ULLZ2[27]	V	
2589	float	RD	_FFT_ULLZ2[28]	V	
2591	float	RD	_FFT_ULLZ2[29]	V	
2593	float	RD	_FFT_ULLZ2[30]	V	
2595	float	RD	_FFT_ULLZ2[31]	V	
2597	float	RD	_FFT_ULLZ2[32]	V	
2599	float	RD	_FFT_ULLZ2[33]	V	
2601	float	RD	_FFT_ULLZ2[34]	V	
2603	float	RD	_FFT_ULLZ2[35]	V	
2605	float	RD	_FFT_ULLZ2[36]	V	
2607	float	RD	_FFT_ULLZ2[37]	V	
2609	float	RD	_FFT_ULLZ2[38]	V	
2611	float	RD	_FFT_ULLZ2[39]	V	
2613	float	RD	_FFT_ULLZ2[40]	V	
2615	float	RD	_FFT_ULLZ2[41]	V	
2617	float	RD	_FFT_ULLZ2[42]	V	
2619	float	RD	_FFT_ULLZ2[43]	V	
2621	float	RD	_FFT_ULLZ2[44]	V	
2623	float	RD	_FFT_ULLZ2[45]	V	
2625	float	RD	_FFT_ULLZ2[46]	V	
2627	float	RD	_FFT_ULLZ2[47]	V	
2629	float	RD	_FFT_ULLZ2[48]	V	
2631	float	RD	_FFT_ULLZ2[49]	V	
2633	float	RD	_FFT_ULLZ2[50]	V	
2635	float	RD	_FFT_ULLZ2[51]	V	
2637	float	RD	_FFT_ULLZ2[52]	V	
2639	float	RD	_FFT_ULLZ2[53]	V	
2641	float	RD	_FFT_ULLZ2[54]	V	
2643	float	RD	_FFT_ULLZ2[55]	V	
2645	float	RD	_FFT_ULLZ2[56]	V	
2647	float	RD	_FFT_ULLZ2[57]	V	
2649	float	RD	_FFT_ULLZ2[58]	V	
2651	float	RD	_FFT_ULLZ2[59]	V	
2653	float	RD	_FFT_ULLZ2[60]	V	
2655	float	RD	_FFT_ULLZ2[61]	V	
2657	float	RD	_FFT_ULLZ2[62]	V	
2659	float	RD	_FFT_ULLZ3[0]	V	
2661	float	RD	_FFT_ULLZ3[1]	V	
2663	float	RD	_FFT_ULLZ3[2]	V	
2665	float	RD	_FFT_ULLZ3[3]	V	
2667	float	RD	_FFT_ULLZ3[4]	V	
2669	float	RD	_FFT_ULLZ3[5]	V	
2671	float	RD	_FFT_ULLZ3[6]	V	
2673	float	RD	_FFT_ULLZ3[7]	V	
2675	float	RD	_FFT_ULLZ3[8]	V	
2677	float	RD	_FFT_ULLZ3[9]	V	
2679	float	RD	_FFT_ULLZ3[10]	V	

Adresse	Format	RD/WR	Designation	Unit	Note
2681	float	RD	_FFT_ULLZ3[11]	V	
2683	float	RD	_FFT_ULLZ3[12]	V	
2685	float	RD	_FFT_ULLZ3[13]	V	
2687	float	RD	_FFT_ULLZ3[14]	V	
2689	float	RD	_FFT_ULLZ3[15]	V	
2691	float	RD	_FFT_ULLZ3[16]	V	
2693	float	RD	_FFT_ULLZ3[17]	V	
2695	float	RD	_FFT_ULLZ3[18]	V	
2697	float	RD	_FFT_ULLZ3[19]	V	
2699	float	RD	_FFT_ULLZ3[20]	V	
2701	float	RD	_FFT_ULLZ3[21]	V	
2703	float	RD	_FFT_ULLZ3[22]	V	
2705	float	RD	_FFT_ULLZ3[23]	V	
2707	float	RD	_FFT_ULLZ3[24]	V	
2709	float	RD	_FFT_ULLZ3[25]	V	
2711	float	RD	_FFT_ULLZ3[26]	V	
2713	float	RD	_FFT_ULLZ3[27]	V	
2715	float	RD	_FFT_ULLZ3[28]	V	
2717	float	RD	_FFT_ULLZ3[29]	V	
2719	float	RD	_FFT_ULLZ3[30]	V	
2721	float	RD	_FFT_ULLZ3[31]	V	
2723	float	RD	_FFT_ULLZ3[32]	V	
2725	float	RD	_FFT_ULLZ3[33]	V	
2727	float	RD	_FFT_ULLZ3[34]	V	
2729	float	RD	_FFT_ULLZ3[35]	V	
2731	float	RD	_FFT_ULLZ3[36]	V	
2733	float	RD	_FFT_ULLZ3[37]	V	
2735	float	RD	_FFT_ULLZ3[38]	V	
2737	float	RD	_FFT_ULLZ3[39]	V	
2739	float	RD	_FFT_ULLZ3[40]	V	
2741	float	RD	_FFT_ULLZ3[41]	V	
2743	float	RD	_FFT_ULLZ3[42]	V	
2745	float	RD	_FFT_ULLZ3[43]	V	
2747	float	RD	_FFT_ULLZ3[44]	V	
2749	float	RD	_FFT_ULLZ3[45]	V	
2751	float	RD	_FFT_ULLZ3[46]	V	
2753	float	RD	_FFT_ULLZ3[47]	V	
2755	float	RD	_FFT_ULLZ3[48]	V	
2757	float	RD	_FFT_ULLZ3[49]	V	
2759	float	RD	_FFT_ULLZ3[50]	V	
2761	float	RD	_FFT_ULLZ3[51]	V	
2763	float	RD	_FFT_ULLZ3[52]	V	
2765	float	RD	_FFT_ULLZ3[53]	V	
2767	float	RD	_FFT_ULLZ3[54]	V	
2769	float	RD	_FFT_ULLZ3[55]	V	
2771	float	RD	_FFT_ULLZ3[56]	V	
2773	float	RD	_FFT_ULLZ3[57]	V	
2775	float	RD	_FFT_ULLZ3[58]	V	
2777	float	RD	_FFT_ULLZ3[59]	V	
2779	float	RD	_FFT_ULLZ3[60]	V	
2781	float	RD	_FFT_ULLZ3[61]	V	
2783	float	RD	_FFT_ULLZ3[62]	V	
2785	float	RD	_FFT_ULZ1[0]	V	
2787	float	RD	_FFT_ULZ1[1]	V	
2789	float	RD	_FFT_ULZ1[2]	V	
2791	float	RD	_FFT_ULZ1[3]	V	
2793	float	RD	_FFT_ULZ1[4]	V	
2795	float	RD	_FFT_ULZ1[5]	V	

Address	Format	RD/WR	Designation	Unit	Note
2797	float	RD	_FFT_ULZ1[6]	V	
2799	float	RD	_FFT_ULZ1[7]	V	
2801	float	RD	_FFT_ULZ1[8]	V	
2803	float	RD	_FFT_ULZ1[9]	V	
2805	float	RD	_FFT_ULZ1[10]	V	
2807	float	RD	_FFT_ULZ1[11]	V	
2809	float	RD	_FFT_ULZ1[12]	V	
2811	float	RD	_FFT_ULZ1[13]	V	
2813	float	RD	_FFT_ULZ1[14]	V	
2815	float	RD	_FFT_ULZ1[15]	V	
2817	float	RD	_FFT_ULZ1[16]	V	
2819	float	RD	_FFT_ULZ1[17]	V	
2821	float	RD	_FFT_ULZ1[18]	V	
2823	float	RD	_FFT_ULZ1[19]	V	
2825	float	RD	_FFT_ULZ1[20]	V	
2827	float	RD	_FFT_ULZ1[21]	V	
2829	float	RD	_FFT_ULZ1[22]	V	
2831	float	RD	_FFT_ULZ1[23]	V	
2833	float	RD	_FFT_ULZ1[24]	V	
2835	float	RD	_FFT_ULZ1[25]	V	
2837	float	RD	_FFT_ULZ1[26]	V	
2839	float	RD	_FFT_ULZ1[27]	V	
2841	float	RD	_FFT_ULZ1[28]	V	
2843	float	RD	_FFT_ULZ1[29]	V	
2845	float	RD	_FFT_ULZ1[30]	V	
2847	float	RD	_FFT_ULZ1[31]	V	
2849	float	RD	_FFT_ULZ1[32]	V	
2851	float	RD	_FFT_ULZ1[33]	V	
2853	float	RD	_FFT_ULZ1[34]	V	
2855	float	RD	_FFT_ULZ1[35]	V	
2857	float	RD	_FFT_ULZ1[36]	V	
2859	float	RD	_FFT_ULZ1[37]	V	
2861	float	RD	_FFT_ULZ1[38]	V	
2863	float	RD	_FFT_ULZ1[39]	V	
2865	float	RD	_FFT_ULZ1[40]	V	
2867	float	RD	_FFT_ULZ1[41]	V	
2869	float	RD	_FFT_ULZ1[42]	V	
2871	float	RD	_FFT_ULZ1[43]	V	
2873	float	RD	_FFT_ULZ1[44]	V	
2875	float	RD	_FFT_ULZ1[45]	V	
2877	float	RD	_FFT_ULZ1[46]	V	
2879	float	RD	_FFT_ULZ1[47]	V	
2881	float	RD	_FFT_ULZ1[48]	V	
2883	float	RD	_FFT_ULZ1[49]	V	
2885	float	RD	_FFT_ULZ1[50]	V	
2887	float	RD	_FFT_ULZ1[51]	V	
2889	float	RD	_FFT_ULZ1[52]	V	
2891	float	RD	_FFT_ULZ1[53]	V	
2893	float	RD	_FFT_ULZ1[54]	V	
2895	float	RD	_FFT_ULZ1[55]	V	
2897	float	RD	_FFT_ULZ1[56]	V	
2899	float	RD	_FFT_ULZ1[57]	V	
2901	float	RD	_FFT_ULZ1[58]	V	
2903	float	RD	_FFT_ULZ1[59]	V	
2905	float	RD	_FFT_ULZ1[60]	V	
2907	float	RD	_FFT_ULZ1[61]	V	
2909	float	RD	_FFT_ULZ1[62]	V	
2911	float	RD	_FFT_ULZ2[0]	V	

Adresse	Format	RD/WR	Designation	Unit	Note
2913	float	RD	_FFT_ULZ2[1]	V	
2915	float	RD	_FFT_ULZ2[2]	V	
2917	float	RD	_FFT_ULZ2[3]	V	
2919	float	RD	_FFT_ULZ2[4]	V	
2921	float	RD	_FFT_ULZ2[5]	V	
2923	float	RD	_FFT_ULZ2[6]	V	
2925	float	RD	_FFT_ULZ2[7]	V	
2927	float	RD	_FFT_ULZ2[8]	V	
2929	float	RD	_FFT_ULZ2[9]	V	
2931	float	RD	_FFT_ULZ2[10]	V	
2933	float	RD	_FFT_ULZ2[11]	V	
2935	float	RD	_FFT_ULZ2[12]	V	
2937	float	RD	_FFT_ULZ2[13]	V	
2939	float	RD	_FFT_ULZ2[14]	V	
2941	float	RD	_FFT_ULZ2[15]	V	
2943	float	RD	_FFT_ULZ2[16]	V	
2945	float	RD	_FFT_ULZ2[17]	V	
2947	float	RD	_FFT_ULZ2[18]	V	
2949	float	RD	_FFT_ULZ2[19]	V	
2951	float	RD	_FFT_ULZ2[20]	V	
2953	float	RD	_FFT_ULZ2[21]	V	
2955	float	RD	_FFT_ULZ2[22]	V	
2957	float	RD	_FFT_ULZ2[23]	V	
2959	float	RD	_FFT_ULZ2[24]	V	
2961	float	RD	_FFT_ULZ2[25]	V	
2963	float	RD	_FFT_ULZ2[26]	V	
2965	float	RD	_FFT_ULZ2[27]	V	
2967	float	RD	_FFT_ULZ2[28]	V	
2969	float	RD	_FFT_ULZ2[29]	V	
2971	float	RD	_FFT_ULZ2[30]	V	
2973	float	RD	_FFT_ULZ2[31]	V	
2975	float	RD	_FFT_ULZ2[32]	V	
2977	float	RD	_FFT_ULZ2[33]	V	
2979	float	RD	_FFT_ULZ2[34]	V	
2981	float	RD	_FFT_ULZ2[35]	V	
2983	float	RD	_FFT_ULZ2[36]	V	
2985	float	RD	_FFT_ULZ2[37]	V	
2987	float	RD	_FFT_ULZ2[38]	V	
2989	float	RD	_FFT_ULZ2[39]	V	
2991	float	RD	_FFT_ULZ2[40]	V	
2993	float	RD	_FFT_ULZ2[41]	V	
2995	float	RD	_FFT_ULZ2[42]	V	
2997	float	RD	_FFT_ULZ2[43]	V	
2999	float	RD	_FFT_ULZ2[44]	V	
3001	float	RD	_FFT_ULZ2[45]	V	
3003	float	RD	_FFT_ULZ2[46]	V	
3005	float	RD	_FFT_ULZ2[47]	V	
3007	float	RD	_FFT_ULZ2[48]	V	
3009	float	RD	_FFT_ULZ2[49]	V	
3011	float	RD	_FFT_ULZ2[50]	V	
3013	float	RD	_FFT_ULZ2[51]	V	
3015	float	RD	_FFT_ULZ2[52]	V	
3017	float	RD	_FFT_ULZ2[53]	V	
3019	float	RD	_FFT_ULZ2[54]	V	
3021	float	RD	_FFT_ULZ2[55]	V	
3023	float	RD	_FFT_ULZ2[56]	V	
3025	float	RD	_FFT_ULZ2[57]	V	
3027	float	RD	_FFT_ULZ2[58]	V	

Address	Format	RD/WR	Designation	Unit	Note
3029	float	RD	_FFT_ULZ2[59]	V	
3031	float	RD	_FFT_ULZ2[60]	V	
3033	float	RD	_FFT_ULZ2[61]	V	
3035	float	RD	_FFT_ULZ2[62]	V	
3037	float	RD	_FFT_ULZ3[0]	V	
3039	float	RD	_FFT_ULZ3[1]	V	
3041	float	RD	_FFT_ULZ3[2]	V	
3043	float	RD	_FFT_ULZ3[3]	V	
3045	float	RD	_FFT_ULZ3[4]	V	
3047	float	RD	_FFT_ULZ3[5]	V	
3049	float	RD	_FFT_ULZ3[6]	V	
3051	float	RD	_FFT_ULZ3[7]	V	
3053	float	RD	_FFT_ULZ3[8]	V	
3055	float	RD	_FFT_ULZ3[9]	V	
3057	float	RD	_FFT_ULZ3[10]	V	
3059	float	RD	_FFT_ULZ3[11]	V	
3061	float	RD	_FFT_ULZ3[12]	V	
3063	float	RD	_FFT_ULZ3[13]	V	
3065	float	RD	_FFT_ULZ3[14]	V	
3067	float	RD	_FFT_ULZ3[15]	V	
3069	float	RD	_FFT_ULZ3[16]	V	
3071	float	RD	_FFT_ULZ3[17]	V	
3073	float	RD	_FFT_ULZ3[18]	V	
3075	float	RD	_FFT_ULZ3[19]	V	
3077	float	RD	_FFT_ULZ3[20]	V	
3079	float	RD	_FFT_ULZ3[21]	V	
3081	float	RD	_FFT_ULZ3[22]	V	
3083	float	RD	_FFT_ULZ3[23]	V	
3085	float	RD	_FFT_ULZ3[24]	V	
3087	float	RD	_FFT_ULZ3[25]	V	
3089	float	RD	_FFT_ULZ3[26]	V	
3091	float	RD	_FFT_ULZ3[27]	V	
3093	float	RD	_FFT_ULZ3[28]	V	
3095	float	RD	_FFT_ULZ3[29]	V	
3097	float	RD	_FFT_ULZ3[30]	V	
3099	float	RD	_FFT_ULZ3[31]	V	
3101	float	RD	_FFT_ULZ3[32]	V	
3103	float	RD	_FFT_ULZ3[33]	V	
3105	float	RD	_FFT_ULZ3[34]	V	
3107	float	RD	_FFT_ULZ3[35]	V	
3109	float	RD	_FFT_ULZ3[36]	V	
3111	float	RD	_FFT_ULZ3[37]	V	
3113	float	RD	_FFT_ULZ3[38]	V	
3115	float	RD	_FFT_ULZ3[39]	V	
3117	float	RD	_FFT_ULZ3[40]	V	
3119	float	RD	_FFT_ULZ3[41]	V	
3121	float	RD	_FFT_ULZ3[42]	V	
3123	float	RD	_FFT_ULZ3[43]	V	
3125	float	RD	_FFT_ULZ3[44]	V	
3127	float	RD	_FFT_ULZ3[45]	V	
3129	float	RD	_FFT_ULZ3[46]	V	
3131	float	RD	_FFT_ULZ3[47]	V	
3133	float	RD	_FFT_ULZ3[48]	V	
3135	float	RD	_FFT_ULZ3[49]	V	
3137	float	RD	_FFT_ULZ3[50]	V	
3139	float	RD	_FFT_ULZ3[51]	V	
3141	float	RD	_FFT_ULZ3[52]	V	
3143	float	RD	_FFT_ULZ3[53]	V	

Adresse	Format	RD/WR	Designation	Unit	Note
3145	float	RD	_FFT_ULZ3[54]	V	
3147	float	RD	_FFT_ULZ3[55]	V	
3149	float	RD	_FFT_ULZ3[56]	V	
3151	float	RD	_FFT_ULZ3[57]	V	
3153	float	RD	_FFT_ULZ3[58]	V	
3155	float	RD	_FFT_ULZ3[59]	V	
3157	float	RD	_FFT_ULZ3[60]	V	
3159	float	RD	_FFT_ULZ3[61]	V	
3161	float	RD	_FFT_ULZ3[62]	V	
3163	float	RD	_FFT_ULZ4[0]	V	
3165	float	RD	_FFT_ULZ4[1]	V	
3167	float	RD	_FFT_ULZ4[2]	V	
3169	float	RD	_FFT_ULZ4[3]	V	
3171	float	RD	_FFT_ULZ4[4]	V	
3173	float	RD	_FFT_ULZ4[5]	V	
3175	float	RD	_FFT_ULZ4[6]	V	
3177	float	RD	_FFT_ULZ4[7]	V	
3179	float	RD	_FFT_ULZ4[8]	V	
3181	float	RD	_FFT_ULZ4[9]	V	
3183	float	RD	_FFT_ULZ4[10]	V	
3185	float	RD	_FFT_ULZ4[11]	V	
3187	float	RD	_FFT_ULZ4[12]	V	
3189	float	RD	_FFT_ULZ4[13]	V	
3191	float	RD	_FFT_ULZ4[14]	V	
3193	float	RD	_FFT_ULZ4[15]	V	
3195	float	RD	_FFT_ULZ4[16]	V	
3197	float	RD	_FFT_ULZ4[17]	V	
3199	float	RD	_FFT_ULZ4[18]	V	
3201	float	RD	_FFT_ULZ4[19]	V	
3203	float	RD	_FFT_ULZ4[20]	V	
3205	float	RD	_FFT_ULZ4[21]	V	
3207	float	RD	_FFT_ULZ4[22]	V	
3209	float	RD	_FFT_ULZ4[23]	V	
3211	float	RD	_FFT_ULZ4[24]	V	
3213	float	RD	_FFT_ULZ4[25]	V	
3215	float	RD	_FFT_ULZ4[26]	V	
3217	float	RD	_FFT_ULZ4[27]	V	
3219	float	RD	_FFT_ULZ4[28]	V	
3221	float	RD	_FFT_ULZ4[29]	V	
3223	float	RD	_FFT_ULZ4[30]	V	
3225	float	RD	_FFT_ULZ4[31]	V	
3227	float	RD	_FFT_ULZ4[32]	V	
3229	float	RD	_FFT_ULZ4[33]	V	
3231	float	RD	_FFT_ULZ4[34]	V	
3233	float	RD	_FFT_ULZ4[35]	V	
3235	float	RD	_FFT_ULZ4[36]	V	
3237	float	RD	_FFT_ULZ4[37]	V	
3239	float	RD	_FFT_ULZ4[38]	V	
3241	float	RD	_FFT_ULZ4[39]	V	
3243	float	RD	_FFT_ULZ4[40]	V	
3245	float	RD	_FFT_ULZ4[41]	V	
3247	float	RD	_FFT_ULZ4[42]	V	
3249	float	RD	_FFT_ULZ4[43]	V	
3251	float	RD	_FFT_ULZ4[44]	V	
3253	float	RD	_FFT_ULZ4[45]	V	
3255	float	RD	_FFT_ULZ4[46]	V	
3257	float	RD	_FFT_ULZ4[47]	V	
3259	float	RD	_FFT_ULZ4[48]	V	

Address	Format	RD/WR	Designation	Unit	Note
3261	float	RD	_FFT_ULZ4[49]	V	
3263	float	RD	_FFT_ULZ4[50]	V	
3265	float	RD	_FFT_ULZ4[51]	V	
3267	float	RD	_FFT_ULZ4[52]	V	
3269	float	RD	_FFT_ULZ4[53]	V	
3271	float	RD	_FFT_ULZ4[54]	V	
3273	float	RD	_FFT_ULZ4[55]	V	
3275	float	RD	_FFT_ULZ4[56]	V	
3277	float	RD	_FFT_ULZ4[57]	V	
3279	float	RD	_FFT_ULZ4[58]	V	
3281	float	RD	_FFT_ULZ4[59]	V	
3283	float	RD	_FFT_ULZ4[60]	V	
3285	float	RD	_FFT_ULZ4[61]	V	
3287	float	RD	_FFT_ULZ4[62]	V	
3289	float	RD	_FFT_ILZ1[0]	A	
3291	float	RD	_FFT_ILZ1[1]	A	
3293	float	RD	_FFT_ILZ1[2]	A	
3295	float	RD	_FFT_ILZ1[3]	A	
3297	float	RD	_FFT_ILZ1[4]	A	
3299	float	RD	_FFT_ILZ1[5]	A	
3301	float	RD	_FFT_ILZ1[6]	A	
3303	float	RD	_FFT_ILZ1[7]	A	
3305	float	RD	_FFT_ILZ1[8]	A	
3307	float	RD	_FFT_ILZ1[9]	A	
3309	float	RD	_FFT_ILZ1[10]	A	
3311	float	RD	_FFT_ILZ1[11]	A	
3313	float	RD	_FFT_ILZ1[12]	A	
3315	float	RD	_FFT_ILZ1[13]	A	
3317	float	RD	_FFT_ILZ1[14]	A	
3319	float	RD	_FFT_ILZ1[15]	A	
3321	float	RD	_FFT_ILZ1[16]	A	
3323	float	RD	_FFT_ILZ1[17]	A	
3325	float	RD	_FFT_ILZ1[18]	A	
3327	float	RD	_FFT_ILZ1[19]	A	
3329	float	RD	_FFT_ILZ1[20]	A	
3331	float	RD	_FFT_ILZ1[21]	A	
3333	float	RD	_FFT_ILZ1[22]	A	
3335	float	RD	_FFT_ILZ1[23]	A	
3337	float	RD	_FFT_ILZ1[24]	A	
3339	float	RD	_FFT_ILZ1[25]	A	
3341	float	RD	_FFT_ILZ1[26]	A	
3343	float	RD	_FFT_ILZ1[27]	A	
3345	float	RD	_FFT_ILZ1[28]	A	
3347	float	RD	_FFT_ILZ1[29]	A	
3349	float	RD	_FFT_ILZ1[30]	A	
3351	float	RD	_FFT_ILZ1[31]	A	
3353	float	RD	_FFT_ILZ1[32]	A	
3355	float	RD	_FFT_ILZ1[33]	A	
3357	float	RD	_FFT_ILZ1[34]	A	
3359	float	RD	_FFT_ILZ1[35]	A	
3361	float	RD	_FFT_ILZ1[36]	A	
3363	float	RD	_FFT_ILZ1[37]	A	
3365	float	RD	_FFT_ILZ1[38]	A	
3367	float	RD	_FFT_ILZ1[39]	A	
3369	float	RD	_FFT_ILZ1[40]	A	
3371	float	RD	_FFT_ILZ1[41]	A	
3373	float	RD	_FFT_ILZ1[42]	A	
3375	float	RD	_FFT_ILZ1[43]	A	

Adresse	Format	RD/WR	Designation	Unit	Note
3377	float	RD	_FFT_ILZ1[44]	A	
3379	float	RD	_FFT_ILZ1[45]	A	
3381	float	RD	_FFT_ILZ1[46]	A	
3383	float	RD	_FFT_ILZ1[47]	A	
3385	float	RD	_FFT_ILZ1[48]	A	
3387	float	RD	_FFT_ILZ1[49]	A	
3389	float	RD	_FFT_ILZ1[50]	A	
3391	float	RD	_FFT_ILZ1[51]	A	
3393	float	RD	_FFT_ILZ1[52]	A	
3395	float	RD	_FFT_ILZ1[53]	A	
3397	float	RD	_FFT_ILZ1[54]	A	
3399	float	RD	_FFT_ILZ1[55]	A	
3401	float	RD	_FFT_ILZ1[56]	A	
3403	float	RD	_FFT_ILZ1[57]	A	
3405	float	RD	_FFT_ILZ1[58]	A	
3407	float	RD	_FFT_ILZ1[59]	A	
3409	float	RD	_FFT_ILZ1[60]	A	
3411	float	RD	_FFT_ILZ1[61]	A	
3413	float	RD	_FFT_ILZ1[62]	A	
3415	float	RD	_FFT_ILZ2[0]	A	
3417	float	RD	_FFT_ILZ2[1]	A	
3419	float	RD	_FFT_ILZ2[2]	A	
3421	float	RD	_FFT_ILZ2[3]	A	
3423	float	RD	_FFT_ILZ2[4]	A	
3425	float	RD	_FFT_ILZ2[5]	A	
3427	float	RD	_FFT_ILZ2[6]	A	
3429	float	RD	_FFT_ILZ2[7]	A	
3431	float	RD	_FFT_ILZ2[8]	A	
3433	float	RD	_FFT_ILZ2[9]	A	
3435	float	RD	_FFT_ILZ2[10]	A	
3437	float	RD	_FFT_ILZ2[11]	A	
3439	float	RD	_FFT_ILZ2[12]	A	
3441	float	RD	_FFT_ILZ2[13]	A	
3443	float	RD	_FFT_ILZ2[14]	A	
3445	float	RD	_FFT_ILZ2[15]	A	
3447	float	RD	_FFT_ILZ2[16]	A	
3449	float	RD	_FFT_ILZ2[17]	A	
3451	float	RD	_FFT_ILZ2[18]	A	
3453	float	RD	_FFT_ILZ2[19]	A	
3455	float	RD	_FFT_ILZ2[20]	A	
3457	float	RD	_FFT_ILZ2[21]	A	
3459	float	RD	_FFT_ILZ2[22]	A	
3461	float	RD	_FFT_ILZ2[23]	A	
3463	float	RD	_FFT_ILZ2[24]	A	
3465	float	RD	_FFT_ILZ2[25]	A	
3467	float	RD	_FFT_ILZ2[26]	A	
3469	float	RD	_FFT_ILZ2[27]	A	
3471	float	RD	_FFT_ILZ2[28]	A	
3473	float	RD	_FFT_ILZ2[29]	A	
3475	float	RD	_FFT_ILZ2[30]	A	
3477	float	RD	_FFT_ILZ2[31]	A	
3479	float	RD	_FFT_ILZ2[32]	A	
3481	float	RD	_FFT_ILZ2[33]	A	
3483	float	RD	_FFT_ILZ2[34]	A	
3485	float	RD	_FFT_ILZ2[35]	A	
3487	float	RD	_FFT_ILZ2[36]	A	
3489	float	RD	_FFT_ILZ2[37]	A	
3491	float	RD	_FFT_ILZ2[38]	A	

Address	Format	RD/WR	Designation	Unit	Note
3493	float	RD	_FFT_ILZ2[39]	A	
3495	float	RD	_FFT_ILZ2[40]	A	
3497	float	RD	_FFT_ILZ2[41]	A	
3499	float	RD	_FFT_ILZ2[42]	A	
3501	float	RD	_FFT_ILZ2[43]	A	
3503	float	RD	_FFT_ILZ2[44]	A	
3505	float	RD	_FFT_ILZ2[45]	A	
3507	float	RD	_FFT_ILZ2[46]	A	
3509	float	RD	_FFT_ILZ2[47]	A	
3511	float	RD	_FFT_ILZ2[48]	A	
3513	float	RD	_FFT_ILZ2[49]	A	
3515	float	RD	_FFT_ILZ2[50]	A	
3517	float	RD	_FFT_ILZ2[51]	A	
3519	float	RD	_FFT_ILZ2[52]	A	
3521	float	RD	_FFT_ILZ2[53]	A	
3523	float	RD	_FFT_ILZ2[54]	A	
3525	float	RD	_FFT_ILZ2[55]	A	
3527	float	RD	_FFT_ILZ2[56]	A	
3529	float	RD	_FFT_ILZ2[57]	A	
3531	float	RD	_FFT_ILZ2[58]	A	
3533	float	RD	_FFT_ILZ2[59]	A	
3535	float	RD	_FFT_ILZ2[60]	A	
3537	float	RD	_FFT_ILZ2[61]	A	
3539	float	RD	_FFT_ILZ2[62]	A	
3541	float	RD	_FFT_ILZ3[0]	A	
3543	float	RD	_FFT_ILZ3[1]	A	
3545	float	RD	_FFT_ILZ3[2]	A	
3547	float	RD	_FFT_ILZ3[3]	A	
3549	float	RD	_FFT_ILZ3[4]	A	
3551	float	RD	_FFT_ILZ3[5]	A	
3553	float	RD	_FFT_ILZ3[6]	A	
3555	float	RD	_FFT_ILZ3[7]	A	
3557	float	RD	_FFT_ILZ3[8]	A	
3559	float	RD	_FFT_ILZ3[9]	A	
3561	float	RD	_FFT_ILZ3[10]	A	
3563	float	RD	_FFT_ILZ3[11]	A	
3565	float	RD	_FFT_ILZ3[12]	A	
3567	float	RD	_FFT_ILZ3[13]	A	
3569	float	RD	_FFT_ILZ3[14]	A	
3571	float	RD	_FFT_ILZ3[15]	A	
3573	float	RD	_FFT_ILZ3[16]	A	
3575	float	RD	_FFT_ILZ3[17]	A	
3577	float	RD	_FFT_ILZ3[18]	A	
3579	float	RD	_FFT_ILZ3[19]	A	
3581	float	RD	_FFT_ILZ3[20]	A	
3583	float	RD	_FFT_ILZ3[21]	A	
3585	float	RD	_FFT_ILZ3[22]	A	
3587	float	RD	_FFT_ILZ3[23]	A	
3589	float	RD	_FFT_ILZ3[24]	A	
3591	float	RD	_FFT_ILZ3[25]	A	
3593	float	RD	_FFT_ILZ3[26]	A	
3595	float	RD	_FFT_ILZ3[27]	A	
3597	float	RD	_FFT_ILZ3[28]	A	
3599	float	RD	_FFT_ILZ3[29]	A	
3601	float	RD	_FFT_ILZ3[30]	A	
3603	float	RD	_FFT_ILZ3[31]	A	
3605	float	RD	_FFT_ILZ3[32]	A	
3607	float	RD	_FFT_ILZ3[33]	A	

Adresse	Format	RD/WR	Designation	Unit	Note
3609	float	RD	_FFT_ILZ3[34]	A	
3611	float	RD	_FFT_ILZ3[35]	A	
3613	float	RD	_FFT_ILZ3[36]	A	
3615	float	RD	_FFT_ILZ3[37]	A	
3617	float	RD	_FFT_ILZ3[38]	A	
3619	float	RD	_FFT_ILZ3[39]	A	
3621	float	RD	_FFT_ILZ3[40]	A	
3623	float	RD	_FFT_ILZ3[41]	A	
3625	float	RD	_FFT_ILZ3[42]	A	
3627	float	RD	_FFT_ILZ3[43]	A	
3629	float	RD	_FFT_ILZ3[44]	A	
3631	float	RD	_FFT_ILZ3[45]	A	
3633	float	RD	_FFT_ILZ3[46]	A	
3635	float	RD	_FFT_ILZ3[47]	A	
3637	float	RD	_FFT_ILZ3[48]	A	
3639	float	RD	_FFT_ILZ3[49]	A	
3641	float	RD	_FFT_ILZ3[50]	A	
3643	float	RD	_FFT_ILZ3[51]	A	
3645	float	RD	_FFT_ILZ3[52]	A	
3647	float	RD	_FFT_ILZ3[53]	A	
3649	float	RD	_FFT_ILZ3[54]	A	
3651	float	RD	_FFT_ILZ3[55]	A	
3653	float	RD	_FFT_ILZ3[56]	A	
3655	float	RD	_FFT_ILZ3[57]	A	
3657	float	RD	_FFT_ILZ3[58]	A	
3659	float	RD	_FFT_ILZ3[59]	A	
3661	float	RD	_FFT_ILZ3[60]	A	
3663	float	RD	_FFT_ILZ3[61]	A	
3665	float	RD	_FFT_ILZ3[62]	A	
3667	float	RD	_FFT_ILZ4[0]	A	
3669	float	RD	_FFT_ILZ4[1]	A	
3671	float	RD	_FFT_ILZ4[2]	A	
3673	float	RD	_FFT_ILZ4[3]	A	
3675	float	RD	_FFT_ILZ4[4]	A	
3677	float	RD	_FFT_ILZ4[5]	A	
3679	float	RD	_FFT_ILZ4[6]	A	
3681	float	RD	_FFT_ILZ4[7]	A	
3683	float	RD	_FFT_ILZ4[8]	A	
3685	float	RD	_FFT_ILZ4[9]	A	
3687	float	RD	_FFT_ILZ4[10]	A	
3689	float	RD	_FFT_ILZ4[11]	A	
3691	float	RD	_FFT_ILZ4[12]	A	
3693	float	RD	_FFT_ILZ4[13]	A	
3695	float	RD	_FFT_ILZ4[14]	A	
3697	float	RD	_FFT_ILZ4[15]	A	
3699	float	RD	_FFT_ILZ4[16]	A	
3701	float	RD	_FFT_ILZ4[17]	A	
3703	float	RD	_FFT_ILZ4[18]	A	
3705	float	RD	_FFT_ILZ4[19]	A	
3707	float	RD	_FFT_ILZ4[20]	A	
3709	float	RD	_FFT_ILZ4[21]	A	
3711	float	RD	_FFT_ILZ4[22]	A	
3713	float	RD	_FFT_ILZ4[23]	A	
3715	float	RD	_FFT_ILZ4[24]	A	
3717	float	RD	_FFT_ILZ4[25]	A	
3719	float	RD	_FFT_ILZ4[26]	A	
3721	float	RD	_FFT_ILZ4[27]	A	
3723	float	RD	_FFT_ILZ4[28]	A	

Address	Format	RD/WR	Designation	Unit	Note
3725	float	RD	_FFT_ILZ4[29]	A	
3727	float	RD	_FFT_ILZ4[30]	A	
3729	float	RD	_FFT_ILZ4[31]	A	
3731	float	RD	_FFT_ILZ4[32]	A	
3733	float	RD	_FFT_ILZ4[33]	A	
3735	float	RD	_FFT_ILZ4[34]	A	
3737	float	RD	_FFT_ILZ4[35]	A	
3739	float	RD	_FFT_ILZ4[36]	A	
3741	float	RD	_FFT_ILZ4[37]	A	
3743	float	RD	_FFT_ILZ4[38]	A	
3745	float	RD	_FFT_ILZ4[39]	A	
3747	float	RD	_FFT_ILZ4[40]	A	
3749	float	RD	_FFT_ILZ4[41]	A	
3751	float	RD	_FFT_ILZ4[42]	A	
3753	float	RD	_FFT_ILZ4[43]	A	
3755	float	RD	_FFT_ILZ4[44]	A	
3757	float	RD	_FFT_ILZ4[45]	A	
3759	float	RD	_FFT_ILZ4[46]	A	
3761	float	RD	_FFT_ILZ4[47]	A	
3763	float	RD	_FFT_ILZ4[48]	A	
3765	float	RD	_FFT_ILZ4[49]	A	
3767	float	RD	_FFT_ILZ4[50]	A	
3769	float	RD	_FFT_ILZ4[51]	A	
3771	float	RD	_FFT_ILZ4[52]	A	
3773	float	RD	_FFT_ILZ4[53]	A	
3775	float	RD	_FFT_ILZ4[54]	A	
3777	float	RD	_FFT_ILZ4[55]	A	
3779	float	RD	_FFT_ILZ4[56]	A	
3781	float	RD	_FFT_ILZ4[57]	A	
3783	float	RD	_FFT_ILZ4[58]	A	
3785	float	RD	_FFT_ILZ4[59]	A	
3787	float	RD	_FFT_ILZ4[60]	A	
3789	float	RD	_FFT_ILZ4[61]	A	
3791	float	RD	_FFT_ILZ4[62]	A	