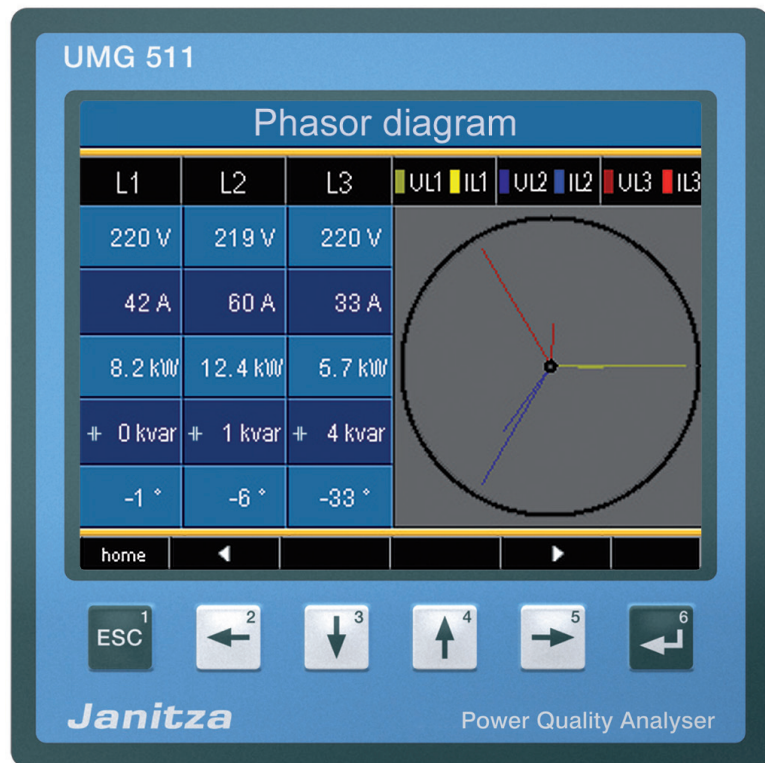


# Power Quality Analyser

## UMG 511

Modbus-address and  
Formulary



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## General

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# Modbus

## Modbus functions (master)

As a master, the UMG511 supports the following modbus functions;

### 01 Read Coil Status

Reads the ON/OFF status of discrete outputs (0X references, coils) in the slave. Broadcast is not supported.

### 02 Read Input Status

Reads the ON/OFF status of discrete inputs (0X references) in the slave. Broadcast is not supported.

### 03 Read Holding Registers

Reads the binary contents of holding registers (4X references) in the slave.

### 04 Read Input Registers

Reads the binary contents of input registers (3X references) in the slave.

### 05 Force Single Coil

Forces a single coil (0X references) to either ON or OFF. When broadcast, the function forces the same coil reference in all attached slaves.

### 06 Preset Single Register

Presets a value into a single holding register (4X reference). When broadcast, the function presets the same register reference in all attached slaves.

### 15 (0F Hex) Force Multiple Coils

Forces each coil (0X references) in a sequence of coils to either ON or OFF. When broadcast, the function forces the same coil reference in all attached slaves.

### 16 (10Hex) Preset Multiple Registers

Presets values into a sequence of holding registers (4X references). When broadcast, the function presets the same register references in all attached slaves.

### 23 (17Hex) Read/Write 4X Registers

Performs a combination of one read and one write operation in a single Modbus transaction. The function can write new contents to a group of 4XXXX registers, and then return the contents of another group of 4XXXX registers. Broadcast is not supported.

## Modbus Functions (Slave)

As a slave, the UMG511 supports the following modbus functions:

### 03 Read Holding Registers

Reads the binary contents of holding registers (4X references) in the slave.

### 04 Read Input Registers

Reads the binary contents of input registers (3X references) in the slave.

### 06 Preset Single Register

Presets a value into a single holding register (4X reference). When broadcast, the function presets the same register reference in all attached slaves.

### 16 (10Hex) Preset Multiple Registers

Presets values into a sequence of holding registers (4X references). When broadcast, the function presets the same register references in all attached slaves.

### 23 (17Hex) Read/Write 4X Registers

Performs a combination of one read and one write operation in a single Modbus transaction. The function can write new contents to a group of 4XXXX registers, and then return the contents of another group of 4XXXX registers. Broadcast is not supported.

## Transfer parameters

The UMG511 supports the following transfer parameters:

Baud rate	: 9.6kbps, 19.2kbps, 38.4kbps, 57.6kbps, 115.2 kbps and 921.6 kbps
Data bits	: 8
Parity	: none
Stop bits (UMG511)	: 2
Stop bits external	: 1 or 2

## Byte sequence

The data in the modbus address list can be called up in the

- Big-Endian (high-Byte before low-Byte) and in the
- Little-Endian (low-byte before high-byte)

format.

The addresses described in this address list supply the data in the „Big-Endian“ format.

If you require the data in the „Little-Endian“ format, you must add the value 32768 to the address.

## Update rate

The modbus register addresses are updated every 200ms.

## Number formats

Type	Size	Minimum	Maximum
char	8 bit	0	255
byte	8 bit	-128	127
short	16 bit	$-2^{15}$	$2^{15} - 1$
int	32 bit	$-2^{31}$	$2^{31} - 1$
uint	32 bit	0	$2^{32} - 1$
long64	64 bit	$-2^{63}$	$2^{63} - 1$
float	32 bit	IEEE 754	IEEE 754
double	64 bit	IEEE 754	IEEE 754



Energy values are processed in double format.

The energy values can be read about the Modbus addresses in float format.

## Symbols and definitions

N	Total number of sample points per period (For example, in a period of 20 ms)
k	Sample value or number of samples per period ( $0 \leq k < N$ )
p	Number or identification of the phase conductor ( $p = 1, 2$ oder $3$ )
$i_{pk}$	Sample value k of the current of the phase conductor p
$u_{pNk}$	Sample value k of the neutral voltage of the phase conductor p
$P_p$	Real power of the phase conductor p

# Explanations of the measured values

## Measured value

- A measured value (in the UMG508) is a effective value which is formed over a period (measuring window) of 200ms.
- A measuring window is 10 periods in the 50Hz network and 12 periods in the 60Hz network.
- A measuring window has a start time and an end time.
- The resolution between the start time and end time is approximately 2ns.
- The accuracy of the start time and end time depends on the accuracy of the internal clock.  
(Typically +/- 1 minute/month)
- In order to improve the accuracy of the internal clock, it is recommended that the clock in the device is compared with a time service and reset.

## Mean value of measured value

- For each measured value, a sliding mean value is calculated over the selected averaging time.
- The mean value is calculated every 200ms.
- You can take the possible averaging times from the table.

n	Mean time / seconds
0	5
1	10
2	15
3	30
4	60
5	300
6	480
7	600
8	900

## Max. value of measured value

- The *max. value of the measured value* is the largest measured value which has occurred since the last deletion.

## Min. value of measured value

- The *min. value of the measured value* is the lowest measured value which has occurred since the last deletion.

## Max. value of mean value

- The *max. value of the mean value* is the largest mean value which has occurred since the last deletion.

## Nominal current, voltage, frequency

- The limit values for events and transients are set by the nominal value in percentage.

## Nominal current $I_{\text{rated}}$

- The  $I_{\text{rated}}$  is the nominal current of the transformers and is required for calculation of the K-factor.

## Peak value negative

- Highest negative sampling value from the last 200ms measuring window..

## Peak value positive

- Highest positive sampling value from the last 200ms measuring window.

## Crest factor

- The crest factor describes the relation between the peak value and effective value of a periodic quantity. It serves as a characteristic value for general description of the curve form of a periodic quantity. The distortion factor is another example of a quantity for characterization of the difference from the pure sinusoidal form.

- Example

*A sinusoidal change voltage with an effective value of 230 V has a peak value of approx. 325 V.  
The crest factor is then  $325 \text{ V} / 230 \text{ V} = 1.414$  .*

Effective value of the current for phase conductor p

$$I_p = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} i_{pk}^2}$$

Effective value of neutral conductor current

$$I_N = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} (i_{1k} + i_{2k} + i_{3k})^2}$$

Effective voltage L-N

$$U_{pN} = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} u_{pNk}^2}$$

Effective voltage L-L

$$U_{pg} = \sqrt{\frac{1}{N} \cdot \sum_{k=0}^{N-1} (u_{gNk} - u_{pNk})^2}$$

Star connection voltage (vectorial)

$$U_{\text{Sternpunktspannung}} = U_{1ms} + U_{2ms} + U_{3ms}$$

Real power for phase conductor

$$P_p = \frac{1}{N} \cdot \sum_{k=0}^{N-1} (u_{pNk} \times i_{pk})$$

Apparent power for phase conductor

- Unsigned

$$S_p = U_{pN} \cdot I_p$$

Total apparent power (arithmetic)  $S_A$

- Unsigned

$$S_A = S_1 + S_2 + S_3$$

## Order number of harmonics

xxx[0] = mains frequency (50Hz/60Hz)  
 xxx[1] = 2nd harmonic (100Hz/120Hz)  
 xxx[2] = 3rd harmonic (150Hz/180Hz)  
 etc.

## THD

- THD (Total Harmonic Distortion) is the distortion factor and provides the relation of the harmonic parts of an oscillation to the mains frequency.

### Distortion factor for the voltage

- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)
- fund corresponds to n=1

$$THD_U = \frac{1}{|U_{fund}|} \sqrt{\sum_{n=2}^M |U_{n.Harm}|^2}$$

### Distortion factor for the current

- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)
- fund corresponds to n=1

$$THD_I = \frac{1}{|I_{fund}|} \sqrt{\sum_{n=2}^M |I_{n.Harm}|^2}$$

## ZHD

- THD for the interharmonics.
- Is calculated in the product series and UMG511 UMG605.

## Interharmonics

- Sinusoidal oscillations, which frequencies are not a multiple integer of the mains frequency.
- Is calculated in the product series and UMG511 UMG605.
- Calculation and measurement methods in accordance with the DIN EN 61000-4-30.
- The order number of interharmonics corresponds to the order number of the next smallest harmonic. For example, between the 3rd and 4th harmonic of the 3rd interharmonics.

## TDD (I)

- TDD Total demand distortion, harmonic current distortion in % of maximum demand load current
- IL = Maximum demand load current
- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)

$$TDD = \frac{1}{I_L} \sqrt{\sum_{n=2}^M I_n^2} \times 100\%$$

## Ripple control signal U (EN61000-4-30)

The ripple control signal U is a voltage (200ms measured value) which is measured at a carrier frequency specified by the user. Only frequencies beneath 3kHz are observed.

## Ripple control signal I

The ripple control signal I is a current (200ms measured value) which is measured at a carrier frequency specified by the user. Only frequencies beneath 3kHz are observed.



## Positive sequence-negative sequence-zero sequence

- The extent of a voltage or current imbalance in a three-phase system is identified using the positive sequence, negative sequence and zero sequence components.
- The balance of the rotation current system strived for in normal operation is disturbed by the unsymmetrical loads, errors and equipment.
- A three-phase system is called symmetric, when the three phase conductor voltages and currents are the same size and are displaced against each other by 120°. If one or both conditions are not fulfilled, the system is described as unsymmetrical. By calculating the symmetrical components consisting of the positive sequence, negative sequence and zero sequence, the simplified analysis of an imbalanced error is possible in a rotary current system..
- Imbalance is a feature of the network quality for the limits specified in international norms (EN 50160 for example).

### Positive sequence

$$U_{Mit} = \frac{1}{3} \left| U_{L1,fund} + U_{L2,fund} \cdot e^{j\frac{2\pi}{3}} + U_{L3,fund} \cdot e^{j\frac{4\pi}{3}} \right|$$

### Negative sequence

$$U_{Geg} = \frac{1}{3} \left| U_{L1,fund} + U_{L2,fund} \cdot e^{-j\frac{2\pi}{3}} + U_{L3,fund} \cdot e^{-j\frac{4\pi}{3}} \right|$$

### Zero sequence

$$U_{Nullsystem} = \frac{1}{3} \left| U_{L1,fund} + U_{L2,fund} + U_{L3,fund} \right|$$

A zero component can only occur if a sum current can flow back through the main conductor.

### Unsymmetrical voltage

$$Unsymmetrical\ voltage = \frac{U_{Negative\ sequence}}{U_{Positive\ sequence}} \cdot 100\%$$

### Unsymmetrical voltage (U0)

$$Unsymmetrical\ voltage\ (U0) = \frac{U_{Zero\ sequence}}{U_{Positive\ sequence}} \cdot 100\%$$

### Under difference U (EN61000-4-30)

$$U_{unter} = \frac{U_{din} - \sqrt{\frac{\sum_{i=1}^n U_{rms-unter,i}^2}{n}}}{U_{din}} [\%]$$

## Under difference I

$$I_{\text{unter}} = \frac{I_{\text{Nennstrom}} - \sqrt{\frac{\sum_{i=1}^n I_{\text{rms-unter},i}^2}{n}}}{I_{\text{Nennstrom}}} \left[ \frac{\%}{\%} \right]$$

## K-factor

- The K-factor describes the increase of the eddy current losses when loaded with harmonics. For a sinusoidal load on the transformer, the K-factor =1. The larger the K-factor, the heavier a transformer can be loaded with harmonics without overheating.

## Power Factor (vectorial) - Lambda

- The power factor is unsigned.

$$PF_A = \frac{|P|}{S_A}$$

## CosPhi - Fundamental Power Factor

- Only the mains frequency part is used for calculation of the cosphi.
- CosPhi sign:
  - = for the supply of real power
  - + = for obtaining real power

$$PF_1 = \cos(\varphi) = \frac{P_1}{S_1}$$

## CosPhi total

- CosPhi sign:
  - = for the supply of real power
  - + = for obtaining real power

$$\cos(\varphi)_{\text{Sum}_3} = \frac{P_{1\text{fund}} + P_{2\text{fund}} + P_{3\text{fund}}}{\sqrt{(P_{1\text{fund}} + P_{2\text{fund}} + P_{3\text{fund}})^2 + (Q_{1\text{fund}} + Q_{2\text{fund}} + Q_{3\text{fund}})^2}}$$

$$\cos(\varphi)_{\text{Sum}_4} = \frac{P_{1\text{fund}} + P_{2\text{fund}} + P_{3\text{fund}} + P_{4\text{fund}}}{\sqrt{(P_{1\text{fund}} + P_{2\text{fund}} + P_{3\text{fund}} + P_{4\text{fund}})^2 + (Q_{1\text{fund}} + Q_{2\text{fund}} + Q_{3\text{fund}} + Q_{4\text{fund}})^2}}$$

## Phase Angle Phi

- The phase angle between current and voltage of the external conductor p is calculated according to DIN EN 61557-12 and displayed.
- The sign of the phase angle corresponding to the sign of the reactive power.

## Mains frequency power factor

The mains frequency power factor is the power factor of the mains frequency and is calculated using the fourier analysis (FFT). The voltage and current must not be sinusoidal. All in the device calculated reactive power are resulting of fundamental reactive power.

### Power factor sign

- Sign  $Q = +1$  for  $\varphi_p$  in the range  $0^\circ \dots 180^\circ$  (inductive)
- Sign  $Q = -1$  for  $\varphi_p$  in the range  $180^\circ \dots 360^\circ$  (capacitive)

$$\text{Vorzeichen } Q(\varphi_p) = +1 \text{ falls } \varphi_p \in [0^\circ - 180^\circ]$$

$$\text{Vorzeichen } Q(\varphi_p) = -1 \text{ falls } \varphi_p \in [180^\circ - 360^\circ]$$

### Reactive power for phase conductor p

- Reactive power of the mains frequency.

$$Q_{fundp} = \text{Vorzeichen } Q(\varphi_p) \cdot \sqrt{S_{fundp}^2 - P_{fundp}^2}$$

### Total reactive power

- Reactive power of the mains frequency.

$$Q_V = Q_1 + Q_2 + Q_3$$

### Distortion power factor

- The distortion power factor is the power factor of all mains frequencies and is calculated using the fourier analysis (FFT).
- The apparent power „S” contains all fundamental harmonics and all harmonic rates up to the M-th harmonic.
- The effective power „P” contains all fundamental harmonics and all harmonic rates up to the M-th harmonic.
- M = 40 (UMG604, UMG508, UMG96RM)
- M = 50 (UMG605, UMG511)

$$D = \sqrt{S^2 - P^2 - Q_{fund}^2}$$

### Reactive energy per phase

$$E_{r_{L1}} = \int Q_{L1}(t) \cdot \Delta t$$

### Reactive energy per phase, inductive

$$E_{r(ind)_{L1}} = \int Q_{L1}(t) \cdot \Delta t \quad \text{für } Q_{L1}(t) > 0$$

### Reactive energy per phase, capacitive

$$E_{r(cap)_{L1}} = \int Q_{L1}(t) \cdot \Delta t \quad \text{für } Q_{L1}(t) < 0$$

### Reactive energy, sum L1-L3

$$E_{r_{L1,L2,L3}} = \int (Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) \cdot \Delta t$$

### Reactive energy, sum L1-L3, inductive

$$E_{r(ind)_{L1,L2,L3}} = \int (Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) \cdot \Delta t$$

für  $(Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) > 0$

### Reactive energy, sum L1-L3, capacitive

$$E_{r(cap)_{L1,L2,L3}} = \int (Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) \cdot \Delta t$$

für  $(Q_{L1}(t) + Q_{L2}(t) + Q_{L3}(t)) < 0$

# Address list

## Frequently required readings

Address	Format	RD/WR	Designation	Unit	Note
19000	float	RD	_G_ULN[0]	V	Voltage L1-N
19002	float	RD	_G_ULN[1]	V	Voltage L2-N
19004	float	RD	_G_ULN[2]	V	Voltage L3-N
19006	float	RD	_G_ULL[0]	V	Voltage L1-L2
19008	float	RD	_G_ULL[1]	V	Voltage L2-L3
19010	float	RD	_G_ULL[2]	V	Voltage L3-L1
19012	float	RD	_G_ILN[0]	A	Apparent current, L1-N
19014	float	RD	_G_ILN[1]	A	Apparent current, L2-N
19016	float	RD	_G_ILN[2]	A	Apparent current, L3-N
19018	float	RD	_G_I_SUM3	A	Vector sum; IN=I1+I2+I3
19020	float	RD	_G_PLN[0]	W	Real power L1-N
19022	float	RD	_G_PLN[1]	W	Real power L2-N
19024	float	RD	_G_PLN[2]	W	Real power L3-N
19026	float	RD	_G_P_SUM3	W	Psum3=P1+P2+P3
19028	float	RD	_G_SLN[0]	VA	Apparent power L1-N
19030	float	RD	_G_SLN[1]	VA	Apparent power L2-N
19032	float	RD	_G_SLN[2]	VA	Apparent power L3-N
19034	float	RD	_G_S_SUM3	VA	Sum; Ssum3=S1+S2+S3
19036	float	RD	_G_QLN[0]	var	Reactive power L1 (fundamental comp.)
19038	float	RD	_G_QLN[1]	var	Reactive power L2 (fundamental comp.)
19040	float	RD	_G_QLN[2]	var	Reactive power L3 (fundamental comp.)
19042	float	RD	_G_Q_SUM3	var	Qsum3=Q1+Q2+Q3 (fundamental comp.)
19044	float	RD	_G_COS_PHI[0]	-	CosPhi; UL1 IL1 (fundamental comp.)
19046	float	RD	_G_COS_PHI[1]	-	CosPhi; UL2 IL2 (fundamental comp.)
19048	float	RD	_G_COS_PHI[2]	-	CosPhi; UL3 IL3 (fundamental comp.)
19050	float	RD	_G_FREQ	Hz	Measured frequency
19052	float	RD	_G_PHASE_SEQ	-	Rotation field; 1=right, 0=none, -1=left
19054	float	RD	_G_WH[0]	Wh	Real energy L1
19056	float	RD	_G_WH[1]	Wh	Real energy L2
19058	float	RD	_G_WH[2]	Wh	Real energy L3
19060	float	RD	_G_WH_SUML13	Wh	Real energy L1..L3
19062	float	RD	_G_WH_V[0]	Wh	Real energy L1, consumed
19064	float	RD	_G_WH_V[1]	Wh	Real energy L2, consumed
19066	float	RD	_G_WH_V[2]	Wh	Real energy L3, consumed
19068	float	RD	_G_WH_V_HT_SUML13	Wh	Real energy L1..L3, consumed, rate 1
19070	float	RD	_G_WH_Z[0]	Wh	Real energy L1, delivered
19072	float	RD	_G_WH_Z[1]	Wh	Real energy L2, delivered
19074	float	RD	_G_WH_Z[2]	Wh	Real energy L3, delivered
19076	float	RD	_G_WH_Z_SUML13	Wh	Real energy L1..L3, delivered
19078	float	RD	_G_WH_S[0]	VAh	Apparent energy L1
19080	float	RD	_G_WH_S[1]	VAh	Apparent energy L2
19082	float	RD	_G_WH_S[2]	VAh	Apparent energy L3
19084	float	RD	_G_WH_S_SUML13	VAh	Apparent energy L1..L3
19086	float	RD	_G_QH[0]	varh	Reactive energy L1 (fundamental comp.)
19088	float	RD	_G_QH[1]	varh	Reactive energy L2 (fundamental comp.)
19090	float	RD	_G_QH[2]	varh	Reactive energy L3 (fundamental comp.)
19092	float	RD	_G_QH_SUML13	varh	Reactive energy L1..L3 (fundamental comp.)
19094	float	RD	_G_IQH[0]	varh	Reactive energy, inductive, L1 (fundamental comp.)
19096	float	RD	_G_IQH[1]	varh	Reactive energy, inductive, L2 (fundamental comp.)
19098	float	RD	_G_IQH[2]	varh	Reactive energy, inductive, L3 (fundamental comp.)
19100	float	RD	_G_IQH_SUML13	varh	Reactive energy L1..L3, ind. (fundamental comp.)
19102	float	RD	_G_CQH[0]	varh	Reactive energy, capacitive, L1 (fundamental comp.)
19104	float	RD	_G_CQH[1]	varh	Reactive energy, capacitive, L2 (fundamental comp.)
19106	float	RD	_G_CQH[2]	varh	Reactive energy, capacitive, L3 (fundamental comp.)
19108	float	RD	_G_CQH_SUML13	varh	Reactive energy L1..L3, cap. (fundamental comp.)
19110	float	RD	_G_THD_ULN[0]	%	Harmonic, THD,U L1-N
19112	float	RD	_G_THD_ULN[1]	%	Harmonic, THD,U L2-N
19114	float	RD	_G_THD_ULN[2]	%	Harmonic, THD,U L3-N
19116	float	RD	_G_THD_ILN[0]	%	Harmonic, THD,I L1
19118	float	RD	_G_THD_ILN[1]	%	Harmonic, THD,I L2
19120	float	RD	_G_THD_ILN[2]	%	Harmonic, THD,I L3

Address	Format	RD/WR	Designation	Unit	Note
<b>Date and time</b>					
0	long64		_REALTIME	2ns	time (UTC)
4	int		_SYSTIME	sec	time (UTC)
6	short		_DAY		Day (1 .. 31)
7	short		_MONTH		Month (0=Jan, .. 11=Dec)
8	short		_YEAR		Year
9	short		_HOUR	h	Hour (1 .. 24)
10	short		_MIN	min	Minute (1 .. 59)
11	short		_SEC	s	Second (1 .. 59)
12	short		_WEEKDAY		Weekday, (0=Sun, .. 6=Sat)

Address	Format	Designation	Unit	Note
<b>Messwerte (200ms Messfenster)</b>				
3793	float	_THD_ULL[0]	%	Harmonics, THD; U, L1-L2
3795	float	_THD_ULL[1]	%	Harmonics, THD; U, L2-L3
3797	float	_THD_ULL[2]	%	Harmonics, THD; U, L3-L1
3799	float	_ZHD_ULL[0]	%	Interharmonics, U L1-L2
3801	float	_ZHD_ULL[1]	%	Interharmonics, U L2-L3
3803	float	_ZHD_ULL[2]	%	Interharmonics, U L3-L1
3805	float	_THD_ULN[0]	%	Harmonics, THD; U, L1-N
3807	float	_THD_ULN[1]	%	Harmonics, THD; U, L2-N
3809	float	_THD_ULN[2]	%	Harmonics, THD; U, L3-N
3811	float	_THD_ULN[3]	%	Harmonics, THD; U, L4-N
3813	float	_THD_IL[0]	%	Harmonics, THD; I, L1
3815	float	_THD_IL[1]	%	Harmonics, THD; I, L2
3817	float	_THD_IL[2]	%	Harmonics, THD; I, L3
3819	float	_THD_IL[3]	%	Harmonics, THD; I, L4
3821	float	_ZHD_ULN[0]	%	Interharmonics, ZHD; U, L1
3823	float	_ZHD_ULN[1]	%	Interharmonics, ZHD; U, L1
3825	float	_ZHD_ULN[2]	%	Interharmonics, ZHD; U, L1
3827	float	_ZHD_ULN[3]	%	Interharmonics, ZHD; U, L1
3829	float	_ZHD_ILN[0]	%	Interharmonics, ZHD; I, L1
3831	float	_ZHD_ILN[1]	%	Interharmonics, ZHD; I, L2
3833	float	_ZHD_ILN[2]	%	Interharmonics, ZHD; I, L3
3835	float	_ZHD_ILN[3]	%	Interharmonics, ZHD; I, L4
3837	float	_KFACT[0]		K-Factor; L1
3839	float	_KFACT[1]		K-Factor; L2
3841	float	_KFACT[2]		K-Factor; L3
3843	float	_KFACT[3]		K-Factor; L4
3845	float	_ULN[0]	V	Voltage L1-N
3847	float	_ULN[1]	V	Voltage L2-N
3849	float	_ULN[2]	V	Voltage L3-N
3851	float	_ULN[3]	V	Voltage L4-N
3853	float	_ILN[0]	A	Apparent current, L1
3855	float	_ILN[1]	A	Apparent current, L2
3857	float	_ILN[2]	A	Apparent current, L3
3859	float	_ILN[3]	A	Apparent current, L4
3861	float	_PLN[0]	W	Real power, L1
3863	float	_PLN[1]	W	Real power, L2
3865	float	_PLN[2]	W	Real power, L3
3867	float	_PLN[3]	W	Real power, L4
3869	float	_QLN[0]	var	Mains frequency power factor, L1
3871	float	_QLN[1]	var	Mains frequency power factor, L2
3873	float	_QLN[2]	var	Mains frequency power factor, L3
3875	float	_QLN[3]	var	Mains frequency power factor; L4
3877	float	_SLN[0]	VA	Apparent power, L1
3879	float	_SLN[1]	VA	Apparent power, L2
3881	float	_SLN[2]	VA	Apparent power, L3
3883	float	_SLN[3]	VA	Apparent power, L4
3885	float	_DLN[0]	VA	Distortion power factor; L1
3887	float	_DLN[1]	VA	Distortion power factor; L2
3889	float	_DLN[2]	VA	Distortion power factor; L3
3891	float	_DLN[3]	VA	Distortion power factor; L4
3893	float	_PFLN[0]		Power Factor; L1
3895	float	_PFLN[1]		Power Factor; L2
3897	float	_PFLN[2]		Power Factor; L3
3899	float	_PFLN[3]		Power Factor; L4
3901	float	_ULL[0]	V	Phase conductor voltage; L1-L2
3903	float	_ULL[1]	V	Phase conductor voltage; L2-L3
3905	float	_ULL[2]	V	Phase conductor voltage; L3-L1
3907	float	_ULL_RE[0]	V	Phase conductor voltage real part; L1-L2
3909	float	_ULL_RE[1]	V	Phase conductor voltage real part; L2-L3

Address	Format	Designation	Unit	Note
3911	float	_ULL_RE[2]	V	Phase conductor voltage real part; L3-L1
3913	float	_ULL_IM[0]	V	Phase conductor voltage imaginary part;
L1-L2				
3915	float	_ULL_IM[1]	V	Phase conductor voltage imaginary part;
L2-L3				
3917	float	_ULL_IM[2]	V	Phase conductor voltage imaginary part;
L3-L1				
3919	float	_I_SUM3	A	Vector sum; $IN = I1 + I2 + I3$
3921	float	_I_SUM	A	Vector sum; $I1 + I2 + I3 + I4$
3923	float	_S_SUM3	VA	Sum; $S = S1 + S2 + S3$
3925	float	_P_SUM3	W	Sum; $P = P1 + P2 + P3$
3927	float	_Q_SUM3	var	Mains frequency reactive power Sum; $Q = Q1 + Q2 + Q3$
3929	float	_COS_SUM3		CosPhi of mains frequency Calculated from Psum3 and Qsum3
3931	float	_S_SUM	VA	Sum; $S = S1 + S2 + S3 + S4$
3933	float	_P_SUM	W	Sum; $P = P1 + P2 + P3 + P4$
3935	float	_Q_SUM	var	Mains frequency reactive power Sum; $Q = Q1 + Q2 + Q3 + Q4$
3937	float	_COS_SUM		CosPhi of mains frequency Calculated from Psum and Qsum
3939	float	_ULN_RE[0]	V	Voltage, real part, L1-N
3941	float	_ULN_RE[1]	V	Voltage, real part, L2-N
3943	float	_ULN_RE[2]	V	Voltage, real part, L3-N
3945	float	_ULN_RE[3]	V	Voltage, real part, L4-N
3947	float	_ULN_IM[0]	V	Voltage, imaginary part, L1-N
3949	float	_ULN_IM[1]	V	Voltage, imaginary part, L2-N
3951	float	_ULN_IM[2]	V	Voltage, imaginary part, L3-N
3953	float	_ULN_IM[3]	V	Voltage, imaginary part, L4-N
3955	float	_IL_RE[0]	A	Current, real part, L1
3957	float	_IL_RE[1]	A	Current, real part, L2
3959	float	_IL_RE[2]	A	Current, real part, L3
3961	float	_IL_RE[3]	A	Current, real part, L4
3963	float	_IL_IM[0]	A	Current, imaginary part, L1
3965	float	_IL_IM[1]	A	Current, imaginary part, L2
3967	float	_IL_IM[2]	A	Current, imaginary part, L3
3969	float	_IL_IM[3]	A	Current, imaginary part, L4
3971	float	_PHASE[0]	°	Phase; UL1 IL1
3973	float	_PHASE[1]	°	Phase; UL2 IL2
3975	float	_PHASE[2]	°	Phase; UL3 IL3
3977	float	_PHASE[3]	°	Phase; UL4 IL4
3979	float	_COS_PHI[0]		Fund. power factor, CosPhi; UL1 IL1
3981	float	_COS_PHI[1]		Fund. power factor, CosPhi; UL2 IL2
3983	float	_COS_PHI[2]		Fund. power factor, CosPhi; UL3 IL3
3985	float	_COS_PHI[3]		Fund. power factor, CosPhi; UL4 IL4
3987	float	_IND_CAP[0]		Sign; Q L1, +1 = ind., -1 = cap.
3989	float	_IND_CAP[1]		Sign; Q L2, +1 = ind., -1 = cap.
3991	float	_IND_CAP[2]		Sign; Q L3, +1 = ind., -1 = cap.
3993	float	_IND_CAP[3]		Sign; Q L4, +1 = ind., -1 = cap.
3995	float	_FREQ	Hz	Measured frequency
3997	float	_NORM_FREQ	Hz	Nominal frequency
3999	float	_UN	V	Zero sequence, voltage
4001	float	_UM	V	Positive sequence, voltage
4003	float	_UG	V	Negative sequence, voltage
4005	float	_U_SYM	%	Unsymmetrical; voltage
4007	float	_I_SYM	%	Unsymmetrical; current
4009	float	_PHASE_SEQ		Rotation field; 1=right, 0=none, -1=left
4011	float	_IN	A	Zero sequence, current
4013	float	_IM	A	Positive sequence, current
4015	float	_IG	A	Negative sequence, current

Address	Format	Designation	Unit	Note
4017	-	-	-	
4019	-	-	-	
4021	float	_IL_CF[0]	-	Crest factor, I L1
4023	float	_IL_CF[1]	-	Crest factor, I L1
4025	float	_IL_CF[2]	-	Crest factor, I L2
4027	float	_IL_CF[3]	-	Crest factor, I L3
4029	float	_ULN_CF[0]	-	Crest factor, U L1-N
4031	float	_ULN_CF[1]	-	Crest factor, U L2-N
4033	float	_ULN_CF[2]	-	Crest factor, U L3-N
4035	float	_ULN_CF[3]	-	Crest factor, U L4-N
4037	float	_ULL_CF[0]	-	Crest factor, U L1-L2
4039	float	_ULL_CF[1]	-	Crest factor, U L2-L3
4041	float	_ULL_CF[2]	-	Crest factor, U L3-L1
4043	float	_IL_NEG_PEAK[0]	A	Peak value negative, I L1
4045	float	_IL_NEG_PEAK[1]	A	Peak value negative, I L2
4047	float	_IL_NEG_PEAK[2]	A	Peak value negative, I L3
4049	float	_IL_NEG_PEAK[3]	A	Peak value negative, I L4
4051	float	_ULN_NEG_PEAK[0]	V	Peak value negative, U L1-N
4053	float	_ULN_NEG_PEAK[1]	V	Peak value negative, U L2-N
4055	float	_ULN_NEG_PEAK[2]	V	Peak value negative, U L3-N
4057	float	_ULN_NEG_PEAK[3]	V	Peak value negative, U L4-N
4059	float	_IL_POS_PEAK[0]	A	Peak value positive, I L1
4061	float	_IL_POS_PEAK[1]	A	Peak value positive, I L2
4063	float	_IL_POS_PEAK[2]	A	Peak value positive, I L3
4065	float	_IL_POS_PEAK[3]	A	Peak value positive, I L4
4067	float	_ULN_POS_PEAK[0]	V	Peak value positive, U L1-N
4069	float	_ULN_POS_PEAK[1]	V	Peak value positive, U L2-N
4071	float	_ULN_POS_PEAK[2]	V	Peak value positive, U L3-N
4073	float	_ULN_POS_PEAK[3]	V	Peak value positive, U L4-N
4075	float	_IL_PEAK_PEAK[0]	A	Peak-peak value, I L1
4077	float	_IL_PEAK_PEAK[1]	A	Peak-peak value, I L2
4079	float	_IL_PEAK_PEAK[2]	A	Peak-peak value, I L3
4081	float	_IL_PEAK_PEAK[3]	A	Peak-peak value, I L4
4083	float	_ULN_PEAK_PEAK[0]	V	Peak-peak value, U L1-N
4085	float	_ULN_PEAK_PEAK[1]	V	Peak-peak value, U L2-N
4087	float	_ULN_PEAK_PEAK[2]	V	Peak-peak value, U L3-N
4089	float	_ULN_PEAK_PEAK[3]	V	Peak-peak value, U L4-N
4091	float	_IL_UNDER[0]	%	Under difference, I L1
4093	float	_IL_UNDER[1]	%	Under difference, I L2
4095	float	_IL_UNDER[2]	%	Under difference, I L3
4097	float	_IL_UNDER[3]	%	Under difference, I L4
4099	float	_ULN_UNDER[0]	%	Under difference, U L1 (61000-4-30)
4101	float	_ULN_UNDER[1]	%	Under difference, U L2 (61000-4-30)
4103	float	_ULN_UNDER[2]	%	Under difference, U L3 (61000-4-30)
4105	float	_ULN_UNDER[3]	%	Under difference, U L4 (61000-4-30)
4107	float	_IL_OVER[0]	%	Over difference, I L1
4109	float	_IL_OVER[1]	%	Over difference, I L2
4111	float	_IL_OVER[2]	%	Over difference, I L3
4113	float	_IL_OVER[3]	%	Over difference, I L4
4115	float	_ULN_OVER[0]	%	Over difference, U L1 (61000-4-30)
4117	float	_ULN_OVER[1]	%	Over difference, U L2 (61000-4-30)
4119	float	_ULN_OVER[2]	%	Over difference, U L3 (61000-4-30)
4121	float	_ULN_OVER[3]	%	Over difference, U L4 (61000-4-30)
4123	float	_ULL_NEG_PEAK[0]	V	Peak value negative, U L1-L2
4125	float	_ULL_NEG_PEAK[1]	V	Peak value negative, U L2-L3
4127	float	_ULL_NEG_PEAK[2]	V	Peak value negative, U L3-L1
4129	float	_ULL_POS_PEAK[0]	V	Peak value positive, U L1-L2
4131	float	_ULL_POS_PEAK[1]	V	Peak value positive, U L2-L3
4133	float	_ULL_POS_PEAK[2]	V	Peak value positive, U L3-L1



Address	Format	Designation	Unit	Note
4135	float	_ULL_PEAK_PEAK[0]	V	Peak-peak value, U L1-L2
4137	float	_ULL_PEAK_PEAK[1]	V	Peak-peak value, U L2-L3
4139	float	_ULL_PEAK_PEAK[2]	V	Peak-peak value, U L3-L1
4141	float	_ULL_UNDER[0]	%	Under difference, U L1-L2 (61000-4-30)
4143	float	_ULL_UNDER[1]	%	Under difference, U L2-L3 (61000-4-30)
4145	float	_ULL_UNDER[2]	%	Under difference, U L3-L1 (61000-4-30)
4147	float	_ULL_OVER[0]	%	Over difference, U L1-L2 (61000-4-30)
4149	float	_ULL_OVER[1]	%	Over difference, U L2-L3 (61000-4-30)
4151	float	_ULL_OVER[2]	%	Over difference, U L3-L1 (61000-4-30)
4153	float	_FLI_PF5[0]		Current flicker Pf5, L1-N
4155	float	_FLI_PF5[1]		Current flicker Pf5, L2-N
4157	float	_FLI_PF5[2]		Current flicker Pf5, L3-N
4159	float	_FLI_PF5[3]		Current flicker Pf5, L4-N
4161	float	_FLI_SHORT_TERM[0]		Short-term flicker level, Pst (10m), L1-N
4163	float	_FLI_SHORT_TERM[1]		Short-term flicker level, Pst (10m), L2-N
4165	float	_FLI_SHORT_TERM[2]		Short-term flicker level, Pst (10m), L3-N
4167	float	_FLI_SHORT_TERM[3]		Short-term flicker level, Pst (10m), L4-N
4169	float	_FLI_LONG_TERM[0]		Long-term flicker level, Plt (2h), L1-N
4171	float	_FLI_LONG_TERM[1]		Long-term flicker level, Plt (2h), L2-N
4173	float	_FLI_LONG_TERM[2]		Long-term flicker level, Plt (2h), L3-N
4175	float	_FLI_LONG_TERM[3]		Long-term flicker level, Plt (2h), L4-N
4177	float	_URC[0]	V	Ripple control signal, U L1-N, (61000-4-30)
4179	float	_URC[1]	V	Ripple control signal, U L2-N, (61000-4-30)
4181	float	_URC[2]	V	Ripple control signal, U L3-N, (61000-4-30)
4183	float	_URC[3]	V	Ripple control signal, U L4-N, (61000-4-30)
4185	float	_IRC[0]	A	Ripple control signal, I L1
4187	float	_IRC[1]	A	Ripple control signal, I L2
4189	float	_IRC[2]	A	Ripple control signal, I L3
4191	float	_IRC[3]	A	Ripple control signal, I L4
4193	float	_ULLRC[0]	V	Ripple control signal, U L1-L2, (61000-4-30)
4195	float	_ULLRC[1]	V	Ripple control signal, U L2-L3, (61000-4-30)
4197	float	_ULLRC[2]	V	Ripple control signal, U L3-L1, (61000-4-30)
4209	float	_TEMPERATUR	°C	Internal temperature
13017	float	_IRATED_TDD[0]	A	Maximum demand load current, L1..L3
13019	float	_IRATED_TDD[1]	A	Maximum demand load current, L4
13021	float	_TDD_IL[0]	%	TDD, Total Demand Distortion, ILL1
13023	float	_TDD_IL[1]	%	TDD, Total Demand Distortion, ILL2
13025	float	_TDD_IL[2]	%	TDD, Total Demand Distortion, ILL3
13027	float	_TDD_IL[3]	%	TDD, Total Demand Distortion, ILL4
13222	float	_U_SYM_U0	%	Unsymmetrical; voltage U0

Address	Format	Designation	Unit	Note
<b>Mean values (float type)</b>				
4211	float	_ULN_AVG[0]	V	Mean value of voltage U L1-N
4213	float	_ULN_AVG[1]	V	
4215	float	_ULN_AVG[2]	V	
4217	float	_ULN_AVG[3]	V	
4219	float	_ULL_AVG[0]	V	Mean value of voltage U L1-L2
4221	float	_ULL_AVG[1]	V	
4223	float	_ULL_AVG[2]	V	
4225	float	_ULN_CF_AVG[0]	-	Mean value of the crest factor of the Voltage U L1-N
4227	float	_ULN_CF_AVG[1]	-	
4229	float	_ULN_CF_AVG[2]	-	
4231	float	_ULN_CF_AVG[3]	-	
4233	float	_ULL_CF_AVG[0]	-	Mean value of crest factor of the Voltage U L1-L2
4235	float	_ULL_CF_AVG[1]	-	
4237	float	_ULL_CF_AVG[2]	-	
4239	float	_UN_AVG	V	Mean value zero sequence
4241	float	_UM_AVG	V	Mean value positive sequence
4243	float	_UG_AVG	V	Mean value negative sequence
4245	float	_THD_ULN_AVG[0]	%	Mean value THD U L1-N
4247	float	_THD_ULN_AVG[1]	%	
4249	float	_THD_ULN_AVG[2]	%	
4251	float	_THD_ULN_AVG[3]	%	
4253	float	_THD_ZLN_AVG[0]	%	Mean value ZHD U L1-N
4255	float	_THD_ZLN_AVG[1]	%	
4257	float	_THD_ZLN_AVG[2]	%	
4259	float	_THD_ZLN_AVG[3]	%	
4261	float	_ULN_OVER_AVG[0]	%	
4263	float	_ULN_OVER_AVG[1]	%	
4265	float	_ULN_OVER_AVG[2]	%	
4267	float	_ULN_OVER_AVG[3]	%	
4269	float	_ULN_UNDER_AVG[0]	%	
4271	float	_ULN_UNDER_AVG[1]	%	
4273	float	_ULN_UNDER_AVG[2]	%	
4275	float	_ULN_UNDER_AVG[3]	%	
4277	float	_ULN_NEG_PEAK_AVG[0]	V	
4279	float	_ULN_NEG_PEAK_AVG[1]	V	
4281	float	_ULN_NEG_PEAK_AVG[2]	V	
4283	float	_ULN_NEG_PEAK_AVG[3]	V	
4285	float	_ULN_POS_PEAK_AVG[0]	V	
4287	float	_ULN_POS_PEAK_AVG[1]	V	
4289	float	_ULN_POS_PEAK_AVG[2]	V	
4291	float	_ULN_POS_PEAK_AVG[3]	V	
4293	float	_ULN_PEAK_PEAK_AVG[0]	V	
4295	float	_ULN_PEAK_PEAK_AVG[1]	V	
4297	float	_ULN_PEAK_PEAK_AVG[2]	V	
4299	float	_ULN_PEAK_PEAK_AVG[3]	V	
4301	float	_THD_ULL_AVG[0]	%	
4303	float	_THD_ULL_AVG[1]	%	
4305	float	_THD_ULL_AVG[2]	%	
4307	float	_THD_ZLL_AVG[0]	%	
4309	float	_THD_ZLL_AVG[1]	%	
4311	float	_THD_ZLL_AVG[2]	%	
4313	float	_ULL_OVER_AVG[0]	%	
4315	float	_ULL_OVER_AVG[1]	%	
4317	float	_ULL_OVER_AVG[2]	%	
4319	float	_ULL_UNDER_AVG[0]	%	
4321	float	_ULL_UNDER_AVG[1]	%	

Address	Format	Designation	Unit	Note
4323	float	_ULL_UNDER_AVG[2]	%	
4325	float	_ULL_NEG_PEAK_AVG[0]	V	
4327	float	_ULL_NEG_PEAK_AVG[1]	V	
4329	float	_ULL_NEG_PEAK_AVG[2]	V	
4331	float	_ULL_POS_PEAK_AVG[0]	V	
4333	float	_ULL_POS_PEAK_AVG[1]	V	
4335	float	_ULL_POS_PEAK_AVG[2]	V	
4337	float	_ULL_PEAK_PEAK_AVG[0]	V	
4339	float	_ULL_PEAK_PEAK_AVG[1]	V	
4341	float	_ULL_PEAK_PEAK_AVG[2]	V	
4343	float	_U_STERN_AVG	V	
4345	float	_U_SYM_AVG	%	
4347	float	_FREQ_AVG	Hz	
4349	float	_NORM_FREQ_AVG	Hz	
4351	float	_PLN_AVG[0]	W	
4353	float	_PLN_AVG[1]	W	
4355	float	_PLN_AVG[2]	W	
4357	float	_PLN_AVG[3]	W	
4359	float	_P_SUM_AVG	W	
4361	float	_Q_SUM_AVG	var	
4363	float	_QLN_AVG[0]	var	
4365	float	_QLN_AVG[1]	var	
4367	float	_QLN_AVG[2]	var	
4369	float	_QLN_AVG[3]	var	
4371	float	_P_SUM3_AVG	W	
4373	float	_Q_SUM3_AVG	var	
4375	float	_ILN_AVG[0]	A	
4377	float	_ILN_AVG[1]	A	
4379	float	_ILN_AVG[2]	A	
4381	float	_ILN_AVG[3]	A	
4383	float	_SLN_AVG[0]	VA	
4385	float	_SLN_AVG[1]	VA	
4387	float	_SLN_AVG[2]	VA	
4389	float	_SLN_AVG[3]	VA	
4391	float	_I_SUM3_AVG	A	
4393	float	_I_SUM_AVG	A	
4395	float	_S_SUM3_AVG	VA	
4397	float	_S_SUM_AVG	VA	
4399	float	_THD_IL_AVG[0]	%	
4401	float	_THD_IL_AVG[1]	%	
4403	float	_THD_IL_AVG[2]	%	
4405	float	_THD_IL_AVG[3]	%	
4407	float	_ZHD_IL_AVG[0]	%	
4409	float	_ZHD_IL_AVG[1]	%	
4411	float	_ZHD_IL_AVG[2]	%	
4413	float	_ZHD_IL_AVG[3]	%	
4415	float	_ILN_CF_AVG[0]	-	
4417	float	_ILN_CF_AVG[1]	-	
4419	float	_ILN_CF_AVG[2]	-	
4421	float	_ILN_CF_AVG[3]	-	
4423	float	_IN_AVG	A	
4425	float	_IM_AVG	A	
4427	float	_IG_AVG	A	
4429	float	_I_SYM_AVG	%	
4431	float	_ILN_OVER_AVG[0]	%	
4433	float	_ILN_OVER_AVG[1]	%	
4435	float	_ILN_OVER_AVG[2]	%	
4437	float	_ILN_OVER_AVG[3]	%	
4439	float	_ILN_UNDER_AVG[0]	%	

Address	Format	Designation	Unit	Note
4441	float	_ILN_UNDER_AVG[1]	%	
4443	float	_ILN_UNDER_AVG[2]	%	
4445	float	_ILN_UNDER_AVG[3]	%	
4447	float	_ILN_NEG_PEAK_AVG[0]	A	
4449	float	_ILN_NEG_PEAK_AVG[1]	A	
4451	float	_ILN_NEG_PEAK_AVG[2]	A	
4453	float	_ILN_NEG_PEAK_AVG[3]	A	
4455	float	_ILN_POS_PEAK_AVG[0]	A	
4457	float	_ILN_POS_PEAK_AVG[1]	A	
4459	float	_ILN_POS_PEAK_AVG[2]	A	
4461	float	_ILN_POS_PEAK_AVG[3]	A	
4463	float	_ILN_PEAK_PEAK_AVG[0]	A	
4465	float	_ILN_PEAK_PEAK_AVG[1]	A	
4467	float	_ILN_PEAK_PEAK_AVG[2]	A	
4469	float	_ILN_PEAK_PEAK_AVG[3]	A	
4471	float	_FLI_PF5_AVG[0]		
4473	float	_FLI_PF5_AVG[1]		
4475	float	_FLI_PF5_AVG[2]		
4477	float	_FLI_PF5_AVG[3]		
4479	float	_FLI_ST_AVG[0]		
4481	float	_FLI_ST_AVG[1]		
4483	float	_FLI_ST_AVG[2]		
4485	float	_FLI_ST_AVG[3]		
4487	float	_FLI_LT_AVG[0]		
4489	float	_FLI_LT_AVG[1]		
4491	float	_FLI_LT_AVG[2]		
4493	float	_FLI_LT_AVG[3]		
4495	float	_IRC_AVG[0]	A	
4497	float	_IRC_AVG[1]	A	
4499	float	_IRC_AVG[2]	A	
4501	float	_IRC_AVG[3]	A	
4503	float	_ULLRC_AVG[0]	V	
4505	float	_ULLRC_AVG[1]	V	
4507	float	_ULLRC_AVG[2]	V	
4519	float	_PFLN_AVG[0]	%	
4521	float	_PFLN_AVG[1]	%	
4523	float	_PFLN_AVG[2]	%	
4525	float	_PFLN_AVG[3]	%	
4527	float	_DLN_AVG[0]	var	
4529	float	_DLN_AVG[1]	var	
4531	float	_DLN_AVG[2]	var	
4533	float	_DLN_AVG[3]	var	
4535	float	_KFACT_AVG[0]	%	
4537	float	_KFACT_AVG[1]	%	
4539	float	_KFACT_AVG[2]	%	
4541	float	_KFACT_AVG[3]	%	
4543	float	_S0_POWER_AVG[0]	W	
4545	float	_S0_POWER_AVG[1]	W	
4547	float	_S0_POWER_AVG[2]	W	
4549	float	_S0_POWER_AVG[3]	W	
4551	float	_S0_POWER_AVG[4]	W	
4553	float	_S0_POWER_AVG[5]	W	
4555	float	_S0_POWER_AVG[6]	W	
4557	float	_S0_POWER_AVG[7]	W	
4559	float	_TEMPERATUR_AVG	°C	

Address Format	Designation	Unit	Note
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Address	Format	Designation	Unit	Note
<b>Minimum values (float type)</b>				
4561	float	_ULN_MIN[0]	V	
4563	float	_ULN_MIN[1]	V	
4565	float	_ULN_MIN[2]	V	
4567	float	_ULN_MIN[3]	V	
4569	float	_ULL_MIN[0]	V	
4571	float	_ULL_MIN[1]	V	
4573	float	_ULL_MIN[2]	V	
4575	float	_ULN_CF_MIN[0]	-	
4577	float	_ULN_CF_MIN[1]	-	
4579	float	_ULN_CF_MIN[2]	-	
4581	float	_ULN_CF_MIN[3]	-	
4583	float	_ULL_CF_MIN[0]	-	
4585	float	_ULL_CF_MIN[1]	-	
4587	float	_ULL_CF_MIN[2]	-	
4589	float	_UN_MIN	V	
4591	float	_UM_MIN	V	
4593	float	_UG_MIN	V	
4595	float	_URC_MIN[0]	V	
4597	float	_URC_MIN[1]	V	
4599	float	_URC_MIN[2]	V	
4601	float	_URC_MIN[3]	V	
4603	float	_THD_ULN_MIN[0]	%	
4605	float	_THD_ULN_MIN[1]	%	
4607	float	_THD_ULN_MIN[2]	%	
4609	float	_THD_ULN_MIN[3]	%	
4611	float	_THD_ZLN_MIN[0]	%	
4613	float	_THD_ZLN_MIN[1]	%	
4615	float	_THD_ZLN_MIN[2]	%	
4617	float	_THD_ZLN_MIN[3]	%	
4619	float	_ULN_OVER_MIN[0]	%	
4621	float	_ULN_OVER_MIN[1]	%	
4623	float	_ULN_OVER_MIN[2]	%	
4625	float	_ULN_OVER_MIN[3]	%	
4627	float	_ULN_UNDER_MIN[0]	%	
4629	float	_ULN_UNDER_MIN[1]	%	
4631	float	_ULN_UNDER_MIN[2]	%	
4633	float	_ULN_UNDER_MIN[3]	%	
4635	float	_ULN_NEG_PEAK_MIN[0]	V	
4637	float	_ULN_NEG_PEAK_MIN[1]	V	
4639	float	_ULN_NEG_PEAK_MIN[2]	V	
4641	float	_ULN_NEG_PEAK_MIN[3]	V	
4643	float	_ULN_POS_PEAK_MIN[0]	V	
4645	float	_ULN_POS_PEAK_MIN[1]	V	
4647	float	_ULN_POS_PEAK_MIN[2]	V	
4649	float	_ULN_POS_PEAK_MIN[3]	V	
4651	float	_ULN_PEAK_PEAK_MIN[0]	V	
4653	float	_ULN_PEAK_PEAK_MIN[1]	V	
4655	float	_ULN_PEAK_PEAK_MIN[2]	V	
4657	float	_ULN_PEAK_PEAK_MIN[3]	V	
4659	float	_THD_ULL_MIN[0]	%	
4661	float	_THD_ULL_MIN[1]	%	
4663	float	_THD_ULL_MIN[2]	%	
4665	float	_THD_ZLL_MIN[0]	%	
4667	float	_THD_ZLL_MIN[1]	%	
4669	float	_THD_ZLL_MIN[2]	%	
4671	float	_ULL_OVER_MIN[0]	%	
4673	float	_ULL_OVER_MIN[1]	%	
4675	float	_ULL_OVER_MIN[2]	%	

Address	Format	Designation	Unit	Note
4677	float	_ULL_UNDER_MIN[0]	%	
4679	float	_ULL_UNDER_MIN[1]	%	
4681	float	_ULL_UNDER_MIN[2]	%	
4683	float	_ULL_NEG_PEAK_MIN[0]	V	
4685	float	_ULL_NEG_PEAK_MIN[1]	V	
4687	float	_ULL_NEG_PEAK_MIN[2]	V	
4689	float	_ULL_POS_PEAK_MIN[0]	V	
4691	float	_ULL_POS_PEAK_MIN[1]	V	
4693	float	_ULL_POS_PEAK_MIN[2]	V	
4695	float	_ULL_PEAK_PEAK_MIN[0]	V	
4697	float	_ULL_PEAK_PEAK_MIN[1]	V	
4699	float	_ULL_PEAK_PEAK_MIN[2]	V	
4701	float	_U_STERN_MIN	V	
4703	float	_U_SYM_MIN	%	
4705	float	_FREQ_MIN	Hz	
4707	float	_NORM_FREQ_MIN	Hz	
4709	float	_PLN_MIN[0]	W	
4711	float	_PLN_MIN[1]	W	
4713	float	_PLN_MIN[2]	W	
4715	float	_PLN_MIN[3]	W	
4717	float	_P_SUM_MIN	W	
4719	float	_Q_SUM_MIN	var	
4721	float	_QLN_MIN[0]	var	
4723	float	_QLN_MIN[1]	var	
4725	float	_QLN_MIN[2]	var	
4727	float	_QLN_MIN[3]	var	
4729	float	_P_SUM3_MIN	W	
4731	float	_Q_SUM3_MIN	var	
4733	float	_TEMPERATUR_MIN	°C	Minimum, internal temperature

Address	Format	Designation	Unit	Note
<b>Maximum values (float type)</b>				
4735	float	_ULN_MAX[0]	V	
4737	float	_ULN_MAX[1]	V	
4739	float	_ULN_MAX[2]	V	
4741	float	_ULN_MAX[3]	V	
4743	float	_ULL_MAX[0]	V	
4745	float	_ULL_MAX[1]	V	
4747	float	_ULL_MAX[2]	V	
4749	float	_ULN_CF_MAX[0]	-	
4751	float	_ULN_CF_MAX[1]	-	
4753	float	_ULN_CF_MAX[2]	-	
4755	float	_ULN_CF_MAX[3]	-	
4757	float	_ULL_CF_MAX[0]	-	
4759	float	_ULL_CF_MAX[1]	-	
4761	float	_ULL_CF_MAX[2]	-	
4763	float	_UN_MAX	V	
4765	float	_UM_MAX	V	
4767	float	_UG_MAX	V	
4769	float	_URC_MAX[0]	V	
4771	float	_URC_MAX[1]	V	
4773	float	_URC_MAX[2]	V	
4775	float	_URC_MAX[3]	V	
4777	float	_THD_ULN_MAX[0]	%	
4779	float	_THD_ULN_MAX[1]	%	
4781	float	_THD_ULN_MAX[2]	%	
4783	float	_THD_ULN_MAX[3]	%	
4785	float	_THD_ZLN_MAX[0]	%	
4787	float	_THD_ZLN_MAX[1]	%	
4789	float	_THD_ZLN_MAX[2]	%	
4791	float	_THD_ZLN_MAX[3]	%	
4793	float	_ULN_OVER_MAX[0]	%	
4795	float	_ULN_OVER_MAX[1]	%	
4797	float	_ULN_OVER_MAX[2]	%	
4799	float	_ULN_OVER_MAX[3]	%	
4801	float	_ULN_UNDER_MAX[0]	%	
4803	float	_ULN_UNDER_MAX[1]	%	
4805	float	_ULN_UNDER_MAX[2]	%	
4807	float	_ULN_UNDER_MAX[3]	%	
4809	float	_ULN_NEG_PEAK_MAX[0]	V	
4811	float	_ULN_NEG_PEAK_MAX[1]	V	
4813	float	_ULN_NEG_PEAK_MAX[2]	V	
4815	float	_ULN_NEG_PEAK_MAX[3]	V	
4817	float	_ULN_POS_PEAK_MAX[0]	V	
4819	float	_ULN_POS_PEAK_MAX[1]	V	
4821	float	_ULN_POS_PEAK_MAX[2]	V	
4823	float	_ULN_POS_PEAK_MAX[3]	V	
4825	float	_ULN_PEAK_PEAK_MAX[0]	V	
4827	float	_ULN_PEAK_PEAK_MAX[1]	V	
4829	float	_ULN_PEAK_PEAK_MAX[2]	V	
4831	float	_ULN_PEAK_PEAK_MAX[3]	V	
4833	float	_THD_ULL_MAX[0]	%	
4835	float	_THD_ULL_MAX[1]	%	
4837	float	_THD_ULL_MAX[2]	%	
4839	float	_THD_ZLL_MAX[0]	%	
4841	float	_THD_ZLL_MAX[1]	%	
4843	float	_THD_ZLL_MAX[2]	%	
4845	float	_ULL_OVER_MAX[0]	%	
4847	float	_ULL_OVER_MAX[1]	%	
4849	float	_ULL_OVER_MAX[2]	%	



Address	Format	Designation	Unit	Note
4851	float	_ULL_UNDER_MAX[0]	%	
4853	float	_ULL_UNDER_MAX[1]	%	
4855	float	_ULL_UNDER_MAX[2]	%	
4857	float	_ULL_NEG_PEAK_MAX[0]	V	
4859	float	_ULL_NEG_PEAK_MAX[1]	V	
4861	float	_ULL_NEG_PEAK_MAX[2]	V	
4863	float	_ULL_POS_PEAK_MAX[0]	V	
4865	float	_ULL_POS_PEAK_MAX[1]	V	
4867	float	_ULL_POS_PEAK_MAX[2]	V	
4869	float	_ULL_PEAK_PEAK_MAX[0]	V	
4871	float	_ULL_PEAK_PEAK_MAX[1]	V	
4873	float	_ULL_PEAK_PEAK_MAX[2]	V	
4875	float	_U_STERN_MAX	V	
4877	float	_U_SYM_MAX	%	
4879	float	_FREQ_MAX	Hz	
4881	float	_NORM_FREQ_MAX	Hz	
4883	float	_PLN_MAX[0]	W	
4885	float	_PLN_MAX[1]	W	
4887	float	_PLN_MAX[2]	W	
4889	float	_PLN_MAX[3]	W	
4891	float	_P_SUM_MAX	W	
4893	float	_Q_SUM_MAX	var	
4895	float	_QLN_MAX[0]	var	
4897	float	_QLN_MAX[1]	var	
4899	float	_QLN_MAX[2]	var	
4901	float	_QLN_MAX[3]	var	
4903	float	_P_SUM3_MAX	W	
4905	float	_Q_SUM3_MAX	var	
4907	float	_ILN_MAX[0]	A	
4909	float	_ILN_MAX[1]	A	
4911	float	_ILN_MAX[2]	A	
4913	float	_ILN_MAX[3]	A	
4915	float	_SLN_MAX[0]	VA	
4917	float	_SLN_MAX[1]	VA	
4919	float	_SLN_MAX[2]	VA	
4921	float	_SLN_MAX[3]	VA	
4923	float	_I_SUM3_MAX	A	
4925	float	_I_SUM_MAX	A	
4927	float	_S_SUM3_MAX	VA	
4929	float	_S_SUM_MAX	VA	
4931	float	_THD_IL_MAX[0]	%	
4933	float	_THD_IL_MAX[1]	%	
4935	float	_THD_IL_MAX[2]	%	
4937	float	_THD_IL_MAX[3]	%	
4939	float	_ZHD_IL_MAX[0]	%	
4941	float	_ZHD_IL_MAX[1]	%	
4943	float	_ZHD_IL_MAX[2]	%	
4945	float	_ZHD_IL_MAX[3]	%	
4947	float	_ILN_CF_MAX[0]	-	
4949	float	_ILN_CF_MAX[1]	-	
4951	float	_ILN_CF_MAX[2]	-	
4953	float	_ILN_CF_MAX[3]	-	
4955	float	_IN_MAX	A	
4957	float	_IM_MAX	A	
4959	float	_IG_MAX	A	
4961	float	_I_SYM_MAX	%	
4963	float	_ILN_OVER_MAX[0]	%	
4965	float	_ILN_OVER_MAX[1]	%	
4967	float	_ILN_OVER_MAX[2]	%	

Address	Format	Designation	Unit	Note
4969	float	_ILN_OVER_MAX[3]	%	
4971	float	_ILN_UNDER_MAX[0]	%	
4973	float	_ILN_UNDER_MAX[1]	%	
4975	float	_ILN_UNDER_MAX[2]	%	
4977	float	_ILN_UNDER_MAX[3]	%	
4979	float	_ILN_NEG_PEAK_MAX[0]	A	
4981	float	_ILN_NEG_PEAK_MAX[1]	A	
4983	float	_ILN_NEG_PEAK_MAX[2]	A	
4985	float	_ILN_NEG_PEAK_MAX[3]	A	
4987	float	_ILN_POS_PEAK_MAX[0]	A	
4989	float	_ILN_POS_PEAK_MAX[1]	A	
4991	float	_ILN_POS_PEAK_MAX[2]	A	
4993	float	_ILN_POS_PEAK_MAX[3]	A	
4995	float	_ILN_PEAK_PEAK_MAX[0]	A	
4997	float	_ILN_PEAK_PEAK_MAX[1]	A	
4999	float	_ILN_PEAK_PEAK_MAX[2]	A	
5001	float	_ILN_PEAK_PEAK_MAX[3]	A	
5003	float	_FLI_PF5_MAX[0]		
5005	float	_FLI_PF5_MAX[1]		
5007	float	_FLI_PF5_MAX[2]		
5009	float	_FLI_PF5_MAX[3]		
5011	float	_FLI_ST_MAX[0]		
5013	float	_FLI_ST_MAX[1]		
5015	float	_FLI_ST_MAX[2]		
5017	float	_FLI_ST_MAX[3]		
5019	float	_FLI_LT_MAX[0]		
5021	float	_FLI_LT_MAX[1]		
5023	float	_FLI_LT_MAX[2]		
5025	float	_FLI_LT_MAX[3]		
5027	float	_ILN_RC_MAX[0]	A	
5029	float	_ILN_RC_MAX[1]	A	
5031	float	_ILN_RC_MAX[2]	A	
5033	float	_ILN_RC_MAX[3]	A	
5035	float	_ULLRC_MAX[0]	V	
5037	float	_ULLRC_MAX[1]	V	
5039	float	_ULLRC_MAX[2]	V	
5051	float	_PFLN_MAX[0]	%	
5053	float	_PFLN_MAX[1]	%	
5055	float	_PFLN_MAX[2]	%	
5057	float	_PFLN_MAX[3]	%	
5059	float	_DLN_MAX[0]	var	
5061	float	_DLN_MAX[1]	var	
5063	float	_DLN_MAX[2]	var	
5065	float	_DLN_MAX[3]	var	
5067	float	_KFACT_MAX[0]	%	
5069	float	_KFACT_MAX[1]	%	
5071	float	_KFACT_MAX[2]	%	
5073	float	_KFACT_MAX[3]	%	
5075	float	_S0_POWER_MAX[0]	W	
5077	float	_S0_POWER_MAX[1]	W	
5079	float	_S0_POWER_MAX[2]	W	
5081	float	_S0_POWER_MAX[3]	W	
5083	float	_S0_POWER_MAX[4]	W	
5085	float	_S0_POWER_MAX[5]	W	
5087	float	_S0_POWER_MAX[6]	W	
5089	float	_S0_POWER_MAX[7]	W	
5091	float	_TEMPERATUR_MAX	°C	

Address	Format	Designation	Unit	Note
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Address	Format	Designation	Unit	Note
<b>Averaging time</b>				
5093	short	_ULN_AVG_T[0]	n	Averaging time U L1-N, n refer to table, page 6
5094	short	_ULN_AVG_T[1]	n	
5095	short	_ULN_AVG_T[2]	n	
5096	short	_ULN_AVG_T[3]	n	
5097	short	_ULL_AVG_T[0]	n	
5098	short	_ULL_AVG_T[1]	n	
5099	short	_ULL_AVG_T[2]	n	
5100	short	_ULN_CF_AVG_T[0]	n	
5101	short	_ULN_CF_AVG_T[1]	n	
5102	short	_ULN_CF_AVG_T[2]	n	
5103	short	_ULN_CF_AVG_T[3]	n	
5104	short	_ULL_CF_AVG_T[0]	n	
5105	short	_ULL_CF_AVG_T[1]	n	
5106	short	_ULL_CF_AVG_T[2]	n	
5107	short	_UN_AVG_T	n	
5108	short	_UM_AVG_T	n	
5109	short	_UG_AVG_T	n	
5110	short	_URC_AVG_T[0]	n	
5111	short	_URC_AVG_T[1]	n	
5112	short	_URC_AVG_T[2]	n	
5113	short	_URC_AVG_T[3]	n	
5114	short	_THD_ULN_AVG_T[0]	n	
5115	short	_THD_ULN_AVG_T[1]	n	
5116	short	_THD_ULN_AVG_T[2]	n	
5117	short	_THD_ULN_AVG_T[3]	n	
5118	short	_THD_ZLN_AVG_T[0]	n	
5119	short	_THD_ZLN_AVG_T[1]	n	
5120	short	_THD_ZLN_AVG_T[2]	n	
5121	short	_THD_ZLN_AVG_T[3]	n	
5122	short	_ULN_OVER_AVG_T[0]	n	
5123	short	_ULN_OVER_AVG_T[1]	n	
5124	short	_ULN_OVER_AVG_T[2]	n	
5125	short	_ULN_OVER_AVG_T[3]	n	
5126	short	_ULN_UNDER_AVG_T[0]	n	
5127	short	_ULN_UNDER_AVG_T[1]	n	
5128	short	_ULN_UNDER_AVG_T[2]	n	
5129	short	_ULN_UNDER_AVG_T[3]	n	
5130	short	_ULN_NEG_PEAK_AVG_T[0]	n	
5131	short	_ULN_NEG_PEAK_AVG_T[1]	n	
5132	short	_ULN_NEG_PEAK_AVG_T[2]	n	
5133	short	_ULN_NEG_PEAK_AVG_T[3]	n	
5134	short	_ULN_POS_PEAK_AVG_T[0]	n	
5135	short	_ULN_POS_PEAK_AVG_T[1]	n	
5136	short	_ULN_POS_PEAK_AVG_T[2]	n	
5137	short	_ULN_POS_PEAK_AVG_T[3]	n	
5138	short	_ULN_PEAK_PEAK_AVG_T[0]	n	
5139	short	_ULN_PEAK_PEAK_AVG_T[1]	n	
5140	short	_ULN_PEAK_PEAK_AVG_T[2]	n	
5141	short	_ULN_PEAK_PEAK_AVG_T[3]	n	
5142	short	_THD_ULL_AVG_T[0]	n	
5143	short	_THD_ULL_AVG_T[1]	n	
5144	short	_THD_ULL_AVG_T[2]	n	
5145	short	_THD_ZLL_AVG_T[0]	n	
5146	short	_THD_ZLL_AVG_T[1]	n	
5147	short	_THD_ZLL_AVG_T[2]	n	
5148	short	_ULL_OVER_AVG_T[0]	n	
5149	short	_ULL_OVER_AVG_T[1]	n	

Address	Format	Designation	Unit	Note
5150	short	_ULL_OVER_AVG_T[2]	n	
5151	short	_ULL_UNDER_AVG_T[0]	n	
5152	short	_ULL_UNDER_AVG_T[1]	n	
5153	short	_ULL_UNDER_AVG_T[2]	n	
5154	short	_ULL_NEG_PEAK_AVG_T[0]	n	
5155	short	_ULL_NEG_PEAK_AVG_T[1]	n	
5156	short	_ULL_NEG_PEAK_AVG_T[2]	n	
5157	short	_ULL_POS_PEAK_AVG_T[0]	n	
5158	short	_ULL_POS_PEAK_AVG_T[1]	n	
5159	short	_ULL_POS_PEAK_AVG_T[2]	n	
5160	short	_ULL_PEAK_PEAK_AVG_T[0]	n	
5161	short	_ULL_PEAK_PEAK_AVG_T[1]	n	
5162	short	_ULL_PEAK_PEAK_AVG_T[2]	n	
5163	short	_U_STERN_AVG_T	n	
5164	short	_U_SYM_AVG_T	n	
5165	short	_FREQ_AVG_T	n	
5166	short	_NORM_FREQ_AVG_T	n	
5167	short	_PLN_AVG_T[0]	n	
5168	short	_PLN_AVG_T[1]	n	
5169	short	_PLN_AVG_T[2]	n	
5170	short	_PLN_AVG_T[3]	n	
5171	short	_P_SUM_AVG_T	n	
5172	short	_Q_SUM_AVG_T	n	
5173	short	_QLN_AVG_T[0]	n	
5174	short	_QLN_AVG_T[1]	n	
5175	short	_QLN_AVG_T[2]	n	
5176	short	_QLN_AVG_T[3]	n	
5177	short	_P_SUM3_AVG_T	n	
5178	short	_Q_SUM3_AVG_T	n	
5179	short	_ILN_AVG_T[0]	n	
5180	short	_ILN_AVG_T[1]	n	
5181	short	_ILN_AVG_T[2]	n	
5182	short	_ILN_AVG_T[3]	n	
5183	short	_SLN_AVG_T[0]	n	
5184	short	_SLN_AVG_T[1]	n	
5185	short	_SLN_AVG_T[2]	n	
5186	short	_SLN_AVG_T[3]	n	
5187	short	_I_SUM3_AVG_T	n	
5188	short	_I_SUM_AVG_T	n	
5189	short	_S_SUM3_AVG_T	n	
5190	short	_S_SUM_AVG_T	n	
5191	short	_THD_IL_AVG_T[0]	n	
5192	short	_THD_IL_AVG_T[1]	n	
5193	short	_THD_IL_AVG_T[2]	n	
5194	short	_THD_IL_AVG_T[3]	n	
5195	short	_ZHD_IL_AVG_T[0]	n	
5196	short	_ZHD_IL_AVG_T[1]	n	
5197	short	_ZHD_IL_AVG_T[2]	n	
5198	short	_ZHD_IL_AVG_T[3]	n	
5199	short	_ILN_CF_AVG_T[0]	n	
5200	short	_ILN_CF_AVG_T[1]	n	
5201	short	_ILN_CF_AVG_T[2]	n	
5202	short	_ILN_CF_AVG_T[3]	n	
5203	short	_IN_AVG_T	n	
5204	short	_IM_AVG_T	n	
5205	short	_IG_AVG_T	n	
5206	short	_I_SYM_AVG_T	n	
5207	short	_ILN_OVER_AVG_T[0]	n	
5208	short	_ILN_OVER_AVG_T[1]	n	

Address	Format	Designation	Unit	Note
5209	short	_ILN_OVER_AVG_T[2]	n	
5210	short	_ILN_OVER_AVG_T[3]	n	
5211	short	_ILN_UNDER_AVG_T[0]	n	
5212	short	_ILN_UNDER_AVG_T[1]	n	
5213	short	_ILN_UNDER_AVG_T[2]	n	
5214	short	_ILN_UNDER_AVG_T[3]	n	
5215	short	_ILN_NEG_PEAK_AVG_T[0]	n	
5216	short	_ILN_NEG_PEAK_AVG_T[1]	n	
5217	short	_ILN_NEG_PEAK_AVG_T[2]	n	
5218	short	_ILN_NEG_PEAK_AVG_T[3]	n	
5219	short	_ILN_POS_PEAK_AVG_T[0]	n	
5220	short	_ILN_POS_PEAK_AVG_T[1]	n	
5221	short	_ILN_POS_PEAK_AVG_T[2]	n	
5222	short	_ILN_POS_PEAK_AVG_T[3]	n	
5223	short	_ILN_PEAK_PEAK_AVG_T[0]	n	
5224	short	_ILN_PEAK_PEAK_AVG_T[1]	n	
5225	short	_ILN_PEAK_PEAK_AVG_T[2]	n	
5226	short	_ILN_PEAK_PEAK_AVG_T[3]	n	
5227	short	_FLI_PF5_AVG_T[0]	n	
5228	short	_FLI_PF5_AVG_T[1]	n	
5229	short	_FLI_PF5_AVG_T[2]	n	
5230	short	_FLI_PF5_AVG_T[3]	n	
5231	short	_FLI_ST_AVG_T[0]	n	
5232	short	_FLI_ST_AVG_T[1]	n	
5233	short	_FLI_ST_AVG_T[2]	n	
5234	short	_FLI_ST_AVG_T[3]	n	
5235	short	_FLI_LT_AVG_T[0]	n	
5236	short	_FLI_LT_AVG_T[1]	n	
5237	short	_FLI_LT_AVG_T[2]	n	
5238	short	_FLI_LT_AVG_T[3]	n	
5239	short	_ILN_RC_AVG_T[0]	n	
5240	short	_ILN_RC_AVG_T[1]	n	
5241	short	_ILN_RC_AVG_T[2]	n	
5242	short	_ILN_RC_AVG_T[3]	n	
5243	short	_ULLRC_AVG_T[0]	n	
5244	short	_ULLRC_AVG_T[1]	n	
5245	short	_ULLRC_AVG_T[2]	n	
5251	short	_PFLN_AVG_T[0]	n	
5252	short	_PFLN_AVG_T[1]	n	
5253	short	_PFLN_AVG_T[2]	n	
5254	short	_PFLN_AVG_T[3]	n	
5255	short	_DLN_AVG_T[0]	n	
5256	short	_DLN_AVG_T[1]	n	
5257	short	_DLN_AVG_T[2]	n	
5258	short	_DLN_AVG_T[3]	n	
5259	short	_KFACT_AVG_T[0]	n	
5260	short	_KFACT_AVG_T[1]	n	
5261	short	_KFACT_AVG_T[2]	n	
5262	short	_KFACT_AVG_T[3]	n	
5263	short	_S0_POWER_AVG_T[0]	n	
5264	short	_S0_POWER_AVG_T[1]	n	
5265	short	_S0_POWER_AVG_T[2]	n	
5266	short	_S0_POWER_AVG_T[3]	n	
5267	short	_S0_POWER_AVG_T[4]	n	
5268	short	_S0_POWER_AVG_T[5]	n	
5269	short	_S0_POWER_AVG_T[6]	n	
5270	short	_S0_POWER_AVG_T[7]	n	
5271	short	_TEMPERATUR_AVG_T	n	Averaging time, internal temperature

Address	Format	Designation	Unit	Note
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Address	Format	Designation	Unit	Note
<b>Minimum values time stamp</b>				
5272	uint	_ULN_MIN_T[0]	s	Time of minimum value (UTC time)
5274	uint	_ULN_MIN_T[1]	s	
5276	uint	_ULN_MIN_T[2]	s	
5278	uint	_ULN_MIN_T[3]	s	
5280	uint	_ULL_MIN_T[0]	s	
5282	uint	_ULL_MIN_T[1]	s	
5284	uint	_ULL_MIN_T[2]	s	
5286	uint	_ULN_CF_MIN_T[0]	s	
5288	uint	_ULN_CF_MIN_T[1]	s	
5290	uint	_ULN_CF_MIN_T[2]	s	
5292	uint	_ULN_CF_MIN_T[3]	s	
5294	uint	_ULL_CF_MIN_T[0]	s	
5296	uint	_ULL_CF_MIN_T[1]	s	
5298	uint	_ULL_CF_MIN_T[2]	s	
5300	uint	_UN_MIN_T	s	
5302	uint	_UM_MIN_T	s	
5304	uint	_UG_MIN_T	s	
5306	uint	_URC_MIN_T[0]	s	
5308	uint	_URC_MIN_T[1]	s	
5310	uint	_URC_MIN_T[2]	s	
5312	uint	_URC_MIN_T[3]	s	
5314	uint	_THD_ULN_MIN_T[0]	s	
5316	uint	_THD_ULN_MIN_T[1]	s	
5318	uint	_THD_ULN_MIN_T[2]	s	
5320	uint	_THD_ULN_MIN_T[3]	s	
5322	uint	_THD_ZLN_MIN_T[0]	s	
5324	uint	_THD_ZLN_MIN_T[1]	s	
5326	uint	_THD_ZLN_MIN_T[2]	s	
5328	uint	_THD_ZLN_MIN_T[3]	s	
5330	uint	_ULN_OVER_MIN_T[0]	s	
5332	uint	_ULN_OVER_MIN_T[1]	s	
5334	uint	_ULN_OVER_MIN_T[2]	s	
5336	uint	_ULN_OVER_MIN_T[3]	s	
5338	uint	_ULN_UNDER_MIN_T[0]	s	
5340	uint	_ULN_UNDER_MIN_T[1]	s	
5342	uint	_ULN_UNDER_MIN_T[2]	s	
5344	uint	_ULN_UNDER_MIN_T[3]	s	
5346	uint	_ULN_NEG_PEAK_MIN_T[0]	s	
5348	uint	_ULN_NEG_PEAK_MIN_T[1]	s	
5350	uint	_ULN_NEG_PEAK_MIN_T[2]	s	
5352	uint	_ULN_NEG_PEAK_MIN_T[3]	s	
5354	uint	_ULN_POS_PEAK_MIN_T[0]	s	
5356	uint	_ULN_POS_PEAK_MIN_T[1]	s	
5358	uint	_ULN_POS_PEAK_MIN_T[2]	s	
5360	uint	_ULN_POS_PEAK_MIN_T[3]	s	
5362	uint	_ULN_PEAK_PEAK_MIN_T[0]	s	
5364	uint	_ULN_PEAK_PEAK_MIN_T[1]	s	
5366	uint	_ULN_PEAK_PEAK_MIN_T[2]	s	
5368	uint	_ULN_PEAK_PEAK_MIN_T[3]	s	
5370	uint	_THD_ULL_MIN_T[0]	s	
5372	uint	_THD_ULL_MIN_T[1]	s	
5374	uint	_THD_ULL_MIN_T[2]	s	
5376	uint	_THD_ZLL_MIN_T[0]	s	
5378	uint	_THD_ZLL_MIN_T[1]	s	
5380	uint	_THD_ZLL_MIN_T[2]	s	
5382	uint	_ULL_OVER_MIN_T[0]	s	
5384	uint	_ULL_OVER_MIN_T[1]	s	
5386	uint	_ULL_OVER_MIN_T[2]	s	



Address	Format	Designation	Unit	Note
5388	uint	_ULL_UNDER_MIN_T[0]	s	
5390	uint	_ULL_UNDER_MIN_T[1]	s	
5392	uint	_ULL_UNDER_MIN_T[2]	s	
5394	uint	_ULL_NEG_PEAK_MIN_T[0]	s	
5396	uint	_ULL_NEG_PEAK_MIN_T[1]	s	
5398	uint	_ULL_NEG_PEAK_MIN_T[2]	s	
5400	uint	_ULL_POS_PEAK_MIN_T[0]	s	
5402	uint	_ULL_POS_PEAK_MIN_T[1]	s	
5404	uint	_ULL_POS_PEAK_MIN_T[2]	s	
5406	uint	_ULL_PEAK_PEAK_MIN_T[0]	s	
5408	uint	_ULL_PEAK_PEAK_MIN_T[1]	s	
5410	uint	_ULL_PEAK_PEAK_MIN_T[2]	s	
5412	uint	_U_STERN_MIN_T	s	
5414	uint	_U_SYM_MIN_T	s	
5416	uint	_FREQ_MIN_T	s	
5418	uint	_NORM_FREQ_MIN_T	s	
5420	uint	_PLN_MIN_T[0]	s	
5422	uint	_PLN_MIN_T[1]	s	
5424	uint	_PLN_MIN_T[2]	s	
5426	uint	_PLN_MIN_T[3]	s	
5428	uint	_P_SUM_MIN_T	s	
5430	uint	_Q_SUM_MIN_T	s	
5432	uint	_QLN_MIN_T[0]	s	
5434	uint	_QLN_MIN_T[1]	s	
5436	uint	_QLN_MIN_T[2]	s	
5438	uint	_QLN_MIN_T[3]	s	
5440	uint	_P_SUM3_MIN_T	s	
5442	uint	_Q_SUM3_MIN_T	s	
5444	uint	_TEMPERATUR_MIN_T	s	Time of min. val. (UTC), internal temperature

Address	Format	Designation	Unit	Note
<b>Maximum values time stamp</b>				
5446	uint	_ULN_MAX_T[0]	s	Time of minimum value (UTC time)
5448	uint	_ULN_MAX_T[1]	s	
5450	uint	_ULN_MAX_T[2]	s	
5452	uint	_ULN_MAX_T[3]	s	
5454	uint	_ULL_MAX_T[0]	s	
5456	uint	_ULL_MAX_T[1]	s	
5458	uint	_ULL_MAX_T[2]	s	
5460	uint	_ULN_CF_MAX_T[0]	s	
5462	uint	_ULN_CF_MAX_T[1]	s	
5464	uint	_ULN_CF_MAX_T[2]	s	
5466	uint	_ULN_CF_MAX_T[3]	s	
5468	uint	_ULL_CF_MAX_T[0]	s	
5470	uint	_ULL_CF_MAX_T[1]	s	
5472	uint	_ULL_CF_MAX_T[2]	s	
5474	uint	_UN_MAX_T	s	
5476	uint	_UM_MAX_T	s	
5478	uint	_UG_MAX_T	s	
5480	uint	_URC_MAX_T[0]	s	
5482	uint	_URC_MAX_T[1]	s	
5484	uint	_URC_MAX_T[2]	s	
5486	uint	_URC_MAX_T[3]	s	
5488	uint	_THD_ULN_MAX_T[0]	s	
5490	uint	_THD_ULN_MAX_T[1]	s	
5492	uint	_THD_ULN_MAX_T[2]	s	
5494	uint	_THD_ULN_MAX_T[3]	s	
5496	uint	_THD_ZLN_MAX_T[0]	s	
5498	uint	_THD_ZLN_MAX_T[1]	s	
5500	uint	_THD_ZLN_MAX_T[2]	s	
5502	uint	_THD_ZLN_MAX_T[3]	s	
5504	uint	_ULN_OVER_MAX_T[0]	s	
5506	uint	_ULN_OVER_MAX_T[1]	s	
5508	uint	_ULN_OVER_MAX_T[2]	s	
5510	uint	_ULN_OVER_MAX_T[3]	s	
5512	uint	_ULN_UNDER_MAX_T[0]	s	
5514	uint	_ULN_UNDER_MAX_T[1]	s	
5516	uint	_ULN_UNDER_MAX_T[2]	s	
5518	uint	_ULN_UNDER_MAX_T[3]	s	
5520	uint	_ULN_NEG_PEAK_MAX_T[0]	s	
5522	uint	_ULN_NEG_PEAK_MAX_T[1]	s	
5524	uint	_ULN_NEG_PEAK_MAX_T[2]	s	
5526	uint	_ULN_NEG_PEAK_MAX_T[3]	s	
5528	uint	_ULN_POS_PEAK_MAX_T[0]	s	
5530	uint	_ULN_POS_PEAK_MAX_T[1]	s	
5532	uint	_ULN_POS_PEAK_MAX_T[2]	s	
5534	uint	_ULN_POS_PEAK_MAX_T[3]	s	
5536	uint	_ULN_PEAK_PEAK_MAX_T[0]	s	
5538	uint	_ULN_PEAK_PEAK_MAX_T[1]	s	
5540	uint	_ULN_PEAK_PEAK_MAX_T[2]	s	
5542	uint	_ULN_PEAK_PEAK_MAX_T[3]	s	
5544	uint	_THD_ULL_MAX_T[0]	s	
5546	uint	_THD_ULL_MAX_T[1]	s	
5548	uint	_THD_ULL_MAX_T[2]	s	
5550	uint	_THD_ZLL_MAX_T[0]	s	
5552	uint	_THD_ZLL_MAX_T[1]	s	
5554	uint	_THD_ZLL_MAX_T[2]	s	
5556	uint	_ULL_OVER_MAX_T[0]	s	
5558	uint	_ULL_OVER_MAX_T[1]	s	
5560	uint	_ULL_OVER_MAX_T[2]	s	

Address	Format	Designation	Unit	Note
5562	uint	_ULL_UNDER_MAX_T[0]	s	
5564	uint	_ULL_UNDER_MAX_T[1]	s	
5566	uint	_ULL_UNDER_MAX_T[2]	s	
5568	uint	_ULL_NEG_PEAK_MAX_T[0]	s	
5570	uint	_ULL_NEG_PEAK_MAX_T[1]	s	
5572	uint	_ULL_NEG_PEAK_MAX_T[2]	s	
5574	uint	_ULL_POS_PEAK_MAX_T[0]	s	
5576	uint	_ULL_POS_PEAK_MAX_T[1]	s	
5578	uint	_ULL_POS_PEAK_MAX_T[2]	s	
5580	uint	_ULL_PEAK_PEAK_MAX_T[0]	s	
5582	uint	_ULL_PEAK_PEAK_MAX_T[1]	s	
5584	uint	_ULL_PEAK_PEAK_MAX_T[2]	s	
5586	uint	_U_STERN_MAX_T	s	
5588	uint	_U_SYM_MAX_T	s	
5590	uint	_FREQ_MAX_T	s	
5592	uint	_NORM_FREQ_MAX_T	s	
5594	uint	_PLN_MAX_T[0]	s	
5596	uint	_PLN_MAX_T[1]	s	
5598	uint	_PLN_MAX_T[2]	s	
5600	uint	_PLN_MAX_T[3]	s	
5602	uint	_P_SUM_MAX_T	s	
5604	uint	_Q_SUM_MAX_T	s	
5606	uint	_QLN_MAX_T[0]	s	
5608	uint	_QLN_MAX_T[1]	s	
5610	uint	_QLN_MAX_T[2]	s	
5612	uint	_QLN_MAX_T[3]	s	
5614	uint	_P_SUM3_MAX_T	s	
5616	uint	_Q_SUM3_MAX_T	s	
5618	uint	_ILN_MAX_T[0]	s	
5620	uint	_ILN_MAX_T[1]	s	
5622	uint	_ILN_MAX_T[2]	s	
5624	uint	_ILN_MAX_T[3]	s	
5626	uint	_SLN_MAX_T[0]	s	
5628	uint	_SLN_MAX_T[1]	s	
5630	uint	_SLN_MAX_T[2]	s	
5632	uint	_SLN_MAX_T[3]	s	
5634	uint	_I_SUM3_MAX_T	s	
5636	uint	_I_SUM_MAX_T	s	
5638	uint	_S_SUM3_MAX_T	s	
5640	uint	_S_SUM_MAX_T	s	
5642	uint	_THD_IL_MAX_T[0]	s	
5644	uint	_THD_IL_MAX_T[1]	s	
5646	uint	_THD_IL_MAX_T[2]	s	
5648	uint	_THD_IL_MAX_T[3]	s	
5650	uint	_ZHD_IL_MAX_T[0]	s	
5652	uint	_ZHD_IL_MAX_T[1]	s	
5654	uint	_ZHD_IL_MAX_T[2]	s	
5656	uint	_ZHD_IL_MAX_T[3]	s	
5658	uint	_ILN_CF_MAX_T[0]	s	
5660	uint	_ILN_CF_MAX_T[1]	s	
5662	uint	_ILN_CF_MAX_T[2]	s	
5664	uint	_ILN_CF_MAX_T[3]	s	
5666	uint	_IN_MAX_T	s	
5668	uint	_IM_MAX_T	s	
5670	uint	_IG_MAX_T	s	
5672	uint	_I_SYM_MAX_T	s	
5674	uint	_ILN_OVER_MAX_T[0]	s	
5676	uint	_ILN_OVER_MAX_T[1]	s	
5678	uint	_ILN_OVER_MAX_T[2]	s	

Address	Format	Designation	Unit	Note
5680	uint	_ILN_OVER_MAX_T[3]	s	
5682	uint	_ILN_UNDER_MAX_T[0]	s	
5684	uint	_ILN_UNDER_MAX_T[1]	s	
5686	uint	_ILN_UNDER_MAX_T[2]	s	
5688	uint	_ILN_UNDER_MAX_T[3]	s	
5690	uint	_ILN_NEG_PEAK_MAX_T[0]	s	
5692	uint	_ILN_NEG_PEAK_MAX_T[1]	s	
5694	uint	_ILN_NEG_PEAK_MAX_T[2]	s	
5696	uint	_ILN_NEG_PEAK_MAX_T[3]	s	
5698	uint	_ILN_POS_PEAK_MAX_T[0]	s	
5700	uint	_ILN_POS_PEAK_MAX_T[1]	s	
5702	uint	_ILN_POS_PEAK_MAX_T[2]	s	
5704	uint	_ILN_POS_PEAK_MAX_T[3]	s	
5706	uint	_ILN_PEAK_PEAK_MAX_T[0]	s	
5708	uint	_ILN_PEAK_PEAK_MAX_T[1]	s	
5710	uint	_ILN_PEAK_PEAK_MAX_T[2]	s	
5712	uint	_ILN_PEAK_PEAK_MAX_T[3]	s	
5714	uint	_FLI_PF5_MAX_T[0]	s	
5716	uint	_FLI_PF5_MAX_T[1]	s	
5718	uint	_FLI_PF5_MAX_T[2]	s	
5720	uint	_FLI_PF5_MAX_T[3]	s	
5722	uint	_FLI_ST_MAX_T[0]	s	
5724	uint	_FLI_ST_MAX_T[1]	s	
5726	uint	_FLI_ST_MAX_T[2]	s	
5728	uint	_FLI_ST_MAX_T[3]	s	
5730	uint	_FLI_LT_MAX_T[0]	s	
5732	uint	_FLI_LT_MAX_T[1]	s	
5734	uint	_FLI_LT_MAX_T[2]	s	
5736	uint	_FLI_LT_MAX_T[3]	s	
5738	uint	_ILN_RC_MAX_T[0]	s	
5740	uint	_ILN_RC_MAX_T[1]	s	
5742	uint	_ILN_RC_MAX_T[2]	s	
5744	uint	_ILN_RC_MAX_T[3]	s	
5746	uint	_ULLRC_MAX_T[0]	s	
5748	uint	_ULLRC_MAX_T[1]	s	
5750	uint	_ULLRC_MAX_T[2]	s	
5762	uint	_PFLN_MAX_T[0]	s	
5764	uint	_PFLN_MAX_T[1]	s	
5766	uint	_PFLN_MAX_T[2]	s	
5768	uint	_PFLN_MAX_T[3]	s	
5770	uint	_DLN_MAX_T[0]	s	
5772	uint	_DLN_MAX_T[1]	s	
5774	uint	_DLN_MAX_T[2]	s	
5776	uint	_DLN_MAX_T[3]	s	
5778	uint	_KFACT_MAX_T[0]	s	
5780	uint	_KFACT_MAX_T[1]	s	
5782	uint	_KFACT_MAX_T[2]	s	
5784	uint	_KFACT_MAX_T[3]	s	
5786	uint	_S0_POWER_MAX_T[0]	s	
5788	uint	_S0_POWER_MAX_T[1]	s	
5790	uint	_S0_POWER_MAX_T[2]	s	
5792	uint	_S0_POWER_MAX_T[3]	s	
5794	uint	_S0_POWER_MAX_T[4]	s	
5796	uint	_S0_POWER_MAX_T[5]	s	
5798	uint	_S0_POWER_MAX_T[6]	s	
5800	uint	_S0_POWER_MAX_T[7]	s	
5802	uint	_TEMPERATUR_MAX_T	s	Time of max. val. (UTC), internal temp.
6162	uint	_THD_ULN_AVG_MAX_T[0]	s	

Address	Format	Designation	Unit	Note
6164	uint	_THD_ULN_AVG_MAX_T[1]	s	
6166	uint	_THD_ULN_AVG_MAX_T[2]	s	
6168	uint	_THD_ULN_AVG_MAX_T[3]	s	
6170	uint	_ULN_AVG_MAX_T[0]	s	
6172	uint	_ULN_AVG_MAX_T[1]	s	
6174	uint	_ULN_AVG_MAX_T[2]	s	
6176	uint	_ULN_AVG_MAX_T[3]	s	
6178	uint	_ULL_AVG_MAX_T[0]	s	
6180	uint	_ULL_AVG_MAX_T[1]	s	
6182	uint	_ULL_AVG_MAX_T[2]	s	
6184	uint	_ULN_CF_AVG_MAX_T[0]	s	
6186	uint	_ULN_CF_AVG_MAX_T[1]	s	
6188	uint	_ULN_CF_AVG_MAX_T[2]	s	
6190	uint	_ULN_CF_AVG_MAX_T[3]	s	
6192	uint	_ULL_CF_AVG_MAX_T[0]	s	
6194	uint	_ULL_CF_AVG_MAX_T[1]	s	
6196	uint	_ULL_CF_AVG_MAX_T[2]	s	
6198	uint	_UN_AVG_MAX_T	s	
6200	uint	_UM_AVG_MAX_T	s	
6202	uint	_UG_AVG_MAX_T	s	
6204	uint	_URC_AVG_MAX_T[0]	s	
6206	uint	_URC_AVG_MAX_T[1]	s	
6208	uint	_URC_AVG_MAX_T[2]	s	
6210	uint	_URC_AVG_MAX_T[3]	s	
6212	uint	_THD_ULN_AVG_MAX_T[0]	s	
6214	uint	_THD_ULN_AVG_MAX_T[1]	s	
6216	uint	_THD_ULN_AVG_MAX_T[2]	s	
6218	uint	_THD_ULN_AVG_MAX_T[3]	s	
6220	uint	_THD_ZLN_AVG_MAX_T[0]	s	
6222	uint	_THD_ZLN_AVG_MAX_T[1]	s	
6224	uint	_THD_ZLN_AVG_MAX_T[2]	s	
6226	uint	_THD_ZLN_AVG_MAX_T[3]	s	
6228	uint	_ULN_OVER_AVG_MAX_T[0]	s	
6230	uint	_ULN_OVER_AVG_MAX_T[1]	s	
6232	uint	_ULN_OVER_AVG_MAX_T[2]	s	
6234	uint	_ULN_OVER_AVG_MAX_T[3]	s	
6236	uint	_ULN_UNDER_AVG_MAX_T[0]	s	
6238	uint	_ULN_UNDER_AVG_MAX_T[1]	s	
6240	uint	_ULN_UNDER_AVG_MAX_T[2]	s	
6242	uint	_ULN_UNDER_AVG_MAX_T[3]	s	
6244	uint	_ULN_NEG_PEAK_AVG_MAX_T[0]	s	
6246	uint	_ULN_NEG_PEAK_AVG_MAX_T[1]	s	
6248	uint	_ULN_NEG_PEAK_AVG_MAX_T[2]	s	
6250	uint	_ULN_NEG_PEAK_AVG_MAX_T[3]	s	
6252	uint	_ULN_POS_PEAK_AVG_MAX_T[0]	s	
6254	uint	_ULN_POS_PEAK_AVG_MAX_T[1]	s	
6256	uint	_ULN_POS_PEAK_AVG_MAX_T[2]	s	
6258	uint	_ULN_POS_PEAK_AVG_MAX_T[3]	s	
6260	uint	_ULN_PEAK_PEAK_AVG_MAX_T[0]	s	
6262	uint	_ULN_PEAK_PEAK_AVG_MAX_T[1]	s	
6264	uint	_ULN_PEAK_PEAK_AVG_MAX_T[2]	s	
6266	uint	_ULN_PEAK_PEAK_AVG_MAX_T[3]	s	
6268	uint	_THD_ULL_AVG_MAX_T[0]	s	
6270	uint	_THD_ULL_AVG_MAX_T[1]	s	
6272	uint	_THD_ULL_AVG_MAX_T[2]	s	
6274	uint	_THD_ZLL_AVG_MAX_T[0]	s	
6276	uint	_THD_ZLL_AVG_MAX_T[1]	s	
6278	uint	_THD_ZLL_AVG_MAX_T[2]	s	
6280	uint	_ULL_OVER_AVG_MAX_T[0]	s	

Address	Format	Designation	Unit	Note
6282	uint	_ULL_OVER_AVG_MAX_T[1]	S	
6284	uint	_ULL_OVER_AVG_MAX_T[2]	S	
6286	uint	_ULL_UNDER_AVG_MAX_T[0]	S	
6288	uint	_ULL_UNDER_AVG_MAX_T[1]	S	
6290	uint	_ULL_UNDER_AVG_MAX_T[2]	S	
6292	uint	_ULL_NEG_PEAK_AVG_MAX_T[0]	S	
6294	uint	_ULL_NEG_PEAK_AVG_MAX_T[1]	S	
6296	uint	_ULL_NEG_PEAK_AVG_MAX_T[2]	S	
6298	uint	_ULL_POS_PEAK_AVG_MAX_T[0]	S	
6300	uint	_ULL_POS_PEAK_AVG_MAX_T[1]	S	
6302	uint	_ULL_POS_PEAK_AVG_MAX_T[2]	S	
6304	uint	_ULL_PEAK_PEAK_AVG_MAX_T[0]	S	
6306	uint	_ULL_PEAK_PEAK_AVG_MAX_T[1]	S	
6308	uint	_ULL_PEAK_PEAK_AVG_MAX_T[2]	S	
6310	uint	_U_STERN_AVG_MAX_T	S	
6312	uint	_U_SYM_AVG_MAX_T	S	
6314	uint	_FREQ_AVG_MAX_T	S	
6316	uint	_NORM_FREQ_AVG_MAX_T	S	
6318	uint	_PLN_AVG_MAX_T[0]	S	
6320	uint	_PLN_AVG_MAX_T[1]	S	
6322	uint	_PLN_AVG_MAX_T[2]	S	
6324	uint	_PLN_AVG_MAX_T[3]	S	
6326	uint	_P_SUM_AVG_MAX_T	S	
6328	uint	_Q_SUM_AVG_MAX_T	S	
6330	uint	_QLN_AVG_MAX_T[0]	S	
6332	uint	_QLN_AVG_MAX_T[1]	S	
6334	uint	_QLN_AVG_MAX_T[2]	S	
6336	uint	_QLN_AVG_MAX_T[3]	S	
6338	uint	_P_SUM3_AVG_MAX_T	S	
6340	uint	_Q_SUM3_AVG_MAX_T	S	
6342	uint	_ILN_AVG_MAX_T[0]	S	
6344	uint	_ILN_AVG_MAX_T[1]	S	
6346	uint	_ILN_AVG_MAX_T[2]	S	
6348	uint	_ILN_AVG_MAX_T[3]	S	
6350	uint	_SLN_AVG_MAX_T[0]	S	
6352	uint	_SLN_AVG_MAX_T[1]	S	
6354	uint	_SLN_AVG_MAX_T[2]	S	
6356	uint	_SLN_AVG_MAX_T[3]	S	
6358	uint	_I_SUM3_AVG_MAX_T	S	
6360	uint	_I_SUM_AVG_MAX_T	S	
6362	uint	_S_SUM3_AVG_MAX_T	S	
6364	uint	_S_SUM_AVG_MAX_T	S	
6366	uint	_THD_IL_AVG_MAX_T[0]	S	
6368	uint	_THD_IL_AVG_MAX_T[1]	S	
6370	uint	_THD_IL_AVG_MAX_T[2]	S	
6372	uint	_THD_IL_AVG_MAX_T[3]	S	
6374	uint	_ZHD_IL_AVG_MAX_T[0]	S	
6376	uint	_ZHD_IL_AVG_MAX_T[1]	S	
6378	uint	_ZHD_IL_AVG_MAX_T[2]	S	
6380	uint	_ZHD_IL_AVG_MAX_T[3]	S	
6382	uint	_ILN_CF_AVG_MAX_T[0]	S	
6384	uint	_ILN_CF_AVG_MAX_T[1]	S	
6386	uint	_ILN_CF_AVG_MAX_T[2]	S	
6388	uint	_ILN_CF_AVG_MAX_T[3]	S	
6390	uint	_IN_AVG_MAX_T	S	
6392	uint	_IM_AVG_MAX_T	S	
6394	uint	_IG_AVG_MAX_T	S	
6396	uint	_I_SYM_AVG_MAX_T	S	
6398	uint	_ILN_OVER_AVG_MAX_T[0]	S	

Address	Format	Designation	Unit	Note
6400	uint	_ILN_OVER_AVG_MAX_T[1]	s	
6402	uint	_ILN_OVER_AVG_MAX_T[2]	s	
6404	uint	_ILN_OVER_AVG_MAX_T[3]	s	
6406	uint	_ILN_UNDER_AVG_MAX_T[0]	s	
6408	uint	_ILN_UNDER_AVG_MAX_T[1]	s	
6410	uint	_ILN_UNDER_AVG_MAX_T[2]	s	
6412	uint	_ILN_UNDER_AVG_MAX_T[3]	s	
6414	uint	_ILN_NEG_PEAK_AVG_MAX_T[0]	s	
6416	uint	_ILN_NEG_PEAK_AVG_MAX_T[1]	s	
6418	uint	_ILN_NEG_PEAK_AVG_MAX_T[2]	s	
6420	uint	_ILN_NEG_PEAK_AVG_MAX_T[3]	s	
6422	uint	_ILN_POS_PEAK_AVG_MAX_T[0]	s	
6424	uint	_ILN_POS_PEAK_AVG_MAX_T[1]	s	
6426	uint	_ILN_POS_PEAK_AVG_MAX_T[2]	s	
6428	uint	_ILN_POS_PEAK_AVG_MAX_T[3]	s	
6430	uint	_ILN_PEAK_PEAK_AVG_MAX_T[0]	s	
6432	uint	_ILN_PEAK_PEAK_AVG_MAX_T[1]	s	
6434	uint	_ILN_PEAK_PEAK_AVG_MAX_T[2]	s	
6436	uint	_ILN_PEAK_PEAK_AVG_MAX_T[3]	s	
6438	uint	_FLI_PF5_AVG_MAX_T[0]	s	
6440	uint	_FLI_PF5_AVG_MAX_T[1]	s	
6442	uint	_FLI_PF5_AVG_MAX_T[2]	s	
6444	uint	_FLI_PF5_AVG_MAX_T[3]	s	
6446	uint	_FLI_ST_AVG_MAX_T[0]	s	
6448	uint	_FLI_ST_AVG_MAX_T[1]	s	
6450	uint	_FLI_ST_AVG_MAX_T[2]	s	
6452	uint	_FLI_ST_AVG_MAX_T[3]	s	
6454	uint	_FLI_LT_AVG_MAX_T[0]	s	
6456	uint	_FLI_LT_AVG_MAX_T[1]	s	
6458	uint	_FLI_LT_AVG_MAX_T[2]	s	
6460	uint	_FLI_LT_AVG_MAX_T[3]	s	
6462	uint	_ILN_RC_AVG_MAX_T[0]	s	
6464	uint	_ILN_RC_AVG_MAX_T[1]	s	
6466	uint	_ILN_RC_AVG_MAX_T[2]	s	
6468	uint	_ILN_RC_AVG_MAX_T[3]	s	
6470	uint	_ULLRC_AVG_MAX_T[0]	s	
6472	uint	_ULLRC_AVG_MAX_T[1]	s	
6474	uint	_ULLRC_AVG_MAX_T[2]	s	
6486	uint	_PFLN_AVG_MAX_T[0]	s	
6488	uint	_PFLN_AVG_MAX_T[1]	s	
6490	uint	_PFLN_AVG_MAX_T[2]	s	
6492	uint	_PFLN_AVG_MAX_T[3]	s	
6494	uint	_DLN_AVG_MAX_T[0]	s	
6496	uint	_DLN_AVG_MAX_T[1]	s	
6498	uint	_DLN_AVG_MAX_T[2]	s	
6500	uint	_DLN_AVG_MAX_T[3]	s	
6502	uint	_KFACT_AVG_MAX_T[0]	s	
6504	uint	_KFACT_AVG_MAX_T[1]	s	
6506	uint	_KFACT_AVG_MAX_T[2]	s	
6508	uint	_KFACT_AVG_MAX_T[3]	s	
6510	uint	_S0_POWER_AVG_MAX_T[0]	s	
6512	uint	_S0_POWER_AVG_MAX_T[1]	s	
6514	uint	_S0_POWER_AVG_MAX_T[2]	s	
6516	uint	_S0_POWER_AVG_MAX_T[3]	s	
6518	uint	_S0_POWER_AVG_MAX_T[4]	s	
6520	uint	_S0_POWER_AVG_MAX_T[5]	s	
6522	uint	_S0_POWER_AVG_MAX_T[6]	s	
6524	uint	_S0_POWER_AVG_MAX_T[7]	s	
6526	uint	_TEMPERATUR_AVG_MAX_T	s	Time of max. val. of aver. val. (UTC), internal temp.

Address	Format	Designation	Unit	Note
<b>Maximum values of mean values (float type)</b>				
5804	float	_ULN_AVG_MAX[0]	V	
5806	float	_ULN_AVG_MAX[1]	V	
5808	float	_ULN_AVG_MAX[2]	V	
5810	float	_ULN_AVG_MAX[3]	V	
5812	float	_ULL_AVG_MAX[0]	V	
5814	float	_ULL_AVG_MAX[1]	V	
5816	float	_ULL_AVG_MAX[2]	V	
5818	float	_ULN_CF_AVG_MAX[0]	-	
5820	float	_ULN_CF_AVG_MAX[1]	-	
5822	float	_ULN_CF_AVG_MAX[2]	-	
5824	float	_ULN_CF_AVG_MAX[3]	-	
5826	float	_ULL_CF_AVG_MAX[0]	-	
5828	float	_ULL_CF_AVG_MAX[1]	-	
5830	float	_ULL_CF_AVG_MAX[2]	-	
5832	float	_UN_AVG_MAX	V	
5834	float	_UM_AVG_MAX	V	
5836	float	_UG_AVG_MAX	V	
5838	float	_URC_AVG_MAX[0]	V	
5840	float	_URC_AVG_MAX[1]	V	
5842	float	_URC_AVG_MAX[2]	V	
5844	float	_URC_AVG_MAX[3]	V	
5846	float	_THD_ULN_AVG_MAX[0]	%	
5848	float	_THD_ULN_AVG_MAX[1]	%	
5850	float	_THD_ULN_AVG_MAX[2]	%	
5852	float	_THD_ULN_AVG_MAX[3]	%	
5854	float	_THD_ZLN_AVG_MAX[0]	%	
5856	float	_THD_ZLN_AVG_MAX[1]	%	
5858	float	_THD_ZLN_AVG_MAX[2]	%	
5860	float	_THD_ZLN_AVG_MAX[3]	%	
5862	float	_ULN_OVER_AVG_MAX[0]	%	
5864	float	_ULN_OVER_AVG_MAX[1]	%	
5866	float	_ULN_OVER_AVG_MAX[2]	%	
5868	float	_ULN_OVER_AVG_MAX[3]	%	
5870	float	_ULN_UNDER_AVG_MAX[0]	%	
5872	float	_ULN_UNDER_AVG_MAX[1]	%	
5874	float	_ULN_UNDER_AVG_MAX[2]	%	
5876	float	_ULN_UNDER_AVG_MAX[3]	%	
5878	float	_ULN_NEG_PEAK_AVG_MAX[0]	V	
5880	float	_ULN_NEG_PEAK_AVG_MAX[1]	V	
5882	float	_ULN_NEG_PEAK_AVG_MAX[2]	V	
5884	float	_ULN_NEG_PEAK_AVG_MAX[3]	V	
5886	float	_ULN_POS_PEAK_AVG_MAX[0]	V	
5888	float	_ULN_POS_PEAK_AVG_MAX[1]	V	
5890	float	_ULN_POS_PEAK_AVG_MAX[2]	V	
5892	float	_ULN_POS_PEAK_AVG_MAX[3]	V	
5894	float	_ULN_PEAK_PEAK_AVG_MAX[0]	V	
5896	float	_ULN_PEAK_PEAK_AVG_MAX[1]	V	
5898	float	_ULN_PEAK_PEAK_AVG_MAX[2]	V	
5900	float	_ULN_PEAK_PEAK_AVG_MAX[3]	V	
5902	float	_THD_ULL_AVG_MAX[0]	%	
5904	float	_THD_ULL_AVG_MAX[1]	%	
5906	float	_THD_ULL_AVG_MAX[2]	%	
5908	float	_THD_ZLL_AVG_MAX[0]	%	
5910	float	_THD_ZLL_AVG_MAX[1]	%	
5912	float	_THD_ZLL_AVG_MAX[2]	%	
5914	float	_ULL_OVER_AVG_MAX[0]	%	
5916	float	_ULL_OVER_AVG_MAX[1]	%	
5918	float	_ULL_OVER_AVG_MAX[2]	%	



Address	Format	Designation	Unit	Note
5920	float	_ULL_UNDER_AVG_MAX[0]	%	
5922	float	_ULL_UNDER_AVG_MAX[1]	%	
5924	float	_ULL_UNDER_AVG_MAX[2]	%	
5926	float	_ULL_NEG_PEAK_AVG_MAX[0]	V	
5928	float	_ULL_NEG_PEAK_AVG_MAX[1]	V	
5930	float	_ULL_NEG_PEAK_AVG_MAX[2]	V	
5932	float	_ULL_POS_PEAK_AVG_MAX[0]	V	
5934	float	_ULL_POS_PEAK_AVG_MAX[1]	V	
5936	float	_ULL_POS_PEAK_AVG_MAX[2]	V	
5938	float	_ULL_PEAK_PEAK_AVG_MAX[0]	V	
5940	float	_ULL_PEAK_PEAK_AVG_MAX[1]	V	
5942	float	_ULL_PEAK_PEAK_AVG_MAX[2]	V	
5944	float	_U_STERN_AVG_MAX	V	
5946	float	_U_SYM_AVG_MAX	%	
5948	float	_FREQ_AVG_MAX	Hz	
5950	float	_NORM_FREQ_AVG_MAX	Hz	
5952	float	_PLN_AVG_MAX[0]	W	
5954	float	_PLN_AVG_MAX[1]	W	
5956	float	_PLN_AVG_MAX[2]	W	
5958	float	_PLN_AVG_MAX[3]	W	
5960	float	_P_SUM_AVG_MAX	W	
5962	float	_Q_SUM_AVG_MAX	var	
5964	float	_QLN_AVG_MAX[0]	var	
5966	float	_QLN_AVG_MAX[1]	var	
5968	float	_QLN_AVG_MAX[2]	var	
5970	float	_QLN_AVG_MAX[3]	var	
5972	float	_P_SUM3_AVG_MAX	W	
5974	float	_Q_SUM3_AVG_MAX	var	
5976	float	_ILN_AVG_MAX[0]	A	
5978	float	_ILN_AVG_MAX[1]	A	
5980	float	_ILN_AVG_MAX[2]	A	
5982	float	_ILN_AVG_MAX[3]	A	
5984	float	_SLN_AVG_MAX[0]	VA	
5986	float	_SLN_AVG_MAX[1]	VA	
5988	float	_SLN_AVG_MAX[2]	VA	
5990	float	_SLN_AVG_MAX[3]	VA	
5992	float	_I_SUM3_AVG_MAX	A	
5994	float	_I_SUM_AVG_MAX	A	
5996	float	_S_SUM3_AVG_MAX	VA	
5998	float	_S_SUM_AVG_MAX	VA	
6000	float	_THD_IL_AVG_MAX[0]	%	
6002	float	_THD_IL_AVG_MAX[1]	%	
6004	float	_THD_IL_AVG_MAX[2]	%	
6006	float	_THD_IL_AVG_MAX[3]	%	
6008	float	_ZHD_IL_AVG_MAX[0]	%	
6010	float	_ZHD_IL_AVG_MAX[1]	%	
6012	float	_ZHD_IL_AVG_MAX[2]	%	
6014	float	_ZHD_IL_AVG_MAX[3]	%	
6016	float	_ILN_CF_AVG_MAX[0]	-	
6018	float	_ILN_CF_AVG_MAX[1]	-	
6020	float	_ILN_CF_AVG_MAX[2]	-	
6022	float	_ILN_CF_AVG_MAX[3]	-	
6024	float	_IN_AVG_MAX	A	
6026	float	_IM_AVG_MAX	A	
6028	float	_IG_AVG_MAX	A	
6030	float	_I_SYM_AVG_MAX	%	
6032	float	_ILN_OVER_AVG_MAX[0]	%	
6034	float	_ILN_OVER_AVG_MAX[1]	%	
6036	float	_ILN_OVER_AVG_MAX[2]	%	

Address	Format	Designation	Unit	Note
6038	float	_ILN_OVER_AVG_MAX[3]	%	
6040	float	_ILN_UNDER_AVG_MAX[0]	%	
6042	float	_ILN_UNDER_AVG_MAX[1]	%	
6044	float	_ILN_UNDER_AVG_MAX[2]	%	
6046	float	_ILN_UNDER_AVG_MAX[3]	%	
6048	float	_ILN_NEG_PEAK_AVG_MAX[0]	A	
6050	float	_ILN_NEG_PEAK_AVG_MAX[1]	A	
6052	float	_ILN_NEG_PEAK_AVG_MAX[2]	A	
6054	float	_ILN_NEG_PEAK_AVG_MAX[3]	A	
6056	float	_ILN_POS_PEAK_AVG_MAX[0]	A	
6058	float	_ILN_POS_PEAK_AVG_MAX[1]	A	
6060	float	_ILN_POS_PEAK_AVG_MAX[2]	A	
6062	float	_ILN_POS_PEAK_AVG_MAX[3]	A	
6064	float	_ILN_PEAK_PEAK_AVG_MAX[0]	A	
6066	float	_ILN_PEAK_PEAK_AVG_MAX[1]	A	
6068	float	_ILN_PEAK_PEAK_AVG_MAX[2]	A	
6070	float	_ILN_PEAK_PEAK_AVG_MAX[3]	A	
6072	float	_FLI_PF5_AVG_MAX[0]		
6074	float	_FLI_PF5_AVG_MAX[1]		
6076	float	_FLI_PF5_AVG_MAX[2]		
6078	float	_FLI_PF5_AVG_MAX[3]		
6080	float	_FLI_ST_AVG_MAX[0]		
6082	float	_FLI_ST_AVG_MAX[1]		
6084	float	_FLI_ST_AVG_MAX[2]		
6086	float	_FLI_ST_AVG_MAX[3]		
6088	float	_FLI_LT_AVG_MAX[0]		
6090	float	_FLI_LT_AVG_MAX[1]		
6092	float	_FLI_LT_AVG_MAX[2]		
6094	float	_FLI_LT_AVG_MAX[3]		
6096	float	_ILN_RC_AVG_MAX[0]	A	
6098	float	_ILN_RC_AVG_MAX[1]	A	
6100	float	_ILN_RC_AVG_MAX[2]	A	
6102	float	_ILN_RC_AVG_MAX[3]	A	
6104	float	_ULLRC_AVG_MAX[0]	V	
6106	float	_ULLRC_AVG_MAX[1]	V	
6108	float	_ULLRC_AVG_MAX[2]	V	
6120	float	_PFLN_AVG_MAX[0]	%	
6122	float	_PFLN_AVG_MAX[1]	%	
6124	float	_PFLN_AVG_MAX[2]	%	
6126	float	_PFLN_AVG_MAX[3]	%	
6128	float	_DLN_AVG_MAX[0]	var	
6130	float	_DLN_AVG_MAX[1]	var	
6132	float	_DLN_AVG_MAX[2]	var	
6134	float	_DLN_AVG_MAX[3]	var	
6136	float	_KFACT_AVG_MAX[0]		
6138	float	_KFACT_AVG_MAX[1]		
6140	float	_KFACT_AVG_MAX[2]		
6142	float	_KFACT_AVG_MAX[3]		
6144	float	_S0_POWER_AVG_MAX[0]	W	
6146	float	_S0_POWER_AVG_MAX[1]	W	
6148	float	_S0_POWER_AVG_MAX[2]	W	
6150	float	_S0_POWER_AVG_MAX[3]	W	
6152	float	_S0_POWER_AVG_MAX[4]	W	
6154	float	_S0_POWER_AVG_MAX[5]	W	
6156	float	_S0_POWER_AVG_MAX[6]	W	
6158	float	_S0_POWER_AVG_MAX[7]	W	
6160	float	_TEMPERATUR_AVG_MAX	°C	

Address	Format	Designation	Unit	Note
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Address	Format	Designation	Unit	Note
<b>Other values</b>				
6706	float	_SPU012	V	Star connection voltage
6708	short	_DIGOUT_STAT[0]	n	Status Digital Output 1 0=not active, 1=active
6709	short	_DIGOUT_STAT[1]	n	Status Digital Output 2
6710	short	_DIGOUT_STAT[2]	n	Status Digital Output 3
6711	short	_DIGOUT_STAT[3]	n	Status Digital Output 4
6712	short	_DIGOUT_STAT[4]	n	Status Digital Output 5
6713	short	_DIGIN_STAT[0]	n	Status Digital Input 1 0=not active, 1=active
6714	short	_DIGIN_STAT[1]	n	Status Digital Input 2
6715	short	_DIGIN_STAT[2]	n	Status Digital Input 3
6716	short	_DIGIN_STAT[3]	n	Status Digital Input 4
6717	short	_DIGIN_STAT[4]	n	Status Digital Input 5
6718	short	_DIGIN_STAT[5]	n	Status Digital Input 6
6719	short	_DIGIN_STAT[6]	n	Status Digital Input 7
6720	short	_DIGIN_STAT[7]	n	Status Digital Input 8
6721	uint	_EVT_COUNT	n	Event counter
6723	uint	_FLAG_COUNT	n	Flag counter
6725	uint	_TRANS_COUNT	n	Error counter, transients
6727	uint	_HWW_COUNT	n	Error counter, half-cycle effective values
6729	uint	_RX232_COUNT	n	Error counter, receive RS232
6731	uint	_TX232_COUNT	n	Error counter, send RS232
6733	uint	_ERR232_COUNT	n	Error counter, RS232
6735	uint	_RX485_COUNT	n	Error counter, receive RS485
6737	uint	_TX485_COUNT	n	Error counter, send RS485
6739	uint	_ERR485_COUNT	n	Error counter, RS485
6741	short	_DEL_WH		1= Delete all real energy counters
6742	short	_DEL_QH		1= Delete all reactive energy counters
6743	short	_INIT_MAX		Only for internal use
6744	string	_RUN	64	Only for internal use
6776	float	_CTPRIM[0]	A	L1, L2, L3; Current transformer, primary
6778	float	_CTPRIM[1]	A	L4; Current transformer, primary
6780	float	_CTSEC[0]	A	L1, L2, L3; Current transf., secondary
6782	float	_CTSEC[1]	A	L4; Current transformer, secondary
6784	float	_VTPRIM[0]	V	L1, L2, L3; Voltage transformer, primary
6786	float	_VTPRIM[1]	V	L4; Voltage transformer, primary
6788	float	_VTSEC[0]	V	L1, L2, L3; Voltage transformer, primary
6790	float	_VTSEC[1]	V	L4; Voltage transformer, primary
6792	float	_IRATED[0]	A	Nominal current transf.; I L1, I L2, I L3
6794	float	_IRATED[1]	A	Nominal current transformer; I L4
6796	float	_NOMINAL_U[0]	V	Nominal voltage; L1, L2, L3
6798	float	_NOMINAL_U[1]	V	Nominal voltage; L4
6800	float	_NOMINAL_I[0]	A	Nominal current; L1, L2, L3
6802	float	_NOMINAL_I[1]	A	Nominal current; L4
6804	float	_TRNS_DELTA[0]	%	Only for internal use
6806	float	_TRNS_DELTA[1]	%	Only for internal use
6808	float	_TRNS_I_ABS[0]	%	Only for internal use
6810	float	_TRNS_I_ABS[1]	%	Only for internal use
6812	float	_TRNS_U_ABS[0]	%	Only for internal use
6814	float	_TRNS_U_ABS[1]	%	Only for internal use
6816	float	_I_EVT_MAX[0]	%	Only for internal use
6818	float	_I_EVT_MAX[1]	%	Only for internal use
6820	float	_U_EVT_MAX[0]	%	Only for internal use
6822	float	_U_EVT_MAX[1]	%	Only for internal use
6824	float	_U_EVT_MIN[0]	%	Only for internal use
6826	float	_U_EVT_MIN[1]	%	Only for internal use
6828	float	_U_EVT_OFF[0]	%	Only for internal use
6830	float	_U_EVT_OFF[1]	%	Only for internal use

Address	Format	Designation	Unit	Note
6832	float	_NOMINAL_F	Hz	Nominal frequency 50Hz or 60Hz
6834	short	_FLICKER_SYSTEM		Only for internal use
6835	short	_TRNS_PRE	n	Only for internal use
6836	short	_TRNS_POST	n	Only for internal use
6837	string	_DEV_NAME	64	Only for internal use
6869	string	_DEV_DESC	128	Only for internal use
6933	string	_LANGUAGE	16	Only for internal use
6941	int	_DISP_LANGUAGE		Only for internal use
6943	uint	_SERNR		Only for internal use
6945	uint	_PRODNR		Only for internal use
6947	int	_MBUSADDR		Only for internal use
6949	int	_MODE485		Only for internal use
6951	int	_BAUD485		Only for internal use
6953	uint	_IP_ADDR		Network address
6955	uint	_IP_MASK		Network mask
6957	uint	_IP_GATE		Gateway
6959	int	_DHCPMODE		Bootp = 1; off = 0; DHCP = 2
6961	int	_BRIGHTNESS		Brightness display
6963	short	_STBY_TIME		Standby time
6964	short	_STBY_CONTRAST		Standby contrast
6965	short	_SCREENSASVE		Only for internal use
6966	short	_DISP_SPEED		Display change time
6967	short	_DISP_ROT		0= Autom. display change on
6968	short	_ROT_TIME		Rotation time display
6969	int	_KEY1		Status button 1
6971	int	_KEY2		Status button 2
6973	int	_KEY3		Status button 3
6975	int	_KEY4		Status button 4
6977	int	_KEY5		Status button 5
6979	int	_KEY6		Status button 6
6981	uint	_DEBUG_IP		Only for internal use
6983	int	_TIME_ZONE	s	Time zone
6985	int	_STIME	s	Only for internal use
6987	short	_SDAY		Start day of summer/winter switchover.
6988	short	_SHOUR	h	
6989	short	_SMON		
6990	short	_SMIN	min	
6991	short	_SDOW		Summer/wintertime switchover
6992	short	_EDAY		
6993	short	_Ehour	h	
6994	short	_EMON		
6995	short	_EMIN	min	
6996	short	_EDOW		
6997	short	_KALIB_I[0]		Only for internal use
6998	short	_KALIB_I[1]		Only for internal use
6999	short	_KALIB_I[2]		Only for internal use
7000	short	_KALIB_I[3]		Only for internal use
7001	short	_KALIB_U[0]		Only for internal use
7002	short	_KALIB_U[1]		Only for internal use
7003	short	_KALIB_U[2]		Only for internal use
7004	short	_KALIB_U[3]		Only for internal use
7005	short	_EVT_VAL_PRE	n	Only for internal use
7006	short	_EVT_VAL_POST	n	Only for internal use
7007	int	_TRNS_MODE	n	Only for internal use
7009	int	_EVT_MODE	n	Only for internal use
7011	int	_CON_AUX_MODE	n	Only for internal use
7013	int	_CON_MODE	n	Only for internal use
7015	int	_PHASE_MODE	n	Only for internal use
7017	short	_COLOR[0]	n	Only for internal use

Address	Format	Designation	Unit	Note
7018	short	_COLOR[1]	n	Only for internal use
7019	short	_COLOR[2]	n	Only for internal use
7020	short	_COLOR[3]	n	Only for internal use
7021	short	_COLOR[4]	n	Only for internal use
7022	short	_COLOR[5]	n	Only for internal use
7023	short	_COLOR[6]	n	Only for internal use
7024	short	_COLOR[7]	n	Only for internal use
7025	string	_MMENU	32	Only for internal use
7041	string	_LANG	32	Only for internal use
7057	string	_COMM	32	Only for internal use
7073	string	_MMEAS	32	Only for internal use
7089	string	_AMEAS	32	Only for internal use
7105	string	_RECOS	32	Only for internal use
7121	string	_SYST	32	Only for internal use
7137	string	_DISPM	32	Only for internal use
7153	string	_COLORM	32	Only for internal use
7169	string	_FB_BAUD	32	Only for internal use
7185	string	_TCPIP	32	Only for internal use
7201	string	_COMMENU	32	Only for internal use
7217	string	_DHCP	32	Only for internal use
7233	string	_IPNO	32	Only for internal use
7249	string	_NETMASK	32	Only for internal use
7265	string	_GATEWAY	32	Only for internal use
7281	string	_FIELDBUS	32	Only for internal use
7297	string	_COMPORT	32	Only for internal use
7313	string	_PROTOCOL	32	Only for internal use
7329	string	_FBPROT_0	32	Only for internal use
7345	string	_FBPROT_1	32	Only for internal use
7361	string	_FBPROT_2	32	Only for internal use
7377	string	_FB_ADDR	32	Only for internal use
7393	string	_DHCP_0	32	Only for internal use
7409	string	_DHCP_1	32	Only for internal use
7425	string	_DHCP_2	32	Only for internal use
7441	string	_MCIRC	32	Only for internal use
7457	string	_RVOLT	32	Only for internal use
7473	string	_MRF	32	Only for internal use
7489	string	_MVT	32	Only for internal use
7505	string	_MCT	32	Only for internal use
7521	string	_MRV	32	Only for internal use
7537	string	_MRC	32	Only for internal use
7553	string	_FLISYS	32	Only for internal use
7569	string	_EVENTS	32	Only for internal use
7585	string	_TRANS	32	Only for internal use
7601	string	_COLOR	32	Only for internal use
7617	string	_CURRENT	32	Only for internal use
7633	string	_VOLTAGE	32	Only for internal use
7649	string	_RMSLOW	32	Only for internal use
7665	string	_RMSHIGH	32	Only for internal use
7681	string	_RMSINTR	32	Only for internal use
7697	string	_RMSIMAX	32	Only for internal use
7713	string	_OFF_STR	32	Only for internal use
7729	string	_TRNSUPK	32	Only for internal use
7745	string	_TRNSUTR	32	Only for internal use
7761	string	_TRNSIPK	32	Only for internal use
7777	string	_STOPR	32	Only for internal use
7793	string	_STARTR1	32	Only for internal use
7809	string	_STARTR2	32	Only for internal use
7825	string	_DELWORK	32	Only for internal use
7841	string	_L_BRIGHTNESS	32	Only for internal use

Address	Format	Designation	Unit	Note
7857	string	_STANDBY	32	Only for internal use
7873	string	_BRIGHTNESS_LOW	32	Only for internal use
7889	string	_SCREENSAVE	32	Only for internal use
7905	string	_DISP_MODE	32	Only for internal use
7921	string	_ROTATE	32	Only for internal use
7937	string	_ROTATE_TIME	32	Only for internal use
7953	string	_SPEED_LOW	32	Only for internal use
7969	string	_SPEED_HIGH	32	Only for internal use
7985	string	_RECNO	32	Only for internal use
8001	string	_NO_STR	32	Only for internal use
8017	string	_YES_STR	32	Only for internal use
8033	string	_DONE_STR	32	Only for internal use
8049	string	_RUN_STR	32	Only for internal use
8065	string	_STOP_STR	32	Only for internal use
8081	string	_VERSION	32	Only for internal use
8097	string	_IDNO	32	Only for internal use
8113	string	_SERNO	32	Only for internal use
8129	string	_MACADR	32	Only for internal use
8145	string	_PASSW	32	Only for internal use
8161	string	_LANGUAGE_1	32	Only for internal use
8177	string	_LANGUAGE_2	32	Only for internal use
8193	string	_LANGUAGE_3	32	Only for internal use
8209	string	_LANGUAGE_4	32	Only for internal use
8225	string	_LANGUAGE_5	32	Only for internal use
8241	string	_LANGUAGE_6	32	Only for internal use
8257	string	_LANGUAGE_7	32	Only for internal use
8273	string	_LANGUAGE_8	32	Only for internal use
8289	string	_LANGUAGE_9	32	Only for internal use
8305	string	_LANGUAGE_10	32	Only for internal use
8321	string	_LANGUAGE_11	32	Only for internal use
8337	string	_LANGUAGE_12	32	Only for internal use
8353	string	_LANGUAGE_13	32	Only for internal use
8369	string	_LANGUAGE_14	32	Only for internal use
8385	string	_LANGUAGE_15	32	Only for internal use
8401	string	_LANGUAGE_16	32	Only for internal use
8417	string	_GUEST_PASSWD	64	Password, guest
8449	string	_USER_PASSWD	64	Password, user
8481	string	_ADMIN_PASSWD	64	Password, admin
8513	float	_PULSWERT[0]	Wh/n	Pulse value for input 1
8515	float	_PULSWERT[1]	Wh/n	Pulse value for input 2
8517	float	_PULSWERT[2]	Wh/n	Pulse value for input 3
8519	float	_PULSWERT[3]	Wh/n	Pulse value for input 4
8521	float	_PULSWERT[4]	Wh/n	Pulse value for input 5
8523	float	_PULSWERT[5]	Wh/n	Pulse value for input 6
8525	float	_PULSWERT[6]	Wh/n	Pulse value for input 7
8527	float	_PULSWERT[7]	Wh/n	Pulse value for input 8
8529	float	_MAXSIZE_REC	%	Only for internal use
8531	float	_MAXSIZE_TRNS	%	Only for internal use
8533	float	_MAXSIZE_VWW	%	Only for internal use
8535	float	_MAXSIZE_EVT	%	Only for internal use
8537	float	_MAXSIZE_FLAGS	%	Only for internal use
8539	int	_TFTP_FILE_NR	n	Only for internal use
8541	int	_TFTP_NEWFILE	n	Only for internal use
8543	int	_DIGOUTEVT[0]	bin	Only for internal use
8545	int	_DIGOUTEVT[1]	bin	Only for internal use
8547	int	_DIGOUTEVT[2]	bin	Only for internal use
8549	int	_DIGOUTEVT[3]	bin	Only for internal use
8551	int	_DIGOUTEVT[4]	bin	Only for internal use
8553	int	_DIGOUTEVT_TIME[0]	0.01s	Only for internal use

Address	Format	Designation	Unit	Note
8555	int	_DIGOUTEVT_TIME[1]	0.01s	Only for internal use
8557	int	_DIGOUTEVT_TIME[2]	0.01s	Only for internal use
8559	int	_DIGOUTEVT_TIME[3]	0.01s	Only for internal use
8561	int	_DIGOUTEVT_TIME[4]	0.01s	Only for internal use
8563	short	_INVERT_DIGOUT[0]	bool	Only for internal use
8564	short	_INVERT_DIGOUT[1]	bool	Only for internal use
8565	short	_INVERT_DIGOUT[2]	bool	Only for internal use
8566	short	_INVERT_DIGOUT[3]	bool	Only for internal use
8567	short	_INVERT_DIGOUT[4]	bool	Only for internal use
10370	int	_KORR_INT		Only for internal use
10372	int	_QUARZ_KORR_NTP	ppm	Only for internal use
10374	float	_RC_FREQ	Hz	Only for internal use
10376	int	_BACNET_SENDDIAM_TIME	s	Only for internal use
10378	ushort	_HTML_PORT		Only for internal use
10379	string	_IP_ADDR_STR	32	Only for internal use
10395	string	_IP_GATEWAY_STR	32	Only for internal use
10411	string	_IP_MASK_STR	32	Only for internal use
10427	string	_NAMESRV_IP	32	Only for internal use
10443	string	_NTPSRV_IP	128	Only for internal use
10507	string	_HOSTNAME	64	Only for internal use
10539	string	_EVT_NAME	16	Only for internal use
10547	string	_FL_NAME	16	Only for internal use
10555	string	_TR_NAME	16	Only for internal use
10563	string	_HWW_NAME	16	Only for internal use
10571	int	_FILEMAGIC		Only for internal use
10573	int	_MODE_NTP		Only for internal use
10575	int	_QUARZ_KORR	ppm	Only for internal use
10577	string	_TFTP_PRG1	256	Only for internal use
10705	string	_TFTP_PRG2	256	Only for internal use
10833	string	_TFTP_PRG3	256	Only for internal use
10961	string	_TFTP_PRG4	256	Only for internal use
11089	string	_TFTP_PRG5	256	Only for internal use
11217	string	_TFTP_PRG6	256	Only for internal use
11345	string	_TFTP_REC	256	Only for internal use
11473	string	_TFTP_DISPLAY	256	Only for internal use
11601	string	_RELEASE	16	Only for internal use
11609	string	_DOWNLOAD	64	Only for internal use
11641	int	_JASIC_VAR		Only for internal use
11643	int	_DUMMY		Only for internal use
11645	uint	_MASTER_TIMEOUT	msec	Only for internal use
11647	int	_ED_PASSWD		Only for internal use
11649	int	_HTML_PASSWD		Password HTML
11651	int	_PASSWD_MODE		Password HTML
11653	float	_CHALLENGE		Only for internal use
11655	uint	_EMAX_PASSWORD		Passwort EMAX
11657	uint	_BACNET_PASSWORD		Passwort BACnet
11659	short	_FORBID_HTML		Only for internal use
11660	short	_FORBID_CFG_HTML		Only for internal use
11661	short	_FORBID_FTP		Only for internal use
11662	short	_FORBID_CFG_FTP		Only for internal use
11663	short	_FORBID_MODETH		Only for internal use
11664	short	_FORBID_CFG_MODETH		Only for internal use
11665	short	_FORBID_BACNET		Only for internal use
11666	short	_IP_UP		Only for internal use
11667	short	_SYSVAR_CNT		Only for internal use
11668	string	_SEQ_IP0	32	Only for internal use
11684	string	_SEQ_IP1	32	Only for internal use
11700	string	_SEQ_IP2	32	Only for internal use
11716	string	_SEQ_IP3	32	Only for internal use



Address	Format	Designation	Unit	Note
11732	string	_SEQ_IP4	32	Only for internal use
11748	string	_SEQ_IP5	32	Only for internal use
11764	string	_SEQ_IP6	32	Only for internal use
11780	string	_SEQ_IP7	32	Only for internal use
11796	short	_CH_MAP[0]		Only for internal use
11797	short	_CH_MAP[1]		Only for internal use
11798	short	_CH_MAP[2]		Only for internal use
11799	short	_CH_MAP[3]		Only for internal use
11800	short	_CH_MAP[4]		Only for internal use
11801	short	_CH_MAP[5]		Only for internal use
11802	short	_CH_MAP[6]		Only for internal use
11803	short	_CH_MAP[7]		Only for internal use
11804	float	_NTP_DIV	s	Only for internal use
11806	float	_NTP_TURNAROUND	s	Only for internal use
11808	float	_NTP_KORR	ppm	Only for internal use
11810	long64	_RX_ETH_COUNT		Only for internal use
11814	long64	_TX_ETH_COUNT		Only for internal use
11818	long64	_ERR_ETH_COUNT		Only for internal use
11822	long64	_RX_NTP_COUNT		Only for internal use
11826	long64	_TX_NTP_COUNT		Only for internal use
11830	long64	_ERR_NTP_COUNT		Only for internal use
11834	long64	_RX_DNS_COUNT		Only for internal use
11838	long64	_TX_DNS_COUNT		Only for internal use
11842	long64	_ERR_DNS_COUNT		Only for internal use
11846	long64	_RX_DHCP_COUNT		Only for internal use
11850	long64	_TX_DHCP_COUNT		Only for internal use
11854	long64	_ERR_DHCP_COUNT		Only for internal use
11858	long64	_TX_EMAIL_COUNT		Only for internal use
11862	long64	_ERR_EMAIL_COUNT		Only for internal use
11866	int	_MTU_SIZE		Only for internal use
11868	long64	_SYSTIMEUP	10ms	Only for internal use
11872	float	_WH_V_T3[0]	Wh	Rate 3, real energy L1
11874	float	_WH_V_T3[1]	Wh	
11876	float	_WH_V_T3[2]	Wh	
11878	float	_WH_V_T3[3]	Wh	
11880	float	_WH_V_T3[4]	Wh	
11882	float	_WH_V_T3[5]	Wh	
11884	float	_WH_V_T4[0]	Wh	
11886	float	_WH_V_T4[1]	Wh	
11888	float	_WH_V_T4[2]	Wh	
11890	float	_WH_V_T4[3]	Wh	
11892	float	_WH_V_T4[4]	Wh	
11894	float	_WH_V_T4[5]	Wh	
11896	float	_WH_Z_T3[0]	Wh	
11898	float	_WH_Z_T3[1]	Wh	
11900	float	_WH_Z_T3[2]	Wh	
11902	float	_WH_Z_T3[3]	Wh	
11904	float	_WH_Z_T3[4]	Wh	
11906	float	_WH_Z_T3[5]	Wh	
11908	float	_WH_Z_T4[0]	Wh	
11910	float	_WH_Z_T4[1]	Wh	
11912	float	_WH_Z_T4[2]	Wh	
11914	float	_WH_Z_T4[3]	Wh	
11916	float	_WH_Z_T4[4]	Wh	
11918	float	_WH_Z_T4[5]	Wh	
11920	float	_IQH_T3[0]	varh	
11922	float	_IQH_T3[1]	varh	
11924	float	_IQH_T3[2]	varh	
11926	float	_IQH_T3[3]	varh	

Address	Format	Designation	Unit	Note
11928	float	_IQH_T3[4]	varh	
11930	float	_IQH_T3[5]	varh	
11932	float	_IQH_T4[0]	varh	
11934	float	_IQH_T4[1]	varh	
11936	float	_IQH_T4[2]	varh	
11938	float	_IQH_T4[3]	varh	
11940	float	_IQH_T4[4]	varh	
11942	float	_IQH_T4[5]	varh	
11944	float	_SNMP_USERVAR[0]		Only for internal use
11946	float	_SNMP_USERVAR[1]		Only for internal use
11948	float	_SNMP_USERVAR[2]		Only for internal use
11950	float	_SNMP_USERVAR[3]		Only for internal use
11952	float	_SNMP_USERVAR[4]		Only for internal use
11954	float	_SNMP_USERVAR[5]		Only for internal use
11956	float	_SNMP_USERVAR[6]		Only for internal use
11958	float	_SNMP_USERVAR[7]		Only for internal use
11960	float	_SNMP_USERVAR[8]		Only for internal use
11962	float	_SNMP_USERVAR[9]		Only for internal use
11964	float	_SNMP_USERVAR[10]		Only for internal use
11966	float	_SNMP_USERVAR[11]		Only for internal use
11968	float	_SNMP_USERVAR[12]		Only for internal use
11970	float	_SNMP_USERVAR[13]		Only for internal use
11972	float	_SNMP_USERVAR[14]		Only for internal use
11974	float	_SNMP_USERVAR[15]		Only for internal use
11976	double	_AKT_EVT_START[0]	s	Only for internal use
11980	double	_AKT_EVT_START[1]	s	Only for internal use
11984	double	_AKT_EVT_START[2]	s	Only for internal use
11988	double	_AKT_EVT_START[3]	s	Only for internal use
11992	double	_AKT_EVT_START[4]	s	Only for internal use
11996	double	_AKT_EVT_START[5]	s	Only for internal use
12000	double	_AKT_EVT_START[6]	s	Only for internal use
12004	double	_AKT_EVT_START[7]	s	Only for internal use
12008	double	_AKT_EVT_STOP[0]	s	Only for internal use
12012	double	_AKT_EVT_STOP[1]	s	Only for internal use
12016	double	_AKT_EVT_STOP[2]	s	Only for internal use
12020	double	_AKT_EVT_STOP[3]	s	Only for internal use
12024	double	_AKT_EVT_STOP[4]	s	Only for internal use
12028	double	_AKT_EVT_STOP[5]	s	Only for internal use
12032	double	_AKT_EVT_STOP[6]	s	Only for internal use
12036	double	_AKT_EVT_STOP[7]	s	Only for internal use
12040	float	_AKT_EVT_BOUND[0]		Only for internal use
12042	float	_AKT_EVT_BOUND[1]		Only for internal use
12044	float	_AKT_EVT_BOUND[2]		Only for internal use
12046	float	_AKT_EVT_BOUND[3]		Only for internal use
12048	float	_AKT_EVT_BOUND[4]		Only for internal use
12050	float	_AKT_EVT_BOUND[5]		Only for internal use
12052	float	_AKT_EVT_BOUND[6]		Only for internal use
12054	float	_AKT_EVT_BOUND[7]		Only for internal use
12056	float	_AKT_EVT_MAXVAL[0]		Only for internal use
12058	float	_AKT_EVT_MAXVAL[1]		Only for internal use
12060	float	_AKT_EVT_MAXVAL[2]		Only for internal use
12062	float	_AKT_EVT_MAXVAL[3]		Only for internal use
12064	float	_AKT_EVT_MAXVAL[4]		Only for internal use
12066	float	_AKT_EVT_MAXVAL[5]		Only for internal use
12068	float	_AKT_EVT_MAXVAL[6]		Only for internal use
12070	float	_AKT_EVT_MAXVAL[7]		Only for internal use
12072	float	_AKT_EVT_MINVAL[0]		Only for internal use
12074	float	_AKT_EVT_MINVAL[1]		Only for internal use
12076	float	_AKT_EVT_MINVAL[2]		Only for internal use

Address	Format	Designation	Unit	Note
12078	float	_AKT_EVT_MINVAL[3]		Only for internal use
12080	float	_AKT_EVT_MINVAL[4]		Only for internal use
12082	float	_AKT_EVT_MINVAL[5]		Only for internal use
12084	float	_AKT_EVT_MINVAL[6]		Only for internal use
12086	float	_AKT_EVT_MINVAL[7]		Only for internal use
12088	float	_AKT_EVT_AVG[0]		Only for internal use
12090	float	_AKT_EVT_AVG[1]		Only for internal use
12092	float	_AKT_EVT_AVG[2]		Only for internal use
12094	float	_AKT_EVT_AVG[3]		Only for internal use
12096	float	_AKT_EVT_AVG[4]		Only for internal use
12098	float	_AKT_EVT_AVG[5]		Only for internal use
12100	float	_AKT_EVT_AVG[6]		Only for internal use
12102	float	_AKT_EVT_AVG[7]		Only for internal use
12104	int	_AKT_EVT_REASON[0]		Only for internal use
12106	int	_AKT_EVT_REASON[1]		Only for internal use
12108	int	_AKT_EVT_REASON[2]		Only for internal use
12110	int	_AKT_EVT_REASON[3]		Only for internal use
12112	int	_AKT_EVT_REASON[4]		Only for internal use
12114	int	_AKT_EVT_REASON[5]		Only for internal use
12116	int	_AKT_EVT_REASON[6]		Only for internal use
12118	int	_AKT_EVT_REASON[7]		Only for internal use
12120	int	_AKT_EVT_CNT[0]		Only for internal use
12122	int	_AKT_EVT_CNT[1]		Only for internal use
12124	int	_AKT_EVT_CNT[2]		Only for internal use
12126	int	_AKT_EVT_CNT[3]		Only for internal use
12128	int	_AKT_EVT_CNT[4]		Only for internal use
12130	int	_AKT_EVT_CNT[5]		Only for internal use
12132	int	_AKT_EVT_CNT[6]		Only for internal use
12134	int	_AKT_EVT_CNT[7]		Only for internal use
12136	int	_HW_INDEX		Device hardware index

Address	Format	Designation	Unit	Note
<b>Energy</b>				
6528	short	_W_TARIF		Current rate, real/apparent energy
6529	short	_Q_TARIF		Current rate, reactive energy
6530	float	_WH_S[0]	VAh	Apparent energy L1
6532	float	_WH_S[1]	VAh	Apparent energy L2
6534	float	_WH_S[2]	VAh	Apparent energy L3
6536	float	_WH_S[3]	VAh	Apparent energy L4
6538	float	_WH_S[4]	VAh	Apparent energy L1,L2,L3
6540	float	_WH_S[5]	VAh	Apparent energy L1,L2,L3,L4
6542	float	_WH[0]	Wh	Real energy L1
6544	float	_WH[1]	Wh	
6546	float	_WH[2]	Wh	
6548	float	_WH[3]	Wh	
6550	float	_WH[4]	Wh	
6552	float	_WH[5]	Wh	
6554	float	_QH[0]	varh	Reactive energy L1
6556	float	_QH[1]	varh	
6558	float	_QH[2]	varh	
6560	float	_QH[3]	varh	
6562	float	_QH[4]	varh	
6564	float	_QH[5]	varh	
6566	float	_WH_V[0]	Wh	Real energy L1, consumed
6568	float	_WH_V[1]	Wh	
6570	float	_WH_V[2]	Wh	
6572	float	_WH_V[3]	Wh	
6574	float	_WH_V[4]	Wh	
6576	float	_WH_V[5]	Wh	
6578	float	_WH_Z[0]	Wh	Real energy L1, delivered
6580	float	_WH_Z[1]	Wh	
6582	float	_WH_Z[2]	Wh	
6584	float	_WH_Z[3]	Wh	
6586	float	_WH_Z[4]	Wh	
6588	float	_WH_Z[5]	Wh	
6590	float	_WH_V_HT[0]	Wh	Real energy L1, rate 1, consumed
6592	float	_WH_V_HT[1]	Wh	
6594	float	_WH_V_HT[2]	Wh	
6596	float	_WH_V_HT[3]	Wh	
6598	float	_WH_V_HT[4]	Wh	
6600	float	_WH_V_HT[5]	Wh	
6602	float	_WH_V_NT[0]	Wh	Real energy L1, rate 2, consumed
6604	float	_WH_V_NT[1]	Wh	
6606	float	_WH_V_NT[2]	Wh	
6608	float	_WH_V_NT[3]	Wh	
6610	float	_WH_V_NT[4]	Wh	
6612	float	_WH_V_NT[5]	Wh	
6614	float	_WH_Z_HT[0]	Wh	Real energy L1, rate 1, delivered
6616	float	_WH_Z_HT[1]	Wh	
6618	float	_WH_Z_HT[2]	Wh	
6620	float	_WH_Z_HT[3]	Wh	
6622	float	_WH_Z_HT[4]	Wh	
6624	float	_WH_Z_HT[5]	Wh	
6626	float	_WH_Z_NT[0]	Wh	Real energy L1, rate 2, delivered
6628	float	_WH_Z_NT[1]	Wh	
6630	float	_WH_Z_NT[2]	Wh	
6632	float	_WH_Z_NT[3]	Wh	
6634	float	_WH_Z_NT[4]	Wh	
6636	float	_WH_Z_NT[5]	Wh	
6638	float	_IQH[0]	varh	Reactive energy L1, inductive
6640	float	_IQH[1]	varh	

Address	Format	Designation	Unit	Note
6642	float	_IQH[2]	varh	
6644	float	_IQH[3]	varh	
6646	float	_IQH[4]	varh	
6648	float	_IQH[5]	varh	
6650	float	_CQH[0]	varh	Reactive energy L1, capacitive
6652	float	_CQH[1]	varh	
6654	float	_CQH[2]	varh	
6656	float	_CQH[3]	varh	
6658	float	_CQH[4]	varh	
6660	float	_CQH[5]	varh	
6662	float	_IQH_HT[0]	varh	Reactive energy L1, inductive, rate 1
6664	float	_IQH_HT[1]	varh	
6666	float	_IQH_HT[2]	varh	
6668	float	_IQH_HT[3]	varh	
6670	float	_IQH_HT[4]	varh	
6672	float	_IQH_HT[5]	varh	
6674	float	_IQH_NT[0]	varhh	Reactive energy L1, inductive, rate 2
6676	float	_IQH_NT[1]	varh	
6678	float	_IQH_NT[2]	varh	
6680	float	_IQH_NT[3]	varh	
6682	float	_IQH_NT[4]	varh	
6684	float	_IQH_NT[5]	varh	
6686	float	_S0_CNT[0]	n	Energy meter (counter, not scaled), impulse input 1
6688	float	_S0_CNT[1]	n	
6690	float	_S0_CNT[2]	n	
6692	float	_S0_CNT[3]	n	
6694	float	_S0_CNT[4]	n	
6696	float	_S0_CNT[5]	n	
6698	float	_S0_CNT[6]	n	
6700	float	_S0_CNT[7]	n	
6702	float	_TIME_WH	s	Runtime of real and apparent energy measurement
6704	float	_TIME_QH	s	Runtime of reactive energy measurement
12138	float	_S0_POWER[0]	W	Input 1, measured value
12140	float	_S0_POWER[1]	W	Input 2, measured value
12142	float	_S0_POWER[2]	W	Input 3, measured value
12144	float	_S0_POWER[3]	W	Input 4, measured value
12146	float	_S0_POWER[4]	W	Input 5, measured value
12148	float	_S0_POWER[5]	W	Input 6, measured value
12150	float	_S0_POWER[6]	W	Input 7, measured value
12152	float	_S0_POWER[7]	W	Input 8, measured value

Address	Format	Designation	Unit	Note
<b>EMAX</b>				
8568	short	_EMAX_SPERRZEIT	s	
8569	short	_EMAX_PAUSENZEIT	s	
8570	float	_EMAX_LEISTUNG	W	
8572	float	_EMAX_MAX	W	
8574	float	_EMAX_TRENDWERT	W	
8576	uint	_EMAX_MAX_T		
8578	short	_EMAX_D_STATUS[0]	n	
8579	short	_EMAX_D_STATUS[1]	n	
8580	short	_EMAX_D_STATUS[2]	n	
8581	short	_EMAX_D_STATUS[3]	n	
8582	short	_EMAX_D_STATUS[4]	n	
8583	short	_EMAX_D_STATUS[5]	n	
8584	short	_EMAX_D_STATUS[6]	n	
8585	short	_EMAX_D_STATUS[7]	n	
8586	short	_EMAX_D_STATUS[8]	n	
8587	short	_EMAX_D_STATUS[9]	n	
8588	short	_EMAX_D_STATUS[10]	n	
8589	short	_EMAX_D_STATUS[11]	n	
8590	short	_EMAX_D_STATUS[12]	n	
8591	short	_EMAX_D_STATUS[13]	n	
8592	short	_EMAX_D_STATUS[14]	n	
8593	short	_EMAX_D_STATUS[15]	n	
8594	short	_EMAX_D_STATUS[16]	n	
8595	short	_EMAX_D_STATUS[17]	n	
8596	short	_EMAX_D_STATUS[18]	n	
8597	short	_EMAX_D_STATUS[19]	n	
8598	short	_EMAX_D_STATUS[20]	n	
8599	short	_EMAX_D_STATUS[21]	n	
8600	short	_EMAX_D_STATUS[22]	n	
8601	short	_EMAX_D_STATUS[23]	n	
8602	short	_EMAX_D_STATUS[24]	n	
8603	short	_EMAX_D_STATUS[25]	n	
8604	short	_EMAX_D_STATUS[26]	n	
8605	short	_EMAX_D_STATUS[27]	n	
8606	short	_EMAX_D_STATUS[28]	n	
8607	short	_EMAX_D_STATUS[29]	n	
8608	short	_EMAX_D_STATUS[30]	n	
8609	short	_EMAX_D_STATUS[31]	n	
8610	short	_EMAX_D_STATUS[32]	n	
8611	short	_EMAX_D_STATUS[33]	n	
8612	short	_EMAX_D_STATUS[34]	n	
8613	short	_EMAX_D_STATUS[35]	n	
8614	short	_EMAX_D_STATUS[36]	n	
8615	short	_EMAX_D_STATUS[37]	n	
8616	short	_EMAX_D_STATUS[38]	n	
8617	short	_EMAX_D_STATUS[39]	n	
8618	short	_EMAX_D_STATUS[40]	n	
8619	short	_EMAX_D_STATUS[41]	n	
8620	short	_EMAX_D_STATUS[42]	n	
8621	short	_EMAX_D_STATUS[43]	n	
8622	short	_EMAX_D_STATUS[44]	n	
8623	short	_EMAX_D_STATUS[45]	n	
8624	short	_EMAX_D_STATUS[46]	n	
8625	short	_EMAX_D_STATUS[47]	n	
8626	short	_EMAX_D_STATUS[48]	n	
8627	short	_EMAX_D_STATUS[49]	n	
8628	short	_EMAX_D_STATUS[50]	n	
8629	short	_EMAX_D_STATUS[51]	n	

Address	Format	Designation	Unit	Note
8630	short	_EMAX_D_STATUS[52]	n	
8631	short	_EMAX_D_STATUS[53]	n	
8632	short	_EMAX_D_STATUS[54]	n	
8633	short	_EMAX_D_STATUS[55]	n	
8634	short	_EMAX_D_STATUS[56]	n	
8635	short	_EMAX_D_STATUS[57]	n	
8636	short	_EMAX_D_STATUS[58]	n	
8637	short	_EMAX_D_STATUS[59]	n	
8638	short	_EMAX_D_STATUS[60]	n	
8639	short	_EMAX_D_STATUS[61]	n	
8640	short	_EMAX_D_STATUS[62]	n	
8641	short	_EMAX_D_STATUS[63]	n	
8642	float	_EMAX_A_STATUS[0]	%	
8644	float	_EMAX_A_STATUS[1]	%	
8646	float	_EMAX_A_STATUS[2]	%	
8648	float	_EMAX_A_STATUS[3]	%	
8650	short	_EMAX_D_ACTIVE[0]		
8651	short	_EMAX_D_ACTIVE[1]		
8652	short	_EMAX_D_ACTIVE[2]		
8653	short	_EMAX_D_ACTIVE[3]		
8654	short	_EMAX_D_ACTIVE[4]		
8655	short	_EMAX_D_ACTIVE[5]		
8656	short	_EMAX_D_ACTIVE[6]		
8657	short	_EMAX_D_ACTIVE[7]		
8658	short	_EMAX_D_ACTIVE[8]		
8659	short	_EMAX_D_ACTIVE[9]		
8660	short	_EMAX_D_ACTIVE[10]		
8661	short	_EMAX_D_ACTIVE[11]		
8662	short	_EMAX_D_ACTIVE[12]		
8663	short	_EMAX_D_ACTIVE[13]		
8664	short	_EMAX_D_ACTIVE[14]		
8665	short	_EMAX_D_ACTIVE[15]		
8666	short	_EMAX_D_ACTIVE[16]		
8667	short	_EMAX_D_ACTIVE[17]		
8668	short	_EMAX_D_ACTIVE[18]		
8669	short	_EMAX_D_ACTIVE[19]		
8670	short	_EMAX_D_ACTIVE[20]		
8671	short	_EMAX_D_ACTIVE[21]		
8672	short	_EMAX_D_ACTIVE[22]		
8673	short	_EMAX_D_ACTIVE[23]		
8674	short	_EMAX_D_ACTIVE[24]		
8675	short	_EMAX_D_ACTIVE[25]		
8676	short	_EMAX_D_ACTIVE[26]		
8677	short	_EMAX_D_ACTIVE[27]		
8678	short	_EMAX_D_ACTIVE[28]		
8679	short	_EMAX_D_ACTIVE[29]		
8680	short	_EMAX_D_ACTIVE[30]		
8681	short	_EMAX_D_ACTIVE[31]		
8682	short	_EMAX_D_ACTIVE[32]		
8683	short	_EMAX_D_ACTIVE[33]		
8684	short	_EMAX_D_ACTIVE[34]		
8685	short	_EMAX_D_ACTIVE[35]		
8686	short	_EMAX_D_ACTIVE[36]		
8687	short	_EMAX_D_ACTIVE[37]		
8688	short	_EMAX_D_ACTIVE[38]		
8689	short	_EMAX_D_ACTIVE[39]		
8690	short	_EMAX_D_ACTIVE[40]		
8691	short	_EMAX_D_ACTIVE[41]		
8692	short	_EMAX_D_ACTIVE[42]		
8693	short	_EMAX_D_ACTIVE[43]		

Address	Format	Designation	Unit	Note
8694	short	_EMAX_D_ACTIVE[44]		
8695	short	_EMAX_D_ACTIVE[45]		
8696	short	_EMAX_D_ACTIVE[46]		
8697	short	_EMAX_D_ACTIVE[47]		
8698	short	_EMAX_D_ACTIVE[48]		
8699	short	_EMAX_D_ACTIVE[49]		
8700	short	_EMAX_D_ACTIVE[50]		
8701	short	_EMAX_D_ACTIVE[51]		
8702	short	_EMAX_D_ACTIVE[52]		
8703	short	_EMAX_D_ACTIVE[53]		
8704	short	_EMAX_D_ACTIVE[54]		
8705	short	_EMAX_D_ACTIVE[55]		
8706	short	_EMAX_D_ACTIVE[56]		
8707	short	_EMAX_D_ACTIVE[57]		
8708	short	_EMAX_D_ACTIVE[58]		
8709	short	_EMAX_D_ACTIVE[59]		
8710	short	_EMAX_D_ACTIVE[60]		
8711	short	_EMAX_D_ACTIVE[61]		
8712	short	_EMAX_D_ACTIVE[62]		
8713	short	_EMAX_D_ACTIVE[63]		
8714	short	_EMAX_A_ACTIVE[0]		
8715	short	_EMAX_A_ACTIVE[1]		
8716	short	_EMAX_A_ACTIVE[2]		
8717	short	_EMAX_A_ACTIVE[3]		
8718	string	_EMAX_D_DESC0	32	
8734	string	_EMAX_D_DESC1	32	
8750	string	_EMAX_D_DESC2	32	
8766	string	_EMAX_D_DESC3	32	
8782	string	_EMAX_D_DESC4	32	
8798	string	_EMAX_D_DESC5	32	
8814	string	_EMAX_D_DESC6	32	
8830	string	_EMAX_D_DESC7	32	
8846	string	_EMAX_D_DESC8	32	
8862	string	_EMAX_D_DESC9	32	
8878	string	_EMAX_D_DESC10	32	
8894	string	_EMAX_D_DESC11	32	
8910	string	_EMAX_D_DESC12	32	
8926	string	_EMAX_D_DESC13	32	
8942	string	_EMAX_D_DESC14	32	
8958	string	_EMAX_D_DESC15	32	
8974	string	_EMAX_D_DESC16	32	
8990	string	_EMAX_D_DESC17	32	
9006	string	_EMAX_D_DESC18	32	
9022	string	_EMAX_D_DESC19	32	
9038	string	_EMAX_D_DESC20	32	
9054	string	_EMAX_D_DESC21	32	
9070	string	_EMAX_D_DESC22	32	
9086	string	_EMAX_D_DESC23	32	
9102	string	_EMAX_D_DESC24	32	
9118	string	_EMAX_D_DESC25	32	
9134	string	_EMAX_D_DESC26	32	
9150	string	_EMAX_D_DESC27	32	
9166	string	_EMAX_D_DESC28	32	
9182	string	_EMAX_D_DESC29	32	
9198	string	_EMAX_D_DESC30	32	
9214	string	_EMAX_D_DESC31	32	
9230	string	_EMAX_D_DESC32	32	
9246	string	_EMAX_D_DESC33	32	
9262	string	_EMAX_D_DESC34	32	



Address	Format	Designation	Unit	Note
9278	string	_EMAX_D_DESC35	32	
9294	string	_EMAX_D_DESC36	32	
9310	string	_EMAX_D_DESC37	32	
9326	string	_EMAX_D_DESC38	32	
9342	string	_EMAX_D_DESC39	32	
9358	string	_EMAX_D_DESC40	32	
9374	string	_EMAX_D_DESC41	32	
9390	string	_EMAX_D_DESC42	32	
9406	string	_EMAX_D_DESC43	32	
9422	string	_EMAX_D_DESC44	32	
9438	string	_EMAX_D_DESC45	32	
9454	string	_EMAX_D_DESC46	32	
9470	string	_EMAX_D_DESC47	32	
9486	string	_EMAX_D_DESC48	32	
9502	string	_EMAX_D_DESC49	32	
9518	string	_EMAX_D_DESC50	32	
9534	string	_EMAX_D_DESC51	32	
9550	string	_EMAX_D_DESC52	32	
9566	string	_EMAX_D_DESC53	32	
9582	string	_EMAX_D_DESC54	32	
9598	string	_EMAX_D_DESC55	32	
9614	string	_EMAX_D_DESC56	32	
9630	string	_EMAX_D_DESC57	32	
9646	string	_EMAX_D_DESC58	32	
9662	string	_EMAX_D_DESC59	32	
9678	string	_EMAX_D_DESC60	32	
9694	string	_EMAX_D_DESC61	32	
9710	string	_EMAX_D_DESC62	32	
9726	string	_EMAX_D_DESC63	32	
9742	string	_EMAX_A_DESC0	32	
9758	string	_EMAX_A_DESC1	32	
9774	string	_EMAX_A_DESC2	32	
9790	string	_EMAX_A_DESC3	32	
9806	short	_EMAX_D_PRIORITY[0]		
9807	short	_EMAX_D_PRIORITY[1]		
9808	short	_EMAX_D_PRIORITY[2]		
9809	short	_EMAX_D_PRIORITY[3]		
9810	short	_EMAX_D_PRIORITY[4]		
9811	short	_EMAX_D_PRIORITY[5]		
9812	short	_EMAX_D_PRIORITY[6]		
9813	short	_EMAX_D_PRIORITY[7]		
9814	short	_EMAX_D_PRIORITY[8]		
9815	short	_EMAX_D_PRIORITY[9]		
9816	short	_EMAX_D_PRIORITY[10]		
9817	short	_EMAX_D_PRIORITY[11]		
9818	short	_EMAX_D_PRIORITY[12]		
9819	short	_EMAX_D_PRIORITY[13]		
9820	short	_EMAX_D_PRIORITY[14]		
9821	short	_EMAX_D_PRIORITY[15]		
9822	short	_EMAX_D_PRIORITY[16]		
9823	short	_EMAX_D_PRIORITY[17]		
9824	short	_EMAX_D_PRIORITY[18]		
9825	short	_EMAX_D_PRIORITY[19]		
9826	short	_EMAX_D_PRIORITY[20]		
9827	short	_EMAX_D_PRIORITY[21]		
9828	short	_EMAX_D_PRIORITY[22]		
9829	short	_EMAX_D_PRIORITY[23]		
9830	short	_EMAX_D_PRIORITY[24]		
9831	short	_EMAX_D_PRIORITY[25]		

Address	Format	Designation	Unit	Note
9832	short	_EMAX_D_PRIORITY[26]		
9833	short	_EMAX_D_PRIORITY[27]		
9834	short	_EMAX_D_PRIORITY[28]		
9835	short	_EMAX_D_PRIORITY[29]		
9836	short	_EMAX_D_PRIORITY[30]		
9837	short	_EMAX_D_PRIORITY[31]		
9838	short	_EMAX_D_PRIORITY[32]		
9839	short	_EMAX_D_PRIORITY[33]		
9840	short	_EMAX_D_PRIORITY[34]		
9841	short	_EMAX_D_PRIORITY[35]		
9842	short	_EMAX_D_PRIORITY[36]		
9843	short	_EMAX_D_PRIORITY[37]		
9844	short	_EMAX_D_PRIORITY[38]		
9845	short	_EMAX_D_PRIORITY[39]		
9846	short	_EMAX_D_PRIORITY[40]		
9847	short	_EMAX_D_PRIORITY[41]		
9848	short	_EMAX_D_PRIORITY[42]		
9849	short	_EMAX_D_PRIORITY[43]		
9850	short	_EMAX_D_PRIORITY[44]		
9851	short	_EMAX_D_PRIORITY[45]		
9852	short	_EMAX_D_PRIORITY[46]		
9853	short	_EMAX_D_PRIORITY[47]		
9854	short	_EMAX_D_PRIORITY[48]		
9855	short	_EMAX_D_PRIORITY[49]		
9856	short	_EMAX_D_PRIORITY[50]		
9857	short	_EMAX_D_PRIORITY[51]		
9858	short	_EMAX_D_PRIORITY[52]		
9859	short	_EMAX_D_PRIORITY[53]		
9860	short	_EMAX_D_PRIORITY[54]		
9861	short	_EMAX_D_PRIORITY[55]		
9862	short	_EMAX_D_PRIORITY[56]		
9863	short	_EMAX_D_PRIORITY[57]		
9864	short	_EMAX_D_PRIORITY[58]		
9865	short	_EMAX_D_PRIORITY[59]		
9866	short	_EMAX_D_PRIORITY[60]		
9867	short	_EMAX_D_PRIORITY[61]		
9868	short	_EMAX_D_PRIORITY[62]		
9869	short	_EMAX_D_PRIORITY[63]		
9870	short	_EMAX_A_PRIORITY[0]		
9871	short	_EMAX_A_PRIORITY[1]		
9872	short	_EMAX_A_PRIORITY[2]		
9873	short	_EMAX_A_PRIORITY[3]		
9874	float	_EMAX_D_POWER[0]	W	
9876	float	_EMAX_D_POWER[1]	W	
9878	float	_EMAX_D_POWER[2]	W	
9880	float	_EMAX_D_POWER[3]	W	
9882	float	_EMAX_D_POWER[4]	W	
9884	float	_EMAX_D_POWER[5]	W	
9886	float	_EMAX_D_POWER[6]	W	
9888	float	_EMAX_D_POWER[7]	W	
9890	float	_EMAX_D_POWER[8]	W	
9892	float	_EMAX_D_POWER[9]	W	
9894	float	_EMAX_D_POWER[10]	W	
9896	float	_EMAX_D_POWER[11]	W	
9898	float	_EMAX_D_POWER[12]	W	
9900	float	_EMAX_D_POWER[13]	W	
9902	float	_EMAX_D_POWER[14]	W	
9904	float	_EMAX_D_POWER[15]	W	
9906	float	_EMAX_D_POWER[16]	W	

Address	Format	Designation	Unit	Note
9908	float	_EMAX_D_POWER[17]	W	
9910	float	_EMAX_D_POWER[18]	W	
9912	float	_EMAX_D_POWER[19]	W	
9914	float	_EMAX_D_POWER[20]	W	
9916	float	_EMAX_D_POWER[21]	W	
9918	float	_EMAX_D_POWER[22]	W	
9920	float	_EMAX_D_POWER[23]	W	
9922	float	_EMAX_D_POWER[24]	W	
9924	float	_EMAX_D_POWER[25]	W	
9926	float	_EMAX_D_POWER[26]	W	
9928	float	_EMAX_D_POWER[27]	W	
9930	float	_EMAX_D_POWER[28]	W	
9932	float	_EMAX_D_POWER[29]	W	
9934	float	_EMAX_D_POWER[30]	W	
9936	float	_EMAX_D_POWER[31]	W	
9938	float	_EMAX_D_POWER[32]	W	
9940	float	_EMAX_D_POWER[33]	W	
9942	float	_EMAX_D_POWER[34]	W	
9944	float	_EMAX_D_POWER[35]	W	
9946	float	_EMAX_D_POWER[36]	W	
9948	float	_EMAX_D_POWER[37]	W	
9950	float	_EMAX_D_POWER[38]	W	
9952	float	_EMAX_D_POWER[39]	W	
9954	float	_EMAX_D_POWER[40]	W	
9956	float	_EMAX_D_POWER[41]	W	
9958	float	_EMAX_D_POWER[42]	W	
9960	float	_EMAX_D_POWER[43]	W	
9962	float	_EMAX_D_POWER[44]	W	
9964	float	_EMAX_D_POWER[45]	W	
9966	float	_EMAX_D_POWER[46]	W	
9968	float	_EMAX_D_POWER[47]	W	
9970	float	_EMAX_D_POWER[48]	W	
9972	float	_EMAX_D_POWER[49]	W	
9974	float	_EMAX_D_POWER[50]	W	
9976	float	_EMAX_D_POWER[51]	W	
9978	float	_EMAX_D_POWER[52]	W	
9980	float	_EMAX_D_POWER[53]	W	
9982	float	_EMAX_D_POWER[54]	W	
9984	float	_EMAX_D_POWER[55]	W	
9986	float	_EMAX_D_POWER[56]	W	
9988	float	_EMAX_D_POWER[57]	W	
9990	float	_EMAX_D_POWER[58]	W	
9992	float	_EMAX_D_POWER[59]	W	
9994	float	_EMAX_D_POWER[60]	W	
9996	float	_EMAX_D_POWER[61]	W	
9998	float	_EMAX_D_POWER[62]	W	
10000	float	_EMAX_D_POWER[63]	W	
10002	short	_EMAX_D_MIN_ONDURATION[0]	s	
10003	short	_EMAX_D_MIN_ONDURATION[1]	s	
10004	short	_EMAX_D_MIN_ONDURATION[2]	s	
10005	short	_EMAX_D_MIN_ONDURATION[3]	s	
10006	short	_EMAX_D_MIN_ONDURATION[4]	s	
10007	short	_EMAX_D_MIN_ONDURATION[5]	s	
10008	short	_EMAX_D_MIN_ONDURATION[6]	s	
10009	short	_EMAX_D_MIN_ONDURATION[7]	s	
10010	short	_EMAX_D_MIN_ONDURATION[8]	s	
10011	short	_EMAX_D_MIN_ONDURATION[9]	s	
10012	short	_EMAX_D_MIN_ONDURATION[10]	s	
10013	short	_EMAX_D_MIN_ONDURATION[11]	s	

Address	Format	Designation	Unit	Note
10014	short	_EMAX_D_MIN_ONDURATION[12]	s	
10015	short	_EMAX_D_MIN_ONDURATION[13]	s	
10016	short	_EMAX_D_MIN_ONDURATION[14]	s	
10017	short	_EMAX_D_MIN_ONDURATION[15]	s	
10018	short	_EMAX_D_MIN_ONDURATION[16]	s	
10019	short	_EMAX_D_MIN_ONDURATION[17]	s	
10020	short	_EMAX_D_MIN_ONDURATION[18]	s	
10021	short	_EMAX_D_MIN_ONDURATION[19]	s	
10022	short	_EMAX_D_MIN_ONDURATION[20]	s	
10023	short	_EMAX_D_MIN_ONDURATION[21]	s	
10024	short	_EMAX_D_MIN_ONDURATION[22]	s	
10025	short	_EMAX_D_MIN_ONDURATION[23]	s	
10026	short	_EMAX_D_MIN_ONDURATION[24]	s	
10027	short	_EMAX_D_MIN_ONDURATION[25]	s	
10028	short	_EMAX_D_MIN_ONDURATION[26]	s	
10029	short	_EMAX_D_MIN_ONDURATION[27]	s	
10030	short	_EMAX_D_MIN_ONDURATION[28]	s	
10031	short	_EMAX_D_MIN_ONDURATION[29]	s	
10032	short	_EMAX_D_MIN_ONDURATION[30]	s	
10033	short	_EMAX_D_MIN_ONDURATION[31]	s	
10034	short	_EMAX_D_MIN_ONDURATION[32]	s	
10035	short	_EMAX_D_MIN_ONDURATION[33]	s	
10036	short	_EMAX_D_MIN_ONDURATION[34]	s	
10037	short	_EMAX_D_MIN_ONDURATION[35]	s	
10038	short	_EMAX_D_MIN_ONDURATION[36]	s	
10039	short	_EMAX_D_MIN_ONDURATION[37]	s	
10040	short	_EMAX_D_MIN_ONDURATION[38]	s	
10041	short	_EMAX_D_MIN_ONDURATION[39]	s	
10042	short	_EMAX_D_MIN_ONDURATION[40]	s	
10043	short	_EMAX_D_MIN_ONDURATION[41]	s	
10044	short	_EMAX_D_MIN_ONDURATION[42]	s	
10045	short	_EMAX_D_MIN_ONDURATION[43]	s	
10046	short	_EMAX_D_MIN_ONDURATION[44]	s	
10047	short	_EMAX_D_MIN_ONDURATION[45]	s	
10048	short	_EMAX_D_MIN_ONDURATION[46]	s	
10049	short	_EMAX_D_MIN_ONDURATION[47]	s	
10050	short	_EMAX_D_MIN_ONDURATION[48]	s	
10051	short	_EMAX_D_MIN_ONDURATION[49]	s	
10052	short	_EMAX_D_MIN_ONDURATION[50]	s	
10053	short	_EMAX_D_MIN_ONDURATION[51]	s	
10054	short	_EMAX_D_MIN_ONDURATION[52]	s	
10055	short	_EMAX_D_MIN_ONDURATION[53]	s	
10056	short	_EMAX_D_MIN_ONDURATION[54]	s	
10057	short	_EMAX_D_MIN_ONDURATION[55]	s	
10058	short	_EMAX_D_MIN_ONDURATION[56]	s	
10059	short	_EMAX_D_MIN_ONDURATION[57]	s	
10060	short	_EMAX_D_MIN_ONDURATION[58]	s	
10061	short	_EMAX_D_MIN_ONDURATION[59]	s	
10062	short	_EMAX_D_MIN_ONDURATION[60]	s	
10063	short	_EMAX_D_MIN_ONDURATION[61]	s	
10064	short	_EMAX_D_MIN_ONDURATION[62]	s	
10065	short	_EMAX_D_MIN_ONDURATION[63]	s	
10066	short	_EMAX_D_MAX_OFFDURATION[0]	s	
10067	short	_EMAX_D_MAX_OFFDURATION[1]	s	
10068	short	_EMAX_D_MAX_OFFDURATION[2]	s	
10069	short	_EMAX_D_MAX_OFFDURATION[3]	s	
10070	short	_EMAX_D_MAX_OFFDURATION[4]	s	
10071	short	_EMAX_D_MAX_OFFDURATION[5]	s	
10072	short	_EMAX_D_MAX_OFFDURATION[6]	s	

Address	Format	Designation	Unit	Note
10073	short	_EMAX_D_MAX_OFFDURATION[7]	s	
10074	short	_EMAX_D_MAX_OFFDURATION[8]	s	
10075	short	_EMAX_D_MAX_OFFDURATION[9]	s	
10076	short	_EMAX_D_MAX_OFFDURATION[10]	s	
10077	short	_EMAX_D_MAX_OFFDURATION[11]	s	
10078	short	_EMAX_D_MAX_OFFDURATION[12]	s	
10079	short	_EMAX_D_MAX_OFFDURATION[13]	s	
10080	short	_EMAX_D_MAX_OFFDURATION[14]	s	
10081	short	_EMAX_D_MAX_OFFDURATION[15]	s	
10082	short	_EMAX_D_MAX_OFFDURATION[16]	s	
10083	short	_EMAX_D_MAX_OFFDURATION[17]	s	
10084	short	_EMAX_D_MAX_OFFDURATION[18]	s	
10085	short	_EMAX_D_MAX_OFFDURATION[19]	s	
10086	short	_EMAX_D_MAX_OFFDURATION[20]	s	
10087	short	_EMAX_D_MAX_OFFDURATION[21]	s	
10088	short	_EMAX_D_MAX_OFFDURATION[22]	s	
10089	short	_EMAX_D_MAX_OFFDURATION[23]	s	
10090	short	_EMAX_D_MAX_OFFDURATION[24]	s	
10091	short	_EMAX_D_MAX_OFFDURATION[25]	s	
10092	short	_EMAX_D_MAX_OFFDURATION[26]	s	
10093	short	_EMAX_D_MAX_OFFDURATION[27]	s	
10094	short	_EMAX_D_MAX_OFFDURATION[28]	s	
10095	short	_EMAX_D_MAX_OFFDURATION[29]	s	
10096	short	_EMAX_D_MAX_OFFDURATION[30]	s	
10097	short	_EMAX_D_MAX_OFFDURATION[31]	s	
10098	short	_EMAX_D_MAX_OFFDURATION[32]	s	
10099	short	_EMAX_D_MAX_OFFDURATION[33]	s	
10100	short	_EMAX_D_MAX_OFFDURATION[34]	s	
10101	short	_EMAX_D_MAX_OFFDURATION[35]	s	
10102	short	_EMAX_D_MAX_OFFDURATION[36]	s	
10103	short	_EMAX_D_MAX_OFFDURATION[37]	s	
10104	short	_EMAX_D_MAX_OFFDURATION[38]	s	
10105	short	_EMAX_D_MAX_OFFDURATION[39]	s	
10106	short	_EMAX_D_MAX_OFFDURATION[40]	s	
10107	short	_EMAX_D_MAX_OFFDURATION[41]	s	
10108	short	_EMAX_D_MAX_OFFDURATION[42]	s	
10109	short	_EMAX_D_MAX_OFFDURATION[43]	s	
10110	short	_EMAX_D_MAX_OFFDURATION[44]	s	
10111	short	_EMAX_D_MAX_OFFDURATION[45]	s	
10112	short	_EMAX_D_MAX_OFFDURATION[46]	s	
10113	short	_EMAX_D_MAX_OFFDURATION[47]	s	
10114	short	_EMAX_D_MAX_OFFDURATION[48]	s	
10115	short	_EMAX_D_MAX_OFFDURATION[49]	s	
10116	short	_EMAX_D_MAX_OFFDURATION[50]	s	
10117	short	_EMAX_D_MAX_OFFDURATION[51]	s	
10118	short	_EMAX_D_MAX_OFFDURATION[52]	s	
10119	short	_EMAX_D_MAX_OFFDURATION[53]	s	
10120	short	_EMAX_D_MAX_OFFDURATION[54]	s	
10121	short	_EMAX_D_MAX_OFFDURATION[55]	s	
10122	short	_EMAX_D_MAX_OFFDURATION[56]	s	
10123	short	_EMAX_D_MAX_OFFDURATION[57]	s	
10124	short	_EMAX_D_MAX_OFFDURATION[58]	s	
10125	short	_EMAX_D_MAX_OFFDURATION[59]	s	
10126	short	_EMAX_D_MAX_OFFDURATION[60]	s	
10127	short	_EMAX_D_MAX_OFFDURATION[61]	s	
10128	short	_EMAX_D_MAX_OFFDURATION[62]	s	
10129	short	_EMAX_D_MAX_OFFDURATION[63]	s	
10130	short	_EMAX_D_MIN_OFFDURATION[0]	s	
10131	short	_EMAX_D_MIN_OFFDURATION[1]	s	

Address	Format	Designation	Unit	Note
10132	short	_EMAX_D_MIN_OFFDURATION[2]	s	
10133	short	_EMAX_D_MIN_OFFDURATION[3]	s	
10134	short	_EMAX_D_MIN_OFFDURATION[4]	s	
10135	short	_EMAX_D_MIN_OFFDURATION[5]	s	
10136	short	_EMAX_D_MIN_OFFDURATION[6]	s	
10137	short	_EMAX_D_MIN_OFFDURATION[7]	s	
10138	short	_EMAX_D_MIN_OFFDURATION[8]	s	
10139	short	_EMAX_D_MIN_OFFDURATION[9]	s	
10140	short	_EMAX_D_MIN_OFFDURATION[10]	s	
10141	short	_EMAX_D_MIN_OFFDURATION[11]	s	
10142	short	_EMAX_D_MIN_OFFDURATION[12]	s	
10143	short	_EMAX_D_MIN_OFFDURATION[13]	s	
10144	short	_EMAX_D_MIN_OFFDURATION[14]	s	
10145	short	_EMAX_D_MIN_OFFDURATION[15]	s	
10146	short	_EMAX_D_MIN_OFFDURATION[16]	s	
10147	short	_EMAX_D_MIN_OFFDURATION[17]	s	
10148	short	_EMAX_D_MIN_OFFDURATION[18]	s	
10149	short	_EMAX_D_MIN_OFFDURATION[19]	s	
10150	short	_EMAX_D_MIN_OFFDURATION[20]	s	
10151	short	_EMAX_D_MIN_OFFDURATION[21]	s	
10152	short	_EMAX_D_MIN_OFFDURATION[22]	s	
10153	short	_EMAX_D_MIN_OFFDURATION[23]	s	
10154	short	_EMAX_D_MIN_OFFDURATION[24]	s	
10155	short	_EMAX_D_MIN_OFFDURATION[25]	s	
10156	short	_EMAX_D_MIN_OFFDURATION[26]	s	
10157	short	_EMAX_D_MIN_OFFDURATION[27]	s	
10158	short	_EMAX_D_MIN_OFFDURATION[28]	s	
10159	short	_EMAX_D_MIN_OFFDURATION[29]	s	
10160	short	_EMAX_D_MIN_OFFDURATION[30]	s	
10161	short	_EMAX_D_MIN_OFFDURATION[31]	s	
10162	short	_EMAX_D_MIN_OFFDURATION[32]	s	
10163	short	_EMAX_D_MIN_OFFDURATION[33]	s	
10164	short	_EMAX_D_MIN_OFFDURATION[34]	s	
10165	short	_EMAX_D_MIN_OFFDURATION[35]	s	
10166	short	_EMAX_D_MIN_OFFDURATION[36]	s	
10167	short	_EMAX_D_MIN_OFFDURATION[37]	s	
10168	short	_EMAX_D_MIN_OFFDURATION[38]	s	
10169	short	_EMAX_D_MIN_OFFDURATION[39]	s	
10170	short	_EMAX_D_MIN_OFFDURATION[40]	s	
10171	short	_EMAX_D_MIN_OFFDURATION[41]	s	
10172	short	_EMAX_D_MIN_OFFDURATION[42]	s	
10173	short	_EMAX_D_MIN_OFFDURATION[43]	s	
10174	short	_EMAX_D_MIN_OFFDURATION[44]	s	
10175	short	_EMAX_D_MIN_OFFDURATION[45]	s	
10176	short	_EMAX_D_MIN_OFFDURATION[46]	s	
10177	short	_EMAX_D_MIN_OFFDURATION[47]	s	
10178	short	_EMAX_D_MIN_OFFDURATION[48]	s	
10179	short	_EMAX_D_MIN_OFFDURATION[49]	s	
10180	short	_EMAX_D_MIN_OFFDURATION[50]	s	
10181	short	_EMAX_D_MIN_OFFDURATION[51]	s	
10182	short	_EMAX_D_MIN_OFFDURATION[52]	s	
10183	short	_EMAX_D_MIN_OFFDURATION[53]	s	
10184	short	_EMAX_D_MIN_OFFDURATION[54]	s	
10185	short	_EMAX_D_MIN_OFFDURATION[55]	s	
10186	short	_EMAX_D_MIN_OFFDURATION[56]	s	
10187	short	_EMAX_D_MIN_OFFDURATION[57]	s	
10188	short	_EMAX_D_MIN_OFFDURATION[58]	s	
10189	short	_EMAX_D_MIN_OFFDURATION[59]	s	
10190	short	_EMAX_D_MIN_OFFDURATION[60]	s	

Address	Format	Designation	Unit	Note
10191	short	_EMAX_D_MIN_OFFDURATION[61]	s	
10192	short	_EMAX_D_MIN_OFFDURATION[62]	s	
10193	short	_EMAX_D_MIN_OFFDURATION[63]	s	
10194	float	_EMAX_D_PROBABILITY[0]	%	
10196	float	_EMAX_D_PROBABILITY[1]	%	
10198	float	_EMAX_D_PROBABILITY[2]	%	
10200	float	_EMAX_D_PROBABILITY[3]	%	
10202	float	_EMAX_D_PROBABILITY[4]	%	
10204	float	_EMAX_D_PROBABILITY[5]	%	
10206	float	_EMAX_D_PROBABILITY[6]	%	
10208	float	_EMAX_D_PROBABILITY[7]	%	
10210	float	_EMAX_D_PROBABILITY[8]	%	
10212	float	_EMAX_D_PROBABILITY[9]	%	
10214	float	_EMAX_D_PROBABILITY[10]	%	
10216	float	_EMAX_D_PROBABILITY[11]	%	
10218	float	_EMAX_D_PROBABILITY[12]	%	
10220	float	_EMAX_D_PROBABILITY[13]	%	
10222	float	_EMAX_D_PROBABILITY[14]	%	
10224	float	_EMAX_D_PROBABILITY[15]	%	
10226	float	_EMAX_D_PROBABILITY[16]	%	
10228	float	_EMAX_D_PROBABILITY[17]	%	
10230	float	_EMAX_D_PROBABILITY[18]	%	
10232	float	_EMAX_D_PROBABILITY[19]	%	
10234	float	_EMAX_D_PROBABILITY[20]	%	
10236	float	_EMAX_D_PROBABILITY[21]	%	
10238	float	_EMAX_D_PROBABILITY[22]	%	
10240	float	_EMAX_D_PROBABILITY[23]	%	
10242	float	_EMAX_D_PROBABILITY[24]	%	
10244	float	_EMAX_D_PROBABILITY[25]	%	
10246	float	_EMAX_D_PROBABILITY[26]	%	
10248	float	_EMAX_D_PROBABILITY[27]	%	
10250	float	_EMAX_D_PROBABILITY[28]	%	
10252	float	_EMAX_D_PROBABILITY[29]	%	
10254	float	_EMAX_D_PROBABILITY[30]	%	
10256	float	_EMAX_D_PROBABILITY[31]	%	
10258	float	_EMAX_D_PROBABILITY[32]	%	
10260	float	_EMAX_D_PROBABILITY[33]	%	
10262	float	_EMAX_D_PROBABILITY[34]	%	
10264	float	_EMAX_D_PROBABILITY[35]	%	
10266	float	_EMAX_D_PROBABILITY[36]	%	
10268	float	_EMAX_D_PROBABILITY[37]	%	
10270	float	_EMAX_D_PROBABILITY[38]	%	
10272	float	_EMAX_D_PROBABILITY[39]	%	
10274	float	_EMAX_D_PROBABILITY[40]	%	
10276	float	_EMAX_D_PROBABILITY[41]	%	
10278	float	_EMAX_D_PROBABILITY[42]	%	
10280	float	_EMAX_D_PROBABILITY[43]	%	
10282	float	_EMAX_D_PROBABILITY[44]	%	
10284	float	_EMAX_D_PROBABILITY[45]	%	
10286	float	_EMAX_D_PROBABILITY[46]	%	
10288	float	_EMAX_D_PROBABILITY[47]	%	
10290	float	_EMAX_D_PROBABILITY[48]	%	
10292	float	_EMAX_D_PROBABILITY[49]	%	
10294	float	_EMAX_D_PROBABILITY[50]	%	
10296	float	_EMAX_D_PROBABILITY[51]	%	
10298	float	_EMAX_D_PROBABILITY[52]	%	
10300	float	_EMAX_D_PROBABILITY[53]	%	
10302	float	_EMAX_D_PROBABILITY[54]	%	
10304	float	_EMAX_D_PROBABILITY[55]	%	

Address	Format	Designation	Unit	Note
10306	float	_EMAX_D_PROBABILITY[56]	%	
10308	float	_EMAX_D_PROBABILITY[57]	%	
10310	float	_EMAX_D_PROBABILITY[58]	%	
10312	float	_EMAX_D_PROBABILITY[59]	%	
10314	float	_EMAX_D_PROBABILITY[60]	%	
10316	float	_EMAX_D_PROBABILITY[61]	%	
10318	float	_EMAX_D_PROBABILITY[62]	%	
10320	float	_EMAX_D_PROBABILITY[63]	%	
10322	short	_EMAX_A_GENERATOR[0]		
10323	short	_EMAX_A_GENERATOR[1]		
10324	short	_EMAX_A_GENERATOR[2]		
10325	short	_EMAX_A_GENERATOR[3]		
10326	float	_EMAX_A_MAXPOWER[0]	W	
10328	float	_EMAX_A_MAXPOWER[1]	W	
10330	float	_EMAX_A_MAXPOWER[2]	W	
10332	float	_EMAX_A_MAXPOWER[3]	W	
10334	float	_EMAX_A_MINPOWER[0]	W	
10336	float	_EMAX_A_MINPOWER[1]	W	
10338	float	_EMAX_A_MINPOWER[2]	W	
10340	float	_EMAX_A_MINPOWER[3]	W	
10342	float	_EMAX_A_MAXSAVE[0]	W	
10344	float	_EMAX_A_MAXSAVE[1]	W	
10346	float	_EMAX_A_MAXSAVE[2]	W	
10348	float	_EMAX_A_MAXSAVE[3]	W	
10350	short	_EMAX_A_STARTUP[0]	s	
10351	short	_EMAX_A_STARTUP[1]	s	
10352	short	_EMAX_A_STARTUP[2]	s	
10353	short	_EMAX_A_STARTUP[3]	s	
10354	int	_EMAX_A_MINACTIVE[0]	s	
10356	int	_EMAX_A_MINACTIVE[1]	s	
10358	int	_EMAX_A_MINACTIVE[2]	s	
10360	int	_EMAX_A_MINACTIVE[3]	s	
10362	int	_EMAX_PERIOD	s	
10364	int	_EMAX_T_REMAIN	s	
10366	float	_EMAX_TREND	W	
10368	float	_EMAX_C_TREND	W	



Address	Format	Designation	Unit	Note
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Address	Format	Designation	Unit	Note
<b>FFT fourier analysis</b>				
13	float	_FFT_ULL1[0]	V	1st harmonic
15	float	_FFT_ULL1[1]	V	
17	float	_FFT_ULL1[2]	V	
19	float	_FFT_ULL1[3]	V	
21	float	_FFT_ULL1[4]	V	
23	float	_FFT_ULL1[5]	V	
25	float	_FFT_ULL1[6]	V	
27	float	_FFT_ULL1[7]	V	
29	float	_FFT_ULL1[8]	V	
31	float	_FFT_ULL1[9]	V	
33	float	_FFT_ULL1[10]	V	
35	float	_FFT_ULL1[11]	V	
37	float	_FFT_ULL1[12]	V	
39	float	_FFT_ULL1[13]	V	
41	float	_FFT_ULL1[14]	V	
43	float	_FFT_ULL1[15]	V	
45	float	_FFT_ULL1[16]	V	
47	float	_FFT_ULL1[17]	V	
49	float	_FFT_ULL1[18]	V	
51	float	_FFT_ULL1[19]	V	
53	float	_FFT_ULL1[20]	V	
55	float	_FFT_ULL1[21]	V	
57	float	_FFT_ULL1[22]	V	
59	float	_FFT_ULL1[23]	V	
61	float	_FFT_ULL1[24]	V	
63	float	_FFT_ULL1[25]	V	
65	float	_FFT_ULL1[26]	V	
67	float	_FFT_ULL1[27]	V	
69	float	_FFT_ULL1[28]	V	
71	float	_FFT_ULL1[29]	V	
73	float	_FFT_ULL1[30]	V	
75	float	_FFT_ULL1[31]	V	
77	float	_FFT_ULL1[32]	V	
79	float	_FFT_ULL1[33]	V	
81	float	_FFT_ULL1[34]	V	
83	float	_FFT_ULL1[35]	V	
85	float	_FFT_ULL1[36]	V	
87	float	_FFT_ULL1[37]	V	
89	float	_FFT_ULL1[38]	V	
91	float	_FFT_ULL1[39]	V	
93	float	_FFT_ULL1[40]	V	
95	float	_FFT_ULL1[41]	V	
97	float	_FFT_ULL1[42]	V	
99	float	_FFT_ULL1[43]	V	
101	float	_FFT_ULL1[44]	V	
103	float	_FFT_ULL1[45]	V	
105	float	_FFT_ULL1[46]	V	
107	float	_FFT_ULL1[47]	V	
109	float	_FFT_ULL1[48]	V	
111	float	_FFT_ULL1[49]	V	
113	float	_FFT_ULL1[50]	V	
115	float	_FFT_ULL1[51]	V	
117	float	_FFT_ULL1[52]	V	
119	float	_FFT_ULL1[53]	V	
121	float	_FFT_ULL1[54]	V	
123	float	_FFT_ULL1[55]	V	
125	float	_FFT_ULL1[56]	V	
127	float	_FFT_ULL1[57]	V	

Address	Format	Designation	Unit	Note
129	float	_FFT_ULL1[58]	V	
131	float	_FFT_ULL1[59]	V	
133	float	_FFT_ULL1[60]	V	
135	float	_FFT_ULL1[61]	V	
137	float	_FFT_ULL1[62]	V	63rd harmonic
139	float	_FFT_ULL2[0]	V	
141	float	_FFT_ULL2[1]	V	
143	float	_FFT_ULL2[2]	V	
145	float	_FFT_ULL2[3]	V	
147	float	_FFT_ULL2[4]	V	
149	float	_FFT_ULL2[5]	V	
151	float	_FFT_ULL2[6]	V	
153	float	_FFT_ULL2[7]	V	
155	float	_FFT_ULL2[8]	V	
157	float	_FFT_ULL2[9]	V	
159	float	_FFT_ULL2[10]	V	
161	float	_FFT_ULL2[11]	V	
163	float	_FFT_ULL2[12]	V	
165	float	_FFT_ULL2[13]	V	
167	float	_FFT_ULL2[14]	V	
169	float	_FFT_ULL2[15]	V	
171	float	_FFT_ULL2[16]	V	
173	float	_FFT_ULL2[17]	V	
175	float	_FFT_ULL2[18]	V	
177	float	_FFT_ULL2[19]	V	
179	float	_FFT_ULL2[20]	V	
181	float	_FFT_ULL2[21]	V	
183	float	_FFT_ULL2[22]	V	
185	float	_FFT_ULL2[23]	V	
187	float	_FFT_ULL2[24]	V	
189	float	_FFT_ULL2[25]	V	
191	float	_FFT_ULL2[26]	V	
193	float	_FFT_ULL2[27]	V	
195	float	_FFT_ULL2[28]	V	
197	float	_FFT_ULL2[29]	V	
199	float	_FFT_ULL2[30]	V	
201	float	_FFT_ULL2[31]	V	
203	float	_FFT_ULL2[32]	V	
205	float	_FFT_ULL2[33]	V	
207	float	_FFT_ULL2[34]	V	
209	float	_FFT_ULL2[35]	V	
211	float	_FFT_ULL2[36]	V	
213	float	_FFT_ULL2[37]	V	
215	float	_FFT_ULL2[38]	V	
217	float	_FFT_ULL2[39]	V	
219	float	_FFT_ULL2[40]	V	
221	float	_FFT_ULL2[41]	V	
223	float	_FFT_ULL2[42]	V	
225	float	_FFT_ULL2[43]	V	
227	float	_FFT_ULL2[44]	V	
229	float	_FFT_ULL2[45]	V	
231	float	_FFT_ULL2[46]	V	
233	float	_FFT_ULL2[47]	V	
235	float	_FFT_ULL2[48]	V	
237	float	_FFT_ULL2[49]	V	
239	float	_FFT_ULL2[50]	V	
241	float	_FFT_ULL2[51]	V	
243	float	_FFT_ULL2[52]	V	
245	float	_FFT_ULL2[53]	V	

Address	Format	Designation	Unit	Note
247	float	_FFT_ULL2[54]	V	
249	float	_FFT_ULL2[55]	V	
251	float	_FFT_ULL2[56]	V	
253	float	_FFT_ULL2[57]	V	
255	float	_FFT_ULL2[58]	V	
257	float	_FFT_ULL2[59]	V	
259	float	_FFT_ULL2[60]	V	
261	float	_FFT_ULL2[61]	V	
263	float	_FFT_ULL2[62]	V	
265	float	_FFT_ULL3[0]	V	
267	float	_FFT_ULL3[1]	V	
269	float	_FFT_ULL3[2]	V	
271	float	_FFT_ULL3[3]	V	
273	float	_FFT_ULL3[4]	V	
275	float	_FFT_ULL3[5]	V	
277	float	_FFT_ULL3[6]	V	
279	float	_FFT_ULL3[7]	V	
281	float	_FFT_ULL3[8]	V	
283	float	_FFT_ULL3[9]	V	
285	float	_FFT_ULL3[10]	V	
287	float	_FFT_ULL3[11]	V	
289	float	_FFT_ULL3[12]	V	
291	float	_FFT_ULL3[13]	V	
293	float	_FFT_ULL3[14]	V	
295	float	_FFT_ULL3[15]	V	
297	float	_FFT_ULL3[16]	V	
299	float	_FFT_ULL3[17]	V	
301	float	_FFT_ULL3[18]	V	
303	float	_FFT_ULL3[19]	V	
305	float	_FFT_ULL3[20]	V	
307	float	_FFT_ULL3[21]	V	
309	float	_FFT_ULL3[22]	V	
311	float	_FFT_ULL3[23]	V	
313	float	_FFT_ULL3[24]	V	
315	float	_FFT_ULL3[25]	V	
317	float	_FFT_ULL3[26]	V	
319	float	_FFT_ULL3[27]	V	
321	float	_FFT_ULL3[28]	V	
323	float	_FFT_ULL3[29]	V	
325	float	_FFT_ULL3[30]	V	
327	float	_FFT_ULL3[31]	V	
329	float	_FFT_ULL3[32]	V	
331	float	_FFT_ULL3[33]	V	
333	float	_FFT_ULL3[34]	V	
335	float	_FFT_ULL3[35]	V	
337	float	_FFT_ULL3[36]	V	
339	float	_FFT_ULL3[37]	V	
341	float	_FFT_ULL3[38]	V	
343	float	_FFT_ULL3[39]	V	
345	float	_FFT_ULL3[40]	V	
347	float	_FFT_ULL3[41]	V	
349	float	_FFT_ULL3[42]	V	
351	float	_FFT_ULL3[43]	V	
353	float	_FFT_ULL3[44]	V	
355	float	_FFT_ULL3[45]	V	
357	float	_FFT_ULL3[46]	V	
359	float	_FFT_ULL3[47]	V	
361	float	_FFT_ULL3[48]	V	
363	float	_FFT_ULL3[49]	V	

Address	Format	Designation	Unit	Note
365	float	_FFT_ULL3[50]	V	
367	float	_FFT_ULL3[51]	V	
369	float	_FFT_ULL3[52]	V	
371	float	_FFT_ULL3[53]	V	
373	float	_FFT_ULL3[54]	V	
375	float	_FFT_ULL3[55]	V	
377	float	_FFT_ULL3[56]	V	
379	float	_FFT_ULL3[57]	V	
381	float	_FFT_ULL3[58]	V	
383	float	_FFT_ULL3[59]	V	
385	float	_FFT_ULL3[60]	V	
387	float	_FFT_ULL3[61]	V	
389	float	_FFT_ULL3[62]	V	
391	float	_FFT_UL1[0]	V	
393	float	_FFT_UL1[1]	V	
395	float	_FFT_UL1[2]	V	
397	float	_FFT_UL1[3]	V	
399	float	_FFT_UL1[4]	V	
401	float	_FFT_UL1[5]	V	
403	float	_FFT_UL1[6]	V	
405	float	_FFT_UL1[7]	V	
407	float	_FFT_UL1[8]	V	
409	float	_FFT_UL1[9]	V	
411	float	_FFT_UL1[10]	V	
413	float	_FFT_UL1[11]	V	
415	float	_FFT_UL1[12]	V	
417	float	_FFT_UL1[13]	V	
419	float	_FFT_UL1[14]	V	
421	float	_FFT_UL1[15]	V	
423	float	_FFT_UL1[16]	V	
425	float	_FFT_UL1[17]	V	
427	float	_FFT_UL1[18]	V	
429	float	_FFT_UL1[19]	V	
431	float	_FFT_UL1[20]	V	
433	float	_FFT_UL1[21]	V	
435	float	_FFT_UL1[22]	V	
437	float	_FFT_UL1[23]	V	
439	float	_FFT_UL1[24]	V	
441	float	_FFT_UL1[25]	V	
443	float	_FFT_UL1[26]	V	
445	float	_FFT_UL1[27]	V	
447	float	_FFT_UL1[28]	V	
449	float	_FFT_UL1[29]	V	
451	float	_FFT_UL1[30]	V	
453	float	_FFT_UL1[31]	V	
455	float	_FFT_UL1[32]	V	
457	float	_FFT_UL1[33]	V	
459	float	_FFT_UL1[34]	V	
461	float	_FFT_UL1[35]	V	
463	float	_FFT_UL1[36]	V	
465	float	_FFT_UL1[37]	V	
467	float	_FFT_UL1[38]	V	
469	float	_FFT_UL1[39]	V	
471	float	_FFT_UL1[40]	V	
473	float	_FFT_UL1[41]	V	
475	float	_FFT_UL1[42]	V	
477	float	_FFT_UL1[43]	V	
479	float	_FFT_UL1[44]	V	
481	float	_FFT_UL1[45]	V	

Address	Format	Designation	Unit	Note
483	float	_FFT_UL1[46]	V	
485	float	_FFT_UL1[47]	V	
487	float	_FFT_UL1[48]	V	
489	float	_FFT_UL1[49]	V	
491	float	_FFT_UL1[50]	V	
493	float	_FFT_UL1[51]	V	
495	float	_FFT_UL1[52]	V	
497	float	_FFT_UL1[53]	V	
499	float	_FFT_UL1[54]	V	
501	float	_FFT_UL1[55]	V	
503	float	_FFT_UL1[56]	V	
505	float	_FFT_UL1[57]	V	
507	float	_FFT_UL1[58]	V	
509	float	_FFT_UL1[59]	V	
511	float	_FFT_UL1[60]	V	
513	float	_FFT_UL1[61]	V	
515	float	_FFT_UL1[62]	V	
517	float	_FFT_UL2[0]	V	
519	float	_FFT_UL2[1]	V	
521	float	_FFT_UL2[2]	V	
523	float	_FFT_UL2[3]	V	
525	float	_FFT_UL2[4]	V	
527	float	_FFT_UL2[5]	V	
529	float	_FFT_UL2[6]	V	
531	float	_FFT_UL2[7]	V	
533	float	_FFT_UL2[8]	V	
535	float	_FFT_UL2[9]	V	
537	float	_FFT_UL2[10]	V	
539	float	_FFT_UL2[11]	V	
541	float	_FFT_UL2[12]	V	
543	float	_FFT_UL2[13]	V	
545	float	_FFT_UL2[14]	V	
547	float	_FFT_UL2[15]	V	
549	float	_FFT_UL2[16]	V	
551	float	_FFT_UL2[17]	V	
553	float	_FFT_UL2[18]	V	
555	float	_FFT_UL2[19]	V	
557	float	_FFT_UL2[20]	V	
559	float	_FFT_UL2[21]	V	
561	float	_FFT_UL2[22]	V	
563	float	_FFT_UL2[23]	V	
565	float	_FFT_UL2[24]	V	
567	float	_FFT_UL2[25]	V	
569	float	_FFT_UL2[26]	V	
571	float	_FFT_UL2[27]	V	
573	float	_FFT_UL2[28]	V	
575	float	_FFT_UL2[29]	V	
577	float	_FFT_UL2[30]	V	
579	float	_FFT_UL2[31]	V	
581	float	_FFT_UL2[32]	V	
583	float	_FFT_UL2[33]	V	
585	float	_FFT_UL2[34]	V	
587	float	_FFT_UL2[35]	V	
589	float	_FFT_UL2[36]	V	
591	float	_FFT_UL2[37]	V	
593	float	_FFT_UL2[38]	V	
595	float	_FFT_UL2[39]	V	
597	float	_FFT_UL2[40]	V	
599	float	_FFT_UL2[41]	V	

Address	Format	Designation	Unit	Note
601	float	_FFT_UL2[42]	V	
603	float	_FFT_UL2[43]	V	
605	float	_FFT_UL2[44]	V	
607	float	_FFT_UL2[45]	V	
609	float	_FFT_UL2[46]	V	
611	float	_FFT_UL2[47]	V	
613	float	_FFT_UL2[48]	V	
615	float	_FFT_UL2[49]	V	
617	float	_FFT_UL2[50]	V	
619	float	_FFT_UL2[51]	V	
621	float	_FFT_UL2[52]	V	
623	float	_FFT_UL2[53]	V	
625	float	_FFT_UL2[54]	V	
627	float	_FFT_UL2[55]	V	
629	float	_FFT_UL2[56]	V	
631	float	_FFT_UL2[57]	V	
633	float	_FFT_UL2[58]	V	
635	float	_FFT_UL2[59]	V	
637	float	_FFT_UL2[60]	V	
639	float	_FFT_UL2[61]	V	
641	float	_FFT_UL2[62]	V	
643	float	_FFT_UL3[0]	V	
645	float	_FFT_UL3[1]	V	
647	float	_FFT_UL3[2]	V	
649	float	_FFT_UL3[3]	V	
651	float	_FFT_UL3[4]	V	
653	float	_FFT_UL3[5]	V	
655	float	_FFT_UL3[6]	V	
657	float	_FFT_UL3[7]	V	
659	float	_FFT_UL3[8]	V	
661	float	_FFT_UL3[9]	V	
663	float	_FFT_UL3[10]	V	
665	float	_FFT_UL3[11]	V	
667	float	_FFT_UL3[12]	V	
669	float	_FFT_UL3[13]	V	
671	float	_FFT_UL3[14]	V	
673	float	_FFT_UL3[15]	V	
675	float	_FFT_UL3[16]	V	
677	float	_FFT_UL3[17]	V	
679	float	_FFT_UL3[18]	V	
681	float	_FFT_UL3[19]	V	
683	float	_FFT_UL3[20]	V	
685	float	_FFT_UL3[21]	V	
687	float	_FFT_UL3[22]	V	
689	float	_FFT_UL3[23]	V	
691	float	_FFT_UL3[24]	V	
693	float	_FFT_UL3[25]	V	
695	float	_FFT_UL3[26]	V	
697	float	_FFT_UL3[27]	V	
699	float	_FFT_UL3[28]	V	
701	float	_FFT_UL3[29]	V	
703	float	_FFT_UL3[30]	V	
705	float	_FFT_UL3[31]	V	
707	float	_FFT_UL3[32]	V	
709	float	_FFT_UL3[33]	V	
711	float	_FFT_UL3[34]	V	
713	float	_FFT_UL3[35]	V	
715	float	_FFT_UL3[36]	V	
717	float	_FFT_UL3[37]	V	

Address	Format	Designation	Unit	Note
719	float	_FFT_UL3[38]	V	
721	float	_FFT_UL3[39]	V	
723	float	_FFT_UL3[40]	V	
725	float	_FFT_UL3[41]	V	
727	float	_FFT_UL3[42]	V	
729	float	_FFT_UL3[43]	V	
731	float	_FFT_UL3[44]	V	
733	float	_FFT_UL3[45]	V	
735	float	_FFT_UL3[46]	V	
737	float	_FFT_UL3[47]	V	
739	float	_FFT_UL3[48]	V	
741	float	_FFT_UL3[49]	V	
743	float	_FFT_UL3[50]	V	
745	float	_FFT_UL3[51]	V	
747	float	_FFT_UL3[52]	V	
749	float	_FFT_UL3[53]	V	
751	float	_FFT_UL3[54]	V	
753	float	_FFT_UL3[55]	V	
755	float	_FFT_UL3[56]	V	
757	float	_FFT_UL3[57]	V	
759	float	_FFT_UL3[58]	V	
761	float	_FFT_UL3[59]	V	
763	float	_FFT_UL3[60]	V	
765	float	_FFT_UL3[61]	V	
767	float	_FFT_UL3[62]	V	
769	float	_FFT_UL4[0]	V	
771	float	_FFT_UL4[1]	V	
773	float	_FFT_UL4[2]	V	
775	float	_FFT_UL4[3]	V	
777	float	_FFT_UL4[4]	V	
779	float	_FFT_UL4[5]	V	
781	float	_FFT_UL4[6]	V	
783	float	_FFT_UL4[7]	V	
785	float	_FFT_UL4[8]	V	
787	float	_FFT_UL4[9]	V	
789	float	_FFT_UL4[10]	V	
791	float	_FFT_UL4[11]	V	
793	float	_FFT_UL4[12]	V	
795	float	_FFT_UL4[13]	V	
797	float	_FFT_UL4[14]	V	
799	float	_FFT_UL4[15]	V	
801	float	_FFT_UL4[16]	V	
803	float	_FFT_UL4[17]	V	
805	float	_FFT_UL4[18]	V	
807	float	_FFT_UL4[19]	V	
809	float	_FFT_UL4[20]	V	
811	float	_FFT_UL4[21]	V	
813	float	_FFT_UL4[22]	V	
815	float	_FFT_UL4[23]	V	
817	float	_FFT_UL4[24]	V	
819	float	_FFT_UL4[25]	V	
821	float	_FFT_UL4[26]	V	
823	float	_FFT_UL4[27]	V	
825	float	_FFT_UL4[28]	V	
827	float	_FFT_UL4[29]	V	
829	float	_FFT_UL4[30]	V	
831	float	_FFT_UL4[31]	V	
833	float	_FFT_UL4[32]	V	
835	float	_FFT_UL4[33]	V	



Address	Format	Designation	Unit	Note
837	float	_FFT_UL4[34]	V	
839	float	_FFT_UL4[35]	V	
841	float	_FFT_UL4[36]	V	
843	float	_FFT_UL4[37]	V	
845	float	_FFT_UL4[38]	V	
847	float	_FFT_UL4[39]	V	
849	float	_FFT_UL4[40]	V	
851	float	_FFT_UL4[41]	V	
853	float	_FFT_UL4[42]	V	
855	float	_FFT_UL4[43]	V	
857	float	_FFT_UL4[44]	V	
859	float	_FFT_UL4[45]	V	
861	float	_FFT_UL4[46]	V	
863	float	_FFT_UL4[47]	V	
865	float	_FFT_UL4[48]	V	
867	float	_FFT_UL4[49]	V	
869	float	_FFT_UL4[50]	V	
871	float	_FFT_UL4[51]	V	
873	float	_FFT_UL4[52]	V	
875	float	_FFT_UL4[53]	V	
877	float	_FFT_UL4[54]	V	
879	float	_FFT_UL4[55]	V	
881	float	_FFT_UL4[56]	V	
883	float	_FFT_UL4[57]	V	
885	float	_FFT_UL4[58]	V	
887	float	_FFT_UL4[59]	V	
889	float	_FFT_UL4[60]	V	
891	float	_FFT_UL4[61]	V	
893	float	_FFT_UL4[62]	V	
895	float	_FFT_IL1[0]	A	
897	float	_FFT_IL1[1]	A	
899	float	_FFT_IL1[2]	A	
901	float	_FFT_IL1[3]	A	
903	float	_FFT_IL1[4]	A	
905	float	_FFT_IL1[5]	A	
907	float	_FFT_IL1[6]	A	
909	float	_FFT_IL1[7]	A	
911	float	_FFT_IL1[8]	A	
913	float	_FFT_IL1[9]	A	
915	float	_FFT_IL1[10]	A	
917	float	_FFT_IL1[11]	A	
919	float	_FFT_IL1[12]	A	
921	float	_FFT_IL1[13]	A	
923	float	_FFT_IL1[14]	A	
925	float	_FFT_IL1[15]	A	
927	float	_FFT_IL1[16]	A	
929	float	_FFT_IL1[17]	A	
931	float	_FFT_IL1[18]	A	
933	float	_FFT_IL1[19]	A	
935	float	_FFT_IL1[20]	A	
937	float	_FFT_IL1[21]	A	
939	float	_FFT_IL1[22]	A	
941	float	_FFT_IL1[23]	A	
943	float	_FFT_IL1[24]	A	
945	float	_FFT_IL1[25]	A	
947	float	_FFT_IL1[26]	A	
949	float	_FFT_IL1[27]	A	
951	float	_FFT_IL1[28]	A	
953	float	_FFT_IL1[29]	A	

Address	Format	Designation	Unit	Note
955	float	_FFT_IL1[30]	A	
957	float	_FFT_IL1[31]	A	
959	float	_FFT_IL1[32]	A	
961	float	_FFT_IL1[33]	A	
963	float	_FFT_IL1[34]	A	
965	float	_FFT_IL1[35]	A	
967	float	_FFT_IL1[36]	A	
969	float	_FFT_IL1[37]	A	
971	float	_FFT_IL1[38]	A	
973	float	_FFT_IL1[39]	A	
975	float	_FFT_IL1[40]	A	
977	float	_FFT_IL1[41]	A	
979	float	_FFT_IL1[42]	A	
981	float	_FFT_IL1[43]	A	
983	float	_FFT_IL1[44]	A	
985	float	_FFT_IL1[45]	A	
987	float	_FFT_IL1[46]	A	
989	float	_FFT_IL1[47]	A	
991	float	_FFT_IL1[48]	A	
993	float	_FFT_IL1[49]	A	
995	float	_FFT_IL1[50]	A	
997	float	_FFT_IL1[51]	A	
999	float	_FFT_IL1[52]	A	
1001	float	_FFT_IL1[53]	A	
1003	float	_FFT_IL1[54]	A	
1005	float	_FFT_IL1[55]	A	
1007	float	_FFT_IL1[56]	A	
1009	float	_FFT_IL1[57]	A	
1011	float	_FFT_IL1[58]	A	
1013	float	_FFT_IL1[59]	A	
1015	float	_FFT_IL1[60]	A	
1017	float	_FFT_IL1[61]	A	
1019	float	_FFT_IL1[62]	A	
1021	float	_FFT_IL2[0]	A	
1023	float	_FFT_IL2[1]	A	
1025	float	_FFT_IL2[2]	A	
1027	float	_FFT_IL2[3]	A	
1029	float	_FFT_IL2[4]	A	
1031	float	_FFT_IL2[5]	A	
1033	float	_FFT_IL2[6]	A	
1035	float	_FFT_IL2[7]	A	
1037	float	_FFT_IL2[8]	A	
1039	float	_FFT_IL2[9]	A	
1041	float	_FFT_IL2[10]	A	
1043	float	_FFT_IL2[11]	A	
1045	float	_FFT_IL2[12]	A	
1047	float	_FFT_IL2[13]	A	
1049	float	_FFT_IL2[14]	A	
1051	float	_FFT_IL2[15]	A	
1053	float	_FFT_IL2[16]	A	
1055	float	_FFT_IL2[17]	A	
1057	float	_FFT_IL2[18]	A	
1059	float	_FFT_IL2[19]	A	
1061	float	_FFT_IL2[20]	A	
1063	float	_FFT_IL2[21]	A	
1065	float	_FFT_IL2[22]	A	
1067	float	_FFT_IL2[23]	A	
1069	float	_FFT_IL2[24]	A	
1071	float	_FFT_IL2[25]	A	

Address	Format	Designation	Unit	Note
1073	float	_FFT_IL2[26]	A	
1075	float	_FFT_IL2[27]	A	
1077	float	_FFT_IL2[28]	A	
1079	float	_FFT_IL2[29]	A	
1081	float	_FFT_IL2[30]	A	
1083	float	_FFT_IL2[31]	A	
1085	float	_FFT_IL2[32]	A	
1087	float	_FFT_IL2[33]	A	
1089	float	_FFT_IL2[34]	A	
1091	float	_FFT_IL2[35]	A	
1093	float	_FFT_IL2[36]	A	
1095	float	_FFT_IL2[37]	A	
1097	float	_FFT_IL2[38]	A	
1099	float	_FFT_IL2[39]	A	
1101	float	_FFT_IL2[40]	A	
1103	float	_FFT_IL2[41]	A	
1105	float	_FFT_IL2[42]	A	
1107	float	_FFT_IL2[43]	A	
1109	float	_FFT_IL2[44]	A	
1111	float	_FFT_IL2[45]	A	
1113	float	_FFT_IL2[46]	A	
1115	float	_FFT_IL2[47]	A	
1117	float	_FFT_IL2[48]	A	
1119	float	_FFT_IL2[49]	A	
1121	float	_FFT_IL2[50]	A	
1123	float	_FFT_IL2[51]	A	
1125	float	_FFT_IL2[52]	A	
1127	float	_FFT_IL2[53]	A	
1129	float	_FFT_IL2[54]	A	
1131	float	_FFT_IL2[55]	A	
1133	float	_FFT_IL2[56]	A	
1135	float	_FFT_IL2[57]	A	
1137	float	_FFT_IL2[58]	A	
1139	float	_FFT_IL2[59]	A	
1141	float	_FFT_IL2[60]	A	
1143	float	_FFT_IL2[61]	A	
1145	float	_FFT_IL2[62]	A	
1147	float	_FFT_IL3[0]	A	
1149	float	_FFT_IL3[1]	A	
1151	float	_FFT_IL3[2]	A	
1153	float	_FFT_IL3[3]	A	
1155	float	_FFT_IL3[4]	A	
1157	float	_FFT_IL3[5]	A	
1159	float	_FFT_IL3[6]	A	
1161	float	_FFT_IL3[7]	A	
1163	float	_FFT_IL3[8]	A	
1165	float	_FFT_IL3[9]	A	
1167	float	_FFT_IL3[10]	A	
1169	float	_FFT_IL3[11]	A	
1171	float	_FFT_IL3[12]	A	
1173	float	_FFT_IL3[13]	A	
1175	float	_FFT_IL3[14]	A	
1177	float	_FFT_IL3[15]	A	
1179	float	_FFT_IL3[16]	A	
1181	float	_FFT_IL3[17]	A	
1183	float	_FFT_IL3[18]	A	
1185	float	_FFT_IL3[19]	A	
1187	float	_FFT_IL3[20]	A	
1189	float	_FFT_IL3[21]	A	

Address	Format	Designation	Unit	Note
1191	float	_FFT_IL3[22]	A	
1193	float	_FFT_IL3[23]	A	
1195	float	_FFT_IL3[24]	A	
1197	float	_FFT_IL3[25]	A	
1199	float	_FFT_IL3[26]	A	
1201	float	_FFT_IL3[27]	A	
1203	float	_FFT_IL3[28]	A	
1205	float	_FFT_IL3[29]	A	
1207	float	_FFT_IL3[30]	A	
1209	float	_FFT_IL3[31]	A	
1211	float	_FFT_IL3[32]	A	
1213	float	_FFT_IL3[33]	A	
1215	float	_FFT_IL3[34]	A	
1217	float	_FFT_IL3[35]	A	
1219	float	_FFT_IL3[36]	A	
1221	float	_FFT_IL3[37]	A	
1223	float	_FFT_IL3[38]	A	
1225	float	_FFT_IL3[39]	A	
1227	float	_FFT_IL3[40]	A	
1229	float	_FFT_IL3[41]	A	
1231	float	_FFT_IL3[42]	A	
1233	float	_FFT_IL3[43]	A	
1235	float	_FFT_IL3[44]	A	
1237	float	_FFT_IL3[45]	A	
1239	float	_FFT_IL3[46]	A	
1241	float	_FFT_IL3[47]	A	
1243	float	_FFT_IL3[48]	A	
1245	float	_FFT_IL3[49]	A	
1247	float	_FFT_IL3[50]	A	
1249	float	_FFT_IL3[51]	A	
1251	float	_FFT_IL3[52]	A	
1253	float	_FFT_IL3[53]	A	
1255	float	_FFT_IL3[54]	A	
1257	float	_FFT_IL3[55]	A	
1259	float	_FFT_IL3[56]	A	
1261	float	_FFT_IL3[57]	A	
1263	float	_FFT_IL3[58]	A	
1265	float	_FFT_IL3[59]	A	
1267	float	_FFT_IL3[60]	A	
1269	float	_FFT_IL3[61]	A	
1271	float	_FFT_IL3[62]	A	
1273	float	_FFT_IL4[0]	A	
1275	float	_FFT_IL4[1]	A	
1277	float	_FFT_IL4[2]	A	
1279	float	_FFT_IL4[3]	A	
1281	float	_FFT_IL4[4]	A	
1283	float	_FFT_IL4[5]	A	
1285	float	_FFT_IL4[6]	A	
1287	float	_FFT_IL4[7]	A	
1289	float	_FFT_IL4[8]	A	
1291	float	_FFT_IL4[9]	A	
1293	float	_FFT_IL4[10]	A	
1295	float	_FFT_IL4[11]	A	
1297	float	_FFT_IL4[12]	A	
1299	float	_FFT_IL4[13]	A	
1301	float	_FFT_IL4[14]	A	
1303	float	_FFT_IL4[15]	A	
1305	float	_FFT_IL4[16]	A	
1307	float	_FFT_IL4[17]	A	

Address	Format	Designation	Unit	Note
1309	float	_FFT_IL4[18]	A	
1311	float	_FFT_IL4[19]	A	
1313	float	_FFT_IL4[20]	A	
1315	float	_FFT_IL4[21]	A	
1317	float	_FFT_IL4[22]	A	
1319	float	_FFT_IL4[23]	A	
1321	float	_FFT_IL4[24]	A	
1323	float	_FFT_IL4[25]	A	
1325	float	_FFT_IL4[26]	A	
1327	float	_FFT_IL4[27]	A	
1329	float	_FFT_IL4[28]	A	
1331	float	_FFT_IL4[29]	A	
1333	float	_FFT_IL4[30]	A	
1335	float	_FFT_IL4[31]	A	
1337	float	_FFT_IL4[32]	A	
1339	float	_FFT_IL4[33]	A	
1341	float	_FFT_IL4[34]	A	
1343	float	_FFT_IL4[35]	A	
1345	float	_FFT_IL4[36]	A	
1347	float	_FFT_IL4[37]	A	
1349	float	_FFT_IL4[38]	A	
1351	float	_FFT_IL4[39]	A	
1353	float	_FFT_IL4[40]	A	
1355	float	_FFT_IL4[41]	A	
1357	float	_FFT_IL4[42]	A	
1359	float	_FFT_IL4[43]	A	
1361	float	_FFT_IL4[44]	A	
1363	float	_FFT_IL4[45]	A	
1365	float	_FFT_IL4[46]	A	
1367	float	_FFT_IL4[47]	A	
1369	float	_FFT_IL4[48]	A	
1371	float	_FFT_IL4[49]	A	
1373	float	_FFT_IL4[50]	A	
1375	float	_FFT_IL4[51]	A	
1377	float	_FFT_IL4[52]	A	
1379	float	_FFT_IL4[53]	A	
1381	float	_FFT_IL4[54]	A	
1383	float	_FFT_IL4[55]	A	
1385	float	_FFT_IL4[56]	A	
1387	float	_FFT_IL4[57]	A	
1389	float	_FFT_IL4[58]	A	
1391	float	_FFT_IL4[59]	A	
1393	float	_FFT_IL4[60]	A	
1395	float	_FFT_IL4[61]	A	
1397	float	_FFT_IL4[62]	A	
1399	float	_FFT_PL1[0]	W	
1401	float	_FFT_PL1[1]	W	
1403	float	_FFT_PL1[2]	W	
1405	float	_FFT_PL1[3]	W	
1407	float	_FFT_PL1[4]	W	
1409	float	_FFT_PL1[5]	W	
1411	float	_FFT_PL1[6]	W	
1413	float	_FFT_PL1[7]	W	
1415	float	_FFT_PL1[8]	W	
1417	float	_FFT_PL1[9]	W	
1419	float	_FFT_PL1[10]	W	
1421	float	_FFT_PL1[11]	W	
1423	float	_FFT_PL1[12]	W	
1425	float	_FFT_PL1[13]	W	

Address	Format	Designation	Unit	Note
1427	float	_FFT_PL1[14]	W	
1429	float	_FFT_PL1[15]	W	
1431	float	_FFT_PL1[16]	W	
1433	float	_FFT_PL1[17]	W	
1435	float	_FFT_PL1[18]	W	
1437	float	_FFT_PL1[19]	W	
1439	float	_FFT_PL1[20]	W	
1441	float	_FFT_PL1[21]	W	
1443	float	_FFT_PL1[22]	W	
1445	float	_FFT_PL1[23]	W	
1447	float	_FFT_PL1[24]	W	
1449	float	_FFT_PL1[25]	W	
1451	float	_FFT_PL1[26]	W	
1453	float	_FFT_PL1[27]	W	
1455	float	_FFT_PL1[28]	W	
1457	float	_FFT_PL1[29]	W	
1459	float	_FFT_PL1[30]	W	
1461	float	_FFT_PL1[31]	W	
1463	float	_FFT_PL1[32]	W	
1465	float	_FFT_PL1[33]	W	
1467	float	_FFT_PL1[34]	W	
1469	float	_FFT_PL1[35]	W	
1471	float	_FFT_PL1[36]	W	
1473	float	_FFT_PL1[37]	W	
1475	float	_FFT_PL1[38]	W	
1477	float	_FFT_PL1[39]	W	
1479	float	_FFT_PL1[40]	W	
1481	float	_FFT_PL1[41]	W	
1483	float	_FFT_PL1[42]	W	
1485	float	_FFT_PL1[43]	W	
1487	float	_FFT_PL1[44]	W	
1489	float	_FFT_PL1[45]	W	
1491	float	_FFT_PL1[46]	W	
1493	float	_FFT_PL1[47]	W	
1495	float	_FFT_PL1[48]	W	
1497	float	_FFT_PL1[49]	W	
1499	float	_FFT_PL1[50]	W	
1501	float	_FFT_PL1[51]	W	
1503	float	_FFT_PL1[52]	W	
1505	float	_FFT_PL1[53]	W	
1507	float	_FFT_PL1[54]	W	
1509	float	_FFT_PL1[55]	W	
1511	float	_FFT_PL1[56]	W	
1513	float	_FFT_PL1[57]	W	
1515	float	_FFT_PL1[58]	W	
1517	float	_FFT_PL1[59]	W	
1519	float	_FFT_PL1[60]	W	
1521	float	_FFT_PL1[61]	W	
1523	float	_FFT_PL1[62]	W	
1525	float	_FFT_PL2[0]	W	
1527	float	_FFT_PL2[1]	W	
1529	float	_FFT_PL2[2]	W	
1531	float	_FFT_PL2[3]	W	
1533	float	_FFT_PL2[4]	W	
1535	float	_FFT_PL2[5]	W	
1537	float	_FFT_PL2[6]	W	
1539	float	_FFT_PL2[7]	W	
1541	float	_FFT_PL2[8]	W	
1543	float	_FFT_PL2[9]	W	

Address	Format	Designation	Unit	Note
1545	float	_FFT_PL2[10]	W	
1547	float	_FFT_PL2[11]	W	
1549	float	_FFT_PL2[12]	W	
1551	float	_FFT_PL2[13]	W	
1553	float	_FFT_PL2[14]	W	
1555	float	_FFT_PL2[15]	W	
1557	float	_FFT_PL2[16]	W	
1559	float	_FFT_PL2[17]	W	
1561	float	_FFT_PL2[18]	W	
1563	float	_FFT_PL2[19]	W	
1565	float	_FFT_PL2[20]	W	
1567	float	_FFT_PL2[21]	W	
1569	float	_FFT_PL2[22]	W	
1571	float	_FFT_PL2[23]	W	
1573	float	_FFT_PL2[24]	W	
1575	float	_FFT_PL2[25]	W	
1577	float	_FFT_PL2[26]	W	
1579	float	_FFT_PL2[27]	W	
1581	float	_FFT_PL2[28]	W	
1583	float	_FFT_PL2[29]	W	
1585	float	_FFT_PL2[30]	W	
1587	float	_FFT_PL2[31]	W	
1589	float	_FFT_PL2[32]	W	
1591	float	_FFT_PL2[33]	W	
1593	float	_FFT_PL2[34]	W	
1595	float	_FFT_PL2[35]	W	
1597	float	_FFT_PL2[36]	W	
1599	float	_FFT_PL2[37]	W	
1601	float	_FFT_PL2[38]	W	
1603	float	_FFT_PL2[39]	W	
1605	float	_FFT_PL2[40]	W	
1607	float	_FFT_PL2[41]	W	
1609	float	_FFT_PL2[42]	W	
1611	float	_FFT_PL2[43]	W	
1613	float	_FFT_PL2[44]	W	
1615	float	_FFT_PL2[45]	W	
1617	float	_FFT_PL2[46]	W	
1619	float	_FFT_PL2[47]	W	
1621	float	_FFT_PL2[48]	W	
1623	float	_FFT_PL2[49]	W	
1625	float	_FFT_PL2[50]	W	
1627	float	_FFT_PL2[51]	W	
1629	float	_FFT_PL2[52]	W	
1631	float	_FFT_PL2[53]	W	
1633	float	_FFT_PL2[54]	W	
1635	float	_FFT_PL2[55]	W	
1637	float	_FFT_PL2[56]	W	
1639	float	_FFT_PL2[57]	W	
1641	float	_FFT_PL2[58]	W	
1643	float	_FFT_PL2[59]	W	
1645	float	_FFT_PL2[60]	W	
1647	float	_FFT_PL2[61]	W	
1649	float	_FFT_PL2[62]	W	
1651	float	_FFT_PL3[0]	W	
1653	float	_FFT_PL3[1]	W	
1655	float	_FFT_PL3[2]	W	
1657	float	_FFT_PL3[3]	W	
1659	float	_FFT_PL3[4]	W	
1661	float	_FFT_PL3[5]	W	

Address	Format	Designation	Unit	Note
1663	float	_FFT_PL3[6]	W	
1665	float	_FFT_PL3[7]	W	
1667	float	_FFT_PL3[8]	W	
1669	float	_FFT_PL3[9]	W	
1671	float	_FFT_PL3[10]	W	
1673	float	_FFT_PL3[11]	W	
1675	float	_FFT_PL3[12]	W	
1677	float	_FFT_PL3[13]	W	
1679	float	_FFT_PL3[14]	W	
1681	float	_FFT_PL3[15]	W	
1683	float	_FFT_PL3[16]	W	
1685	float	_FFT_PL3[17]	W	
1687	float	_FFT_PL3[18]	W	
1689	float	_FFT_PL3[19]	W	
1691	float	_FFT_PL3[20]	W	
1693	float	_FFT_PL3[21]	W	
1695	float	_FFT_PL3[22]	W	
1697	float	_FFT_PL3[23]	W	
1699	float	_FFT_PL3[24]	W	
1701	float	_FFT_PL3[25]	W	
1703	float	_FFT_PL3[26]	W	
1705	float	_FFT_PL3[27]	W	
1707	float	_FFT_PL3[28]	W	
1709	float	_FFT_PL3[29]	W	
1711	float	_FFT_PL3[30]	W	
1713	float	_FFT_PL3[31]	W	
1715	float	_FFT_PL3[32]	W	
1717	float	_FFT_PL3[33]	W	
1719	float	_FFT_PL3[34]	W	
1721	float	_FFT_PL3[35]	W	
1723	float	_FFT_PL3[36]	W	
1725	float	_FFT_PL3[37]	W	
1727	float	_FFT_PL3[38]	W	
1729	float	_FFT_PL3[39]	W	
1731	float	_FFT_PL3[40]	W	
1733	float	_FFT_PL3[41]	W	
1735	float	_FFT_PL3[42]	W	
1737	float	_FFT_PL3[43]	W	
1739	float	_FFT_PL3[44]	W	
1741	float	_FFT_PL3[45]	W	
1743	float	_FFT_PL3[46]	W	
1745	float	_FFT_PL3[47]	W	
1747	float	_FFT_PL3[48]	W	
1749	float	_FFT_PL3[49]	W	
1751	float	_FFT_PL3[50]	W	
1753	float	_FFT_PL3[51]	W	
1755	float	_FFT_PL3[52]	W	
1757	float	_FFT_PL3[53]	W	
1759	float	_FFT_PL3[54]	W	
1761	float	_FFT_PL3[55]	W	
1763	float	_FFT_PL3[56]	W	
1765	float	_FFT_PL3[57]	W	
1767	float	_FFT_PL3[58]	W	
1769	float	_FFT_PL3[59]	W	
1771	float	_FFT_PL3[60]	W	
1773	float	_FFT_PL3[61]	W	
1775	float	_FFT_PL3[62]	W	
1777	float	_FFT_PL4[0]	W	
1779	float	_FFT_PL4[1]	W	



Address	Format	Designation	Unit	Note
1781	float	_FFT_PL4[2]	W	
1783	float	_FFT_PL4[3]	W	
1785	float	_FFT_PL4[4]	W	
1787	float	_FFT_PL4[5]	W	
1789	float	_FFT_PL4[6]	W	
1791	float	_FFT_PL4[7]	W	
1793	float	_FFT_PL4[8]	W	
1795	float	_FFT_PL4[9]	W	
1797	float	_FFT_PL4[10]	W	
1799	float	_FFT_PL4[11]	W	
1801	float	_FFT_PL4[12]	W	
1803	float	_FFT_PL4[13]	W	
1805	float	_FFT_PL4[14]	W	
1807	float	_FFT_PL4[15]	W	
1809	float	_FFT_PL4[16]	W	
1811	float	_FFT_PL4[17]	W	
1813	float	_FFT_PL4[18]	W	
1815	float	_FFT_PL4[19]	W	
1817	float	_FFT_PL4[20]	W	
1819	float	_FFT_PL4[21]	W	
1821	float	_FFT_PL4[22]	W	
1823	float	_FFT_PL4[23]	W	
1825	float	_FFT_PL4[24]	W	
1827	float	_FFT_PL4[25]	W	
1829	float	_FFT_PL4[26]	W	
1831	float	_FFT_PL4[27]	W	
1833	float	_FFT_PL4[28]	W	
1835	float	_FFT_PL4[29]	W	
1837	float	_FFT_PL4[30]	W	
1839	float	_FFT_PL4[31]	W	
1841	float	_FFT_PL4[32]	W	
1843	float	_FFT_PL4[33]	W	
1845	float	_FFT_PL4[34]	W	
1847	float	_FFT_PL4[35]	W	
1849	float	_FFT_PL4[36]	W	
1851	float	_FFT_PL4[37]	W	
1853	float	_FFT_PL4[38]	W	
1855	float	_FFT_PL4[39]	W	
1857	float	_FFT_PL4[40]	W	
1859	float	_FFT_PL4[41]	W	
1861	float	_FFT_PL4[42]	W	
1863	float	_FFT_PL4[43]	W	
1865	float	_FFT_PL4[44]	W	
1867	float	_FFT_PL4[45]	W	
1869	float	_FFT_PL4[46]	W	
1871	float	_FFT_PL4[47]	W	
1873	float	_FFT_PL4[48]	W	
1875	float	_FFT_PL4[49]	W	
1877	float	_FFT_PL4[50]	W	
1879	float	_FFT_PL4[51]	W	
1881	float	_FFT_PL4[52]	W	
1883	float	_FFT_PL4[53]	W	
1885	float	_FFT_PL4[54]	W	
1887	float	_FFT_PL4[55]	W	
1889	float	_FFT_PL4[56]	W	
1891	float	_FFT_PL4[57]	W	
1893	float	_FFT_PL4[58]	W	
1895	float	_FFT_PL4[59]	W	
1897	float	_FFT_PL4[60]	W	

Address	Format	Designation	Unit	Note
1899	float	_FFT_PL4[61]	W	
1901	float	_FFT_PL4[62]	W	
1903	float	_FFT_QL1[0]	var	
1905	float	_FFT_QL1[1]	var	
1907	float	_FFT_QL1[2]	var	
1909	float	_FFT_QL1[3]	var	
1911	float	_FFT_QL1[4]	var	
1913	float	_FFT_QL1[5]	var	
1915	float	_FFT_QL1[6]	var	
1917	float	_FFT_QL1[7]	var	
1919	float	_FFT_QL1[8]	var	
1921	float	_FFT_QL1[9]	var	
1923	float	_FFT_QL1[10]	var	
1925	float	_FFT_QL1[11]	var	
1927	float	_FFT_QL1[12]	var	
1929	float	_FFT_QL1[13]	var	
1931	float	_FFT_QL1[14]	var	
1933	float	_FFT_QL1[15]	var	
1935	float	_FFT_QL1[16]	var	
1937	float	_FFT_QL1[17]	var	
1939	float	_FFT_QL1[18]	var	
1941	float	_FFT_QL1[19]	var	
1943	float	_FFT_QL1[20]	var	
1945	float	_FFT_QL1[21]	var	
1947	float	_FFT_QL1[22]	var	
1949	float	_FFT_QL1[23]	var	
1951	float	_FFT_QL1[24]	var	
1953	float	_FFT_QL1[25]	var	
1955	float	_FFT_QL1[26]	var	
1957	float	_FFT_QL1[27]	var	
1959	float	_FFT_QL1[28]	var	
1961	float	_FFT_QL1[29]	var	
1963	float	_FFT_QL1[30]	var	
1965	float	_FFT_QL1[31]	var	
1967	float	_FFT_QL1[32]	var	
1969	float	_FFT_QL1[33]	var	
1971	float	_FFT_QL1[34]	var	
1973	float	_FFT_QL1[35]	var	
1975	float	_FFT_QL1[36]	var	
1977	float	_FFT_QL1[37]	var	
1979	float	_FFT_QL1[38]	var	
1981	float	_FFT_QL1[39]	var	
1983	float	_FFT_QL1[40]	var	
1985	float	_FFT_QL1[41]	var	
1987	float	_FFT_QL1[42]	var	
1989	float	_FFT_QL1[43]	var	
1991	float	_FFT_QL1[44]	var	
1993	float	_FFT_QL1[45]	var	
1995	float	_FFT_QL1[46]	var	
1997	float	_FFT_QL1[47]	var	
1999	float	_FFT_QL1[48]	var	
2001	float	_FFT_QL1[49]	var	
2003	float	_FFT_QL1[50]	var	
2005	float	_FFT_QL1[51]	var	
2007	float	_FFT_QL1[52]	var	
2009	float	_FFT_QL1[53]	var	
2011	float	_FFT_QL1[54]	var	
2013	float	_FFT_QL1[55]	var	
2015	float	_FFT_QL1[56]	var	

Address	Format	Designation	Unit	Note
2017	float	_FFT_QL1[57]	var	
2019	float	_FFT_QL1[58]	var	
2021	float	_FFT_QL1[59]	var	
2023	float	_FFT_QL1[60]	var	
2025	float	_FFT_QL1[61]	var	
2027	float	_FFT_QL1[62]	var	
2029	float	_FFT_QL2[0]	var	
2031	float	_FFT_QL2[1]	var	
2033	float	_FFT_QL2[2]	var	
2035	float	_FFT_QL2[3]	var	
2037	float	_FFT_QL2[4]	var	
2039	float	_FFT_QL2[5]	var	
2041	float	_FFT_QL2[6]	var	
2043	float	_FFT_QL2[7]	var	
2045	float	_FFT_QL2[8]	var	
2047	float	_FFT_QL2[9]	var	
2049	float	_FFT_QL2[10]	var	
2051	float	_FFT_QL2[11]	var	
2053	float	_FFT_QL2[12]	var	
2055	float	_FFT_QL2[13]	var	
2057	float	_FFT_QL2[14]	var	
2059	float	_FFT_QL2[15]	var	
2061	float	_FFT_QL2[16]	var	
2063	float	_FFT_QL2[17]	var	
2065	float	_FFT_QL2[18]	var	
2067	float	_FFT_QL2[19]	var	
2069	float	_FFT_QL2[20]	var	
2071	float	_FFT_QL2[21]	var	
2073	float	_FFT_QL2[22]	var	
2075	float	_FFT_QL2[23]	var	
2077	float	_FFT_QL2[24]	var	
2079	float	_FFT_QL2[25]	var	
2081	float	_FFT_QL2[26]	var	
2083	float	_FFT_QL2[27]	var	
2085	float	_FFT_QL2[28]	var	
2087	float	_FFT_QL2[29]	var	
2089	float	_FFT_QL2[30]	var	
2091	float	_FFT_QL2[31]	var	
2093	float	_FFT_QL2[32]	var	
2095	float	_FFT_QL2[33]	var	
2097	float	_FFT_QL2[34]	var	
2099	float	_FFT_QL2[35]	var	
2101	float	_FFT_QL2[36]	var	
2103	float	_FFT_QL2[37]	var	
2105	float	_FFT_QL2[38]	var	
2107	float	_FFT_QL2[39]	var	
2109	float	_FFT_QL2[40]	var	
2111	float	_FFT_QL2[41]	var	
2113	float	_FFT_QL2[42]	var	
2115	float	_FFT_QL2[43]	var	
2117	float	_FFT_QL2[44]	var	
2119	float	_FFT_QL2[45]	var	
2121	float	_FFT_QL2[46]	var	
2123	float	_FFT_QL2[47]	var	
2125	float	_FFT_QL2[48]	var	
2127	float	_FFT_QL2[49]	var	
2129	float	_FFT_QL2[50]	var	
2131	float	_FFT_QL2[51]	var	
2133	float	_FFT_QL2[52]	var	

Address	Format	Designation	Unit	Note
2135	float	_FFT_QL2[53]	var	
2137	float	_FFT_QL2[54]	var	
2139	float	_FFT_QL2[55]	var	
2141	float	_FFT_QL2[56]	var	
2143	float	_FFT_QL2[57]	var	
2145	float	_FFT_QL2[58]	var	
2147	float	_FFT_QL2[59]	var	
2149	float	_FFT_QL2[60]	var	
2151	float	_FFT_QL2[61]	var	
2153	float	_FFT_QL2[62]	var	
2155	float	_FFT_QL3[0]	var	
2157	float	_FFT_QL3[1]	var	
2159	float	_FFT_QL3[2]	var	
2161	float	_FFT_QL3[3]	var	
2163	float	_FFT_QL3[4]	var	
2165	float	_FFT_QL3[5]	var	
2167	float	_FFT_QL3[6]	var	
2169	float	_FFT_QL3[7]	var	
2171	float	_FFT_QL3[8]	var	
2173	float	_FFT_QL3[9]	var	
2175	float	_FFT_QL3[10]	var	
2177	float	_FFT_QL3[11]	var	
2179	float	_FFT_QL3[12]	var	
2181	float	_FFT_QL3[13]	var	
2183	float	_FFT_QL3[14]	var	
2185	float	_FFT_QL3[15]	var	
2187	float	_FFT_QL3[16]	var	
2189	float	_FFT_QL3[17]	var	
2191	float	_FFT_QL3[18]	var	
2193	float	_FFT_QL3[19]	var	
2195	float	_FFT_QL3[20]	var	
2197	float	_FFT_QL3[21]	var	
2199	float	_FFT_QL3[22]	var	
2201	float	_FFT_QL3[23]	var	
2203	float	_FFT_QL3[24]	var	
2205	float	_FFT_QL3[25]	var	
2207	float	_FFT_QL3[26]	var	
2209	float	_FFT_QL3[27]	var	
2211	float	_FFT_QL3[28]	var	
2213	float	_FFT_QL3[29]	var	
2215	float	_FFT_QL3[30]	var	
2217	float	_FFT_QL3[31]	var	
2219	float	_FFT_QL3[32]	var	
2221	float	_FFT_QL3[33]	var	
2223	float	_FFT_QL3[34]	var	
2225	float	_FFT_QL3[35]	var	
2227	float	_FFT_QL3[36]	var	
2229	float	_FFT_QL3[37]	var	
2231	float	_FFT_QL3[38]	var	
2233	float	_FFT_QL3[39]	var	
2235	float	_FFT_QL3[40]	var	
2237	float	_FFT_QL3[41]	var	
2239	float	_FFT_QL3[42]	var	
2241	float	_FFT_QL3[43]	var	
2243	float	_FFT_QL3[44]	var	
2245	float	_FFT_QL3[45]	var	
2247	float	_FFT_QL3[46]	var	
2249	float	_FFT_QL3[47]	var	
2251	float	_FFT_QL3[48]	var	

Address	Format	Designation	Unit	Note
2253	float	_FFT_QL3[49]	var	
2255	float	_FFT_QL3[50]	var	
2257	float	_FFT_QL3[51]	var	
2259	float	_FFT_QL3[52]	var	
2261	float	_FFT_QL3[53]	var	
2263	float	_FFT_QL3[54]	var	
2265	float	_FFT_QL3[55]	var	
2267	float	_FFT_QL3[56]	var	
2269	float	_FFT_QL3[57]	var	
2271	float	_FFT_QL3[58]	var	
2273	float	_FFT_QL3[59]	var	
2275	float	_FFT_QL3[60]	var	
2277	float	_FFT_QL3[61]	var	
2279	float	_FFT_QL3[62]	var	
2281	float	_FFT_QL4[0]	var	
2283	float	_FFT_QL4[1]	var	
2285	float	_FFT_QL4[2]	var	
2287	float	_FFT_QL4[3]	var	
2289	float	_FFT_QL4[4]	var	
2291	float	_FFT_QL4[5]	var	
2293	float	_FFT_QL4[6]	var	
2295	float	_FFT_QL4[7]	var	
2297	float	_FFT_QL4[8]	var	
2299	float	_FFT_QL4[9]	var	
2301	float	_FFT_QL4[10]	var	
2303	float	_FFT_QL4[11]	var	
2305	float	_FFT_QL4[12]	var	
2307	float	_FFT_QL4[13]	var	
2309	float	_FFT_QL4[14]	var	
2311	float	_FFT_QL4[15]	var	
2313	float	_FFT_QL4[16]	var	
2315	float	_FFT_QL4[17]	var	
2317	float	_FFT_QL4[18]	var	
2319	float	_FFT_QL4[19]	var	
2321	float	_FFT_QL4[20]	var	
2323	float	_FFT_QL4[21]	var	
2325	float	_FFT_QL4[22]	var	
2327	float	_FFT_QL4[23]	var	
2329	float	_FFT_QL4[24]	var	
2331	float	_FFT_QL4[25]	var	
2333	float	_FFT_QL4[26]	var	
2335	float	_FFT_QL4[27]	var	
2337	float	_FFT_QL4[28]	var	
2339	float	_FFT_QL4[29]	var	
2341	float	_FFT_QL4[30]	var	
2343	float	_FFT_QL4[31]	var	
2345	float	_FFT_QL4[32]	var	
2347	float	_FFT_QL4[33]	var	
2349	float	_FFT_QL4[34]	var	
2351	float	_FFT_QL4[35]	var	
2353	float	_FFT_QL4[36]	var	
2355	float	_FFT_QL4[37]	var	
2357	float	_FFT_QL4[38]	var	
2359	float	_FFT_QL4[39]	var	
2361	float	_FFT_QL4[40]	var	
2363	float	_FFT_QL4[41]	var	
2365	float	_FFT_QL4[42]	var	
2367	float	_FFT_QL4[43]	var	
2369	float	_FFT_QL4[44]	var	

Address	Format	Designation	Unit	Note
2371	float	_FFT_QL4[45]	var	
2373	float	_FFT_QL4[46]	var	
2375	float	_FFT_QL4[47]	var	
2377	float	_FFT_QL4[48]	var	
2379	float	_FFT_QL4[49]	var	
2381	float	_FFT_QL4[50]	var	
2383	float	_FFT_QL4[51]	var	
2385	float	_FFT_QL4[52]	var	
2387	float	_FFT_QL4[53]	var	
2389	float	_FFT_QL4[54]	var	
2391	float	_FFT_QL4[55]	var	
2393	float	_FFT_QL4[56]	var	
2395	float	_FFT_QL4[57]	var	
2397	float	_FFT_QL4[58]	var	
2399	float	_FFT_QL4[59]	var	
2401	float	_FFT_QL4[60]	var	
2403	float	_FFT_QL4[61]	var	
2405	float	_FFT_QL4[62]	var	
2407	float	_FFT_U LLZ1[0]	V	
2409	float	_FFT_U LLZ1[1]	V	
2411	float	_FFT_U LLZ1[2]	V	
2413	float	_FFT_U LLZ1[3]	V	
2415	float	_FFT_U LLZ1[4]	V	
2417	float	_FFT_U LLZ1[5]	V	
2419	float	_FFT_U LLZ1[6]	V	
2421	float	_FFT_U LLZ1[7]	V	
2423	float	_FFT_U LLZ1[8]	V	
2425	float	_FFT_U LLZ1[9]	V	
2427	float	_FFT_U LLZ1[10]	V	
2429	float	_FFT_U LLZ1[11]	V	
2431	float	_FFT_U LLZ1[12]	V	
2433	float	_FFT_U LLZ1[13]	V	
2435	float	_FFT_U LLZ1[14]	V	
2437	float	_FFT_U LLZ1[15]	V	
2439	float	_FFT_U LLZ1[16]	V	
2441	float	_FFT_U LLZ1[17]	V	
2443	float	_FFT_U LLZ1[18]	V	
2445	float	_FFT_U LLZ1[19]	V	
2447	float	_FFT_U LLZ1[20]	V	
2449	float	_FFT_U LLZ1[21]	V	
2451	float	_FFT_U LLZ1[22]	V	
2453	float	_FFT_U LLZ1[23]	V	
2455	float	_FFT_U LLZ1[24]	V	
2457	float	_FFT_U LLZ1[25]	V	
2459	float	_FFT_U LLZ1[26]	V	
2461	float	_FFT_U LLZ1[27]	V	
2463	float	_FFT_U LLZ1[28]	V	
2465	float	_FFT_U LLZ1[29]	V	
2467	float	_FFT_U LLZ1[30]	V	
2469	float	_FFT_U LLZ1[31]	V	
2471	float	_FFT_U LLZ1[32]	V	
2473	float	_FFT_U LLZ1[33]	V	
2475	float	_FFT_U LLZ1[34]	V	
2477	float	_FFT_U LLZ1[35]	V	
2479	float	_FFT_U LLZ1[36]	V	
2481	float	_FFT_U LLZ1[37]	V	
2483	float	_FFT_U LLZ1[38]	V	
2485	float	_FFT_U LLZ1[39]	V	
2487	float	_FFT_U LLZ1[40]	V	

Address	Format	Designation	Unit	Note
2489	float	_FFT_ULLZ1[41]	V	
2491	float	_FFT_ULLZ1[42]	V	
2493	float	_FFT_ULLZ1[43]	V	
2495	float	_FFT_ULLZ1[44]	V	
2497	float	_FFT_ULLZ1[45]	V	
2499	float	_FFT_ULLZ1[46]	V	
2501	float	_FFT_ULLZ1[47]	V	
2503	float	_FFT_ULLZ1[48]	V	
2505	float	_FFT_ULLZ1[49]	V	
2507	float	_FFT_ULLZ1[50]	V	
2509	float	_FFT_ULLZ1[51]	V	
2511	float	_FFT_ULLZ1[52]	V	
2513	float	_FFT_ULLZ1[53]	V	
2515	float	_FFT_ULLZ1[54]	V	
2517	float	_FFT_ULLZ1[55]	V	
2519	float	_FFT_ULLZ1[56]	V	
2521	float	_FFT_ULLZ1[57]	V	
2523	float	_FFT_ULLZ1[58]	V	
2525	float	_FFT_ULLZ1[59]	V	
2527	float	_FFT_ULLZ1[60]	V	
2529	float	_FFT_ULLZ1[61]	V	
2531	float	_FFT_ULLZ1[62]	V	
2533	float	_FFT_ULLZ2[0]	V	
2535	float	_FFT_ULLZ2[1]	V	
2537	float	_FFT_ULLZ2[2]	V	
2539	float	_FFT_ULLZ2[3]	V	
2541	float	_FFT_ULLZ2[4]	V	
2543	float	_FFT_ULLZ2[5]	V	
2545	float	_FFT_ULLZ2[6]	V	
2547	float	_FFT_ULLZ2[7]	V	
2549	float	_FFT_ULLZ2[8]	V	
2551	float	_FFT_ULLZ2[9]	V	
2553	float	_FFT_ULLZ2[10]	V	
2555	float	_FFT_ULLZ2[11]	V	
2557	float	_FFT_ULLZ2[12]	V	
2559	float	_FFT_ULLZ2[13]	V	
2561	float	_FFT_ULLZ2[14]	V	
2563	float	_FFT_ULLZ2[15]	V	
2565	float	_FFT_ULLZ2[16]	V	
2567	float	_FFT_ULLZ2[17]	V	
2569	float	_FFT_ULLZ2[18]	V	
2571	float	_FFT_ULLZ2[19]	V	
2573	float	_FFT_ULLZ2[20]	V	
2575	float	_FFT_ULLZ2[21]	V	
2577	float	_FFT_ULLZ2[22]	V	
2579	float	_FFT_ULLZ2[23]	V	
2581	float	_FFT_ULLZ2[24]	V	
2583	float	_FFT_ULLZ2[25]	V	
2585	float	_FFT_ULLZ2[26]	V	
2587	float	_FFT_ULLZ2[27]	V	
2589	float	_FFT_ULLZ2[28]	V	
2591	float	_FFT_ULLZ2[29]	V	
2593	float	_FFT_ULLZ2[30]	V	
2595	float	_FFT_ULLZ2[31]	V	
2597	float	_FFT_ULLZ2[32]	V	
2599	float	_FFT_ULLZ2[33]	V	
2601	float	_FFT_ULLZ2[34]	V	
2603	float	_FFT_ULLZ2[35]	V	
2605	float	_FFT_ULLZ2[36]	V	

Address	Format	Designation	Unit	Note
2607	float	_FFT_ULLZ2[37]	V	
2609	float	_FFT_ULLZ2[38]	V	
2611	float	_FFT_ULLZ2[39]	V	
2613	float	_FFT_ULLZ2[40]	V	
2615	float	_FFT_ULLZ2[41]	V	
2617	float	_FFT_ULLZ2[42]	V	
2619	float	_FFT_ULLZ2[43]	V	
2621	float	_FFT_ULLZ2[44]	V	
2623	float	_FFT_ULLZ2[45]	V	
2625	float	_FFT_ULLZ2[46]	V	
2627	float	_FFT_ULLZ2[47]	V	
2629	float	_FFT_ULLZ2[48]	V	
2631	float	_FFT_ULLZ2[49]	V	
2633	float	_FFT_ULLZ2[50]	V	
2635	float	_FFT_ULLZ2[51]	V	
2637	float	_FFT_ULLZ2[52]	V	
2639	float	_FFT_ULLZ2[53]	V	
2641	float	_FFT_ULLZ2[54]	V	
2643	float	_FFT_ULLZ2[55]	V	
2645	float	_FFT_ULLZ2[56]	V	
2647	float	_FFT_ULLZ2[57]	V	
2649	float	_FFT_ULLZ2[58]	V	
2651	float	_FFT_ULLZ2[59]	V	
2653	float	_FFT_ULLZ2[60]	V	
2655	float	_FFT_ULLZ2[61]	V	
2657	float	_FFT_ULLZ2[62]	V	
2659	float	_FFT_ULLZ3[0]	V	
2661	float	_FFT_ULLZ3[1]	V	
2663	float	_FFT_ULLZ3[2]	V	
2665	float	_FFT_ULLZ3[3]	V	
2667	float	_FFT_ULLZ3[4]	V	
2669	float	_FFT_ULLZ3[5]	V	
2671	float	_FFT_ULLZ3[6]	V	
2673	float	_FFT_ULLZ3[7]	V	
2675	float	_FFT_ULLZ3[8]	V	
2677	float	_FFT_ULLZ3[9]	V	
2679	float	_FFT_ULLZ3[10]	V	
2681	float	_FFT_ULLZ3[11]	V	
2683	float	_FFT_ULLZ3[12]	V	
2685	float	_FFT_ULLZ3[13]	V	
2687	float	_FFT_ULLZ3[14]	V	
2689	float	_FFT_ULLZ3[15]	V	
2691	float	_FFT_ULLZ3[16]	V	
2693	float	_FFT_ULLZ3[17]	V	
2695	float	_FFT_ULLZ3[18]	V	
2697	float	_FFT_ULLZ3[19]	V	
2699	float	_FFT_ULLZ3[20]	V	
2701	float	_FFT_ULLZ3[21]	V	
2703	float	_FFT_ULLZ3[22]	V	
2705	float	_FFT_ULLZ3[23]	V	
2707	float	_FFT_ULLZ3[24]	V	
2709	float	_FFT_ULLZ3[25]	V	
2711	float	_FFT_ULLZ3[26]	V	
2713	float	_FFT_ULLZ3[27]	V	
2715	float	_FFT_ULLZ3[28]	V	
2717	float	_FFT_ULLZ3[29]	V	
2719	float	_FFT_ULLZ3[30]	V	
2721	float	_FFT_ULLZ3[31]	V	
2723	float	_FFT_ULLZ3[32]	V	



Address	Format	Designation	Unit	Note
2725	float	_FFT_ULLZ3[33]	V	
2727	float	_FFT_ULLZ3[34]	V	
2729	float	_FFT_ULLZ3[35]	V	
2731	float	_FFT_ULLZ3[36]	V	
2733	float	_FFT_ULLZ3[37]	V	
2735	float	_FFT_ULLZ3[38]	V	
2737	float	_FFT_ULLZ3[39]	V	
2739	float	_FFT_ULLZ3[40]	V	
2741	float	_FFT_ULLZ3[41]	V	
2743	float	_FFT_ULLZ3[42]	V	
2745	float	_FFT_ULLZ3[43]	V	
2747	float	_FFT_ULLZ3[44]	V	
2749	float	_FFT_ULLZ3[45]	V	
2751	float	_FFT_ULLZ3[46]	V	
2753	float	_FFT_ULLZ3[47]	V	
2755	float	_FFT_ULLZ3[48]	V	
2757	float	_FFT_ULLZ3[49]	V	
2759	float	_FFT_ULLZ3[50]	V	
2761	float	_FFT_ULLZ3[51]	V	
2763	float	_FFT_ULLZ3[52]	V	
2765	float	_FFT_ULLZ3[53]	V	
2767	float	_FFT_ULLZ3[54]	V	
2769	float	_FFT_ULLZ3[55]	V	
2771	float	_FFT_ULLZ3[56]	V	
2773	float	_FFT_ULLZ3[57]	V	
2775	float	_FFT_ULLZ3[58]	V	
2777	float	_FFT_ULLZ3[59]	V	
2779	float	_FFT_ULLZ3[60]	V	
2781	float	_FFT_ULLZ3[61]	V	
2783	float	_FFT_ULLZ3[62]	V	
2785	float	_FFT_ULZ1[0]	V	
2787	float	_FFT_ULZ1[1]	V	
2789	float	_FFT_ULZ1[2]	V	
2791	float	_FFT_ULZ1[3]	V	
2793	float	_FFT_ULZ1[4]	V	
2795	float	_FFT_ULZ1[5]	V	
2797	float	_FFT_ULZ1[6]	V	
2799	float	_FFT_ULZ1[7]	V	
2801	float	_FFT_ULZ1[8]	V	
2803	float	_FFT_ULZ1[9]	V	
2805	float	_FFT_ULZ1[10]	V	
2807	float	_FFT_ULZ1[11]	V	
2809	float	_FFT_ULZ1[12]	V	
2811	float	_FFT_ULZ1[13]	V	
2813	float	_FFT_ULZ1[14]	V	
2815	float	_FFT_ULZ1[15]	V	
2817	float	_FFT_ULZ1[16]	V	
2819	float	_FFT_ULZ1[17]	V	
2821	float	_FFT_ULZ1[18]	V	
2823	float	_FFT_ULZ1[19]	V	
2825	float	_FFT_ULZ1[20]	V	
2827	float	_FFT_ULZ1[21]	V	
2829	float	_FFT_ULZ1[22]	V	
2831	float	_FFT_ULZ1[23]	V	
2833	float	_FFT_ULZ1[24]	V	
2835	float	_FFT_ULZ1[25]	V	
2837	float	_FFT_ULZ1[26]	V	
2839	float	_FFT_ULZ1[27]	V	
2841	float	_FFT_ULZ1[28]	V	

Address	Format	Designation	Unit	Note
2843	float	_FFT_ULZ1[29]	V	
2845	float	_FFT_ULZ1[30]	V	
2847	float	_FFT_ULZ1[31]	V	
2849	float	_FFT_ULZ1[32]	V	
2851	float	_FFT_ULZ1[33]	V	
2853	float	_FFT_ULZ1[34]	V	
2855	float	_FFT_ULZ1[35]	V	
2857	float	_FFT_ULZ1[36]	V	
2859	float	_FFT_ULZ1[37]	V	
2861	float	_FFT_ULZ1[38]	V	
2863	float	_FFT_ULZ1[39]	V	
2865	float	_FFT_ULZ1[40]	V	
2867	float	_FFT_ULZ1[41]	V	
2869	float	_FFT_ULZ1[42]	V	
2871	float	_FFT_ULZ1[43]	V	
2873	float	_FFT_ULZ1[44]	V	
2875	float	_FFT_ULZ1[45]	V	
2877	float	_FFT_ULZ1[46]	V	
2879	float	_FFT_ULZ1[47]	V	
2881	float	_FFT_ULZ1[48]	V	
2883	float	_FFT_ULZ1[49]	V	
2885	float	_FFT_ULZ1[50]	V	
2887	float	_FFT_ULZ1[51]	V	
2889	float	_FFT_ULZ1[52]	V	
2891	float	_FFT_ULZ1[53]	V	
2893	float	_FFT_ULZ1[54]	V	
2895	float	_FFT_ULZ1[55]	V	
2897	float	_FFT_ULZ1[56]	V	
2899	float	_FFT_ULZ1[57]	V	
2901	float	_FFT_ULZ1[58]	V	
2903	float	_FFT_ULZ1[59]	V	
2905	float	_FFT_ULZ1[60]	V	
2907	float	_FFT_ULZ1[61]	V	
2909	float	_FFT_ULZ1[62]	V	
2911	float	_FFT_ULZ2[0]	V	
2913	float	_FFT_ULZ2[1]	V	
2915	float	_FFT_ULZ2[2]	V	
2917	float	_FFT_ULZ2[3]	V	
2919	float	_FFT_ULZ2[4]	V	
2921	float	_FFT_ULZ2[5]	V	
2923	float	_FFT_ULZ2[6]	V	
2925	float	_FFT_ULZ2[7]	V	
2927	float	_FFT_ULZ2[8]	V	
2929	float	_FFT_ULZ2[9]	V	
2931	float	_FFT_ULZ2[10]	V	
2933	float	_FFT_ULZ2[11]	V	
2935	float	_FFT_ULZ2[12]	V	
2937	float	_FFT_ULZ2[13]	V	
2939	float	_FFT_ULZ2[14]	V	
2941	float	_FFT_ULZ2[15]	V	
2943	float	_FFT_ULZ2[16]	V	
2945	float	_FFT_ULZ2[17]	V	
2947	float	_FFT_ULZ2[18]	V	
2949	float	_FFT_ULZ2[19]	V	
2951	float	_FFT_ULZ2[20]	V	
2953	float	_FFT_ULZ2[21]	V	
2955	float	_FFT_ULZ2[22]	V	
2957	float	_FFT_ULZ2[23]	V	
2959	float	_FFT_ULZ2[24]	V	

Address	Format	Designation	Unit	Note
2961	float	_FFT_ULZ2[25]	V	
2963	float	_FFT_ULZ2[26]	V	
2965	float	_FFT_ULZ2[27]	V	
2967	float	_FFT_ULZ2[28]	V	
2969	float	_FFT_ULZ2[29]	V	
2971	float	_FFT_ULZ2[30]	V	
2973	float	_FFT_ULZ2[31]	V	
2975	float	_FFT_ULZ2[32]	V	
2977	float	_FFT_ULZ2[33]	V	
2979	float	_FFT_ULZ2[34]	V	
2981	float	_FFT_ULZ2[35]	V	
2983	float	_FFT_ULZ2[36]	V	
2985	float	_FFT_ULZ2[37]	V	
2987	float	_FFT_ULZ2[38]	V	
2989	float	_FFT_ULZ2[39]	V	
2991	float	_FFT_ULZ2[40]	V	
2993	float	_FFT_ULZ2[41]	V	
2995	float	_FFT_ULZ2[42]	V	
2997	float	_FFT_ULZ2[43]	V	
2999	float	_FFT_ULZ2[44]	V	
3001	float	_FFT_ULZ2[45]	V	
3003	float	_FFT_ULZ2[46]	V	
3005	float	_FFT_ULZ2[47]	V	
3007	float	_FFT_ULZ2[48]	V	
3009	float	_FFT_ULZ2[49]	V	
3011	float	_FFT_ULZ2[50]	V	
3013	float	_FFT_ULZ2[51]	V	
3015	float	_FFT_ULZ2[52]	V	
3017	float	_FFT_ULZ2[53]	V	
3019	float	_FFT_ULZ2[54]	V	
3021	float	_FFT_ULZ2[55]	V	
3023	float	_FFT_ULZ2[56]	V	
3025	float	_FFT_ULZ2[57]	V	
3027	float	_FFT_ULZ2[58]	V	
3029	float	_FFT_ULZ2[59]	V	
3031	float	_FFT_ULZ2[60]	V	
3033	float	_FFT_ULZ2[61]	V	
3035	float	_FFT_ULZ2[62]	V	
3037	float	_FFT_ULZ3[0]	V	
3039	float	_FFT_ULZ3[1]	V	
3041	float	_FFT_ULZ3[2]	V	
3043	float	_FFT_ULZ3[3]	V	
3045	float	_FFT_ULZ3[4]	V	
3047	float	_FFT_ULZ3[5]	V	
3049	float	_FFT_ULZ3[6]	V	
3051	float	_FFT_ULZ3[7]	V	
3053	float	_FFT_ULZ3[8]	V	
3055	float	_FFT_ULZ3[9]	V	
3057	float	_FFT_ULZ3[10]	V	
3059	float	_FFT_ULZ3[11]	V	
3061	float	_FFT_ULZ3[12]	V	
3063	float	_FFT_ULZ3[13]	V	
3065	float	_FFT_ULZ3[14]	V	
3067	float	_FFT_ULZ3[15]	V	
3069	float	_FFT_ULZ3[16]	V	
3071	float	_FFT_ULZ3[17]	V	
3073	float	_FFT_ULZ3[18]	V	
3075	float	_FFT_ULZ3[19]	V	
3077	float	_FFT_ULZ3[20]	V	

Address	Format	Designation	Unit	Note
3079	float	_FFT_ULZ3[21]	V	
3081	float	_FFT_ULZ3[22]	V	
3083	float	_FFT_ULZ3[23]	V	
3085	float	_FFT_ULZ3[24]	V	
3087	float	_FFT_ULZ3[25]	V	
3089	float	_FFT_ULZ3[26]	V	
3091	float	_FFT_ULZ3[27]	V	
3093	float	_FFT_ULZ3[28]	V	
3095	float	_FFT_ULZ3[29]	V	
3097	float	_FFT_ULZ3[30]	V	
3099	float	_FFT_ULZ3[31]	V	
3101	float	_FFT_ULZ3[32]	V	
3103	float	_FFT_ULZ3[33]	V	
3105	float	_FFT_ULZ3[34]	V	
3107	float	_FFT_ULZ3[35]	V	
3109	float	_FFT_ULZ3[36]	V	
3111	float	_FFT_ULZ3[37]	V	
3113	float	_FFT_ULZ3[38]	V	
3115	float	_FFT_ULZ3[39]	V	
3117	float	_FFT_ULZ3[40]	V	
3119	float	_FFT_ULZ3[41]	V	
3121	float	_FFT_ULZ3[42]	V	
3123	float	_FFT_ULZ3[43]	V	
3125	float	_FFT_ULZ3[44]	V	
3127	float	_FFT_ULZ3[45]	V	
3129	float	_FFT_ULZ3[46]	V	
3131	float	_FFT_ULZ3[47]	V	
3133	float	_FFT_ULZ3[48]	V	
3135	float	_FFT_ULZ3[49]	V	
3137	float	_FFT_ULZ3[50]	V	
3139	float	_FFT_ULZ3[51]	V	
3141	float	_FFT_ULZ3[52]	V	
3143	float	_FFT_ULZ3[53]	V	
3145	float	_FFT_ULZ3[54]	V	
3147	float	_FFT_ULZ3[55]	V	
3149	float	_FFT_ULZ3[56]	V	
3151	float	_FFT_ULZ3[57]	V	
3153	float	_FFT_ULZ3[58]	V	
3155	float	_FFT_ULZ3[59]	V	
3157	float	_FFT_ULZ3[60]	V	
3159	float	_FFT_ULZ3[61]	V	
3161	float	_FFT_ULZ3[62]	V	
3163	float	_FFT_ULZ4[0]	V	
3165	float	_FFT_ULZ4[1]	V	
3167	float	_FFT_ULZ4[2]	V	
3169	float	_FFT_ULZ4[3]	V	
3171	float	_FFT_ULZ4[4]	V	
3173	float	_FFT_ULZ4[5]	V	
3175	float	_FFT_ULZ4[6]	V	
3177	float	_FFT_ULZ4[7]	V	
3179	float	_FFT_ULZ4[8]	V	
3181	float	_FFT_ULZ4[9]	V	
3183	float	_FFT_ULZ4[10]	V	
3185	float	_FFT_ULZ4[11]	V	
3187	float	_FFT_ULZ4[12]	V	
3189	float	_FFT_ULZ4[13]	V	
3191	float	_FFT_ULZ4[14]	V	
3193	float	_FFT_ULZ4[15]	V	
3195	float	_FFT_ULZ4[16]	V	

Address	Format	Designation	Unit	Note
3197	float	_FFT_ULZ4[17]	V	
3199	float	_FFT_ULZ4[18]	V	
3201	float	_FFT_ULZ4[19]	V	
3203	float	_FFT_ULZ4[20]	V	
3205	float	_FFT_ULZ4[21]	V	
3207	float	_FFT_ULZ4[22]	V	
3209	float	_FFT_ULZ4[23]	V	
3211	float	_FFT_ULZ4[24]	V	
3213	float	_FFT_ULZ4[25]	V	
3215	float	_FFT_ULZ4[26]	V	
3217	float	_FFT_ULZ4[27]	V	
3219	float	_FFT_ULZ4[28]	V	
3221	float	_FFT_ULZ4[29]	V	
3223	float	_FFT_ULZ4[30]	V	
3225	float	_FFT_ULZ4[31]	V	
3227	float	_FFT_ULZ4[32]	V	
3229	float	_FFT_ULZ4[33]	V	
3231	float	_FFT_ULZ4[34]	V	
3233	float	_FFT_ULZ4[35]	V	
3235	float	_FFT_ULZ4[36]	V	
3237	float	_FFT_ULZ4[37]	V	
3239	float	_FFT_ULZ4[38]	V	
3241	float	_FFT_ULZ4[39]	V	
3243	float	_FFT_ULZ4[40]	V	
3245	float	_FFT_ULZ4[41]	V	
3247	float	_FFT_ULZ4[42]	V	
3249	float	_FFT_ULZ4[43]	V	
3251	float	_FFT_ULZ4[44]	V	
3253	float	_FFT_ULZ4[45]	V	
3255	float	_FFT_ULZ4[46]	V	
3257	float	_FFT_ULZ4[47]	V	
3259	float	_FFT_ULZ4[48]	V	
3261	float	_FFT_ULZ4[49]	V	
3263	float	_FFT_ULZ4[50]	V	
3265	float	_FFT_ULZ4[51]	V	
3267	float	_FFT_ULZ4[52]	V	
3269	float	_FFT_ULZ4[53]	V	
3271	float	_FFT_ULZ4[54]	V	
3273	float	_FFT_ULZ4[55]	V	
3275	float	_FFT_ULZ4[56]	V	
3277	float	_FFT_ULZ4[57]	V	
3279	float	_FFT_ULZ4[58]	V	
3281	float	_FFT_ULZ4[59]	V	
3283	float	_FFT_ULZ4[60]	V	
3285	float	_FFT_ULZ4[61]	V	
3287	float	_FFT_ULZ4[62]	V	
3289	float	_FFT_ILZ1[0]	A	
3291	float	_FFT_ILZ1[1]	A	
3293	float	_FFT_ILZ1[2]	A	
3295	float	_FFT_ILZ1[3]	A	
3297	float	_FFT_ILZ1[4]	A	
3299	float	_FFT_ILZ1[5]	A	
3301	float	_FFT_ILZ1[6]	A	
3303	float	_FFT_ILZ1[7]	A	
3305	float	_FFT_ILZ1[8]	A	
3307	float	_FFT_ILZ1[9]	A	
3309	float	_FFT_ILZ1[10]	A	
3311	float	_FFT_ILZ1[11]	A	
3313	float	_FFT_ILZ1[12]	A	

Address	Format	Designation	Unit	Note
3315	float	_FFT_ILZ1[13]	A	
3317	float	_FFT_ILZ1[14]	A	
3319	float	_FFT_ILZ1[15]	A	
3321	float	_FFT_ILZ1[16]	A	
3323	float	_FFT_ILZ1[17]	A	
3325	float	_FFT_ILZ1[18]	A	
3327	float	_FFT_ILZ1[19]	A	
3329	float	_FFT_ILZ1[20]	A	
3331	float	_FFT_ILZ1[21]	A	
3333	float	_FFT_ILZ1[22]	A	
3335	float	_FFT_ILZ1[23]	A	
3337	float	_FFT_ILZ1[24]	A	
3339	float	_FFT_ILZ1[25]	A	
3341	float	_FFT_ILZ1[26]	A	
3343	float	_FFT_ILZ1[27]	A	
3345	float	_FFT_ILZ1[28]	A	
3347	float	_FFT_ILZ1[29]	A	
3349	float	_FFT_ILZ1[30]	A	
3351	float	_FFT_ILZ1[31]	A	
3353	float	_FFT_ILZ1[32]	A	
3355	float	_FFT_ILZ1[33]	A	
3357	float	_FFT_ILZ1[34]	A	
3359	float	_FFT_ILZ1[35]	A	
3361	float	_FFT_ILZ1[36]	A	
3363	float	_FFT_ILZ1[37]	A	
3365	float	_FFT_ILZ1[38]	A	
3367	float	_FFT_ILZ1[39]	A	
3369	float	_FFT_ILZ1[40]	A	
3371	float	_FFT_ILZ1[41]	A	
3373	float	_FFT_ILZ1[42]	A	
3375	float	_FFT_ILZ1[43]	A	
3377	float	_FFT_ILZ1[44]	A	
3379	float	_FFT_ILZ1[45]	A	
3381	float	_FFT_ILZ1[46]	A	
3383	float	_FFT_ILZ1[47]	A	
3385	float	_FFT_ILZ1[48]	A	
3387	float	_FFT_ILZ1[49]	A	
3389	float	_FFT_ILZ1[50]	A	
3391	float	_FFT_ILZ1[51]	A	
3393	float	_FFT_ILZ1[52]	A	
3395	float	_FFT_ILZ1[53]	A	
3397	float	_FFT_ILZ1[54]	A	
3399	float	_FFT_ILZ1[55]	A	
3401	float	_FFT_ILZ1[56]	A	
3403	float	_FFT_ILZ1[57]	A	
3405	float	_FFT_ILZ1[58]	A	
3407	float	_FFT_ILZ1[59]	A	
3409	float	_FFT_ILZ1[60]	A	
3411	float	_FFT_ILZ1[61]	A	
3413	float	_FFT_ILZ1[62]	A	
3415	float	_FFT_ILZ2[0]	A	
3417	float	_FFT_ILZ2[1]	A	
3419	float	_FFT_ILZ2[2]	A	
3421	float	_FFT_ILZ2[3]	A	
3423	float	_FFT_ILZ2[4]	A	
3425	float	_FFT_ILZ2[5]	A	
3427	float	_FFT_ILZ2[6]	A	
3429	float	_FFT_ILZ2[7]	A	
3431	float	_FFT_ILZ2[8]	A	

Address	Format	Designation	Unit	Note
3433	float	_FFT_ILZ2[9]	A	
3435	float	_FFT_ILZ2[10]	A	
3437	float	_FFT_ILZ2[11]	A	
3439	float	_FFT_ILZ2[12]	A	
3441	float	_FFT_ILZ2[13]	A	
3443	float	_FFT_ILZ2[14]	A	
3445	float	_FFT_ILZ2[15]	A	
3447	float	_FFT_ILZ2[16]	A	
3449	float	_FFT_ILZ2[17]	A	
3451	float	_FFT_ILZ2[18]	A	
3453	float	_FFT_ILZ2[19]	A	
3455	float	_FFT_ILZ2[20]	A	
3457	float	_FFT_ILZ2[21]	A	
3459	float	_FFT_ILZ2[22]	A	
3461	float	_FFT_ILZ2[23]	A	
3463	float	_FFT_ILZ2[24]	A	
3465	float	_FFT_ILZ2[25]	A	
3467	float	_FFT_ILZ2[26]	A	
3469	float	_FFT_ILZ2[27]	A	
3471	float	_FFT_ILZ2[28]	A	
3473	float	_FFT_ILZ2[29]	A	
3475	float	_FFT_ILZ2[30]	A	
3477	float	_FFT_ILZ2[31]	A	
3479	float	_FFT_ILZ2[32]	A	
3481	float	_FFT_ILZ2[33]	A	
3483	float	_FFT_ILZ2[34]	A	
3485	float	_FFT_ILZ2[35]	A	
3487	float	_FFT_ILZ2[36]	A	
3489	float	_FFT_ILZ2[37]	A	
3491	float	_FFT_ILZ2[38]	A	
3493	float	_FFT_ILZ2[39]	A	
3495	float	_FFT_ILZ2[40]	A	
3497	float	_FFT_ILZ2[41]	A	
3499	float	_FFT_ILZ2[42]	A	
3501	float	_FFT_ILZ2[43]	A	
3503	float	_FFT_ILZ2[44]	A	
3505	float	_FFT_ILZ2[45]	A	
3507	float	_FFT_ILZ2[46]	A	
3509	float	_FFT_ILZ2[47]	A	
3511	float	_FFT_ILZ2[48]	A	
3513	float	_FFT_ILZ2[49]	A	
3515	float	_FFT_ILZ2[50]	A	
3517	float	_FFT_ILZ2[51]	A	
3519	float	_FFT_ILZ2[52]	A	
3521	float	_FFT_ILZ2[53]	A	
3523	float	_FFT_ILZ2[54]	A	
3525	float	_FFT_ILZ2[55]	A	
3527	float	_FFT_ILZ2[56]	A	
3529	float	_FFT_ILZ2[57]	A	
3531	float	_FFT_ILZ2[58]	A	
3533	float	_FFT_ILZ2[59]	A	
3535	float	_FFT_ILZ2[60]	A	
3537	float	_FFT_ILZ2[61]	A	
3539	float	_FFT_ILZ2[62]	A	
3541	float	_FFT_ILZ3[0]	A	
3543	float	_FFT_ILZ3[1]	A	
3545	float	_FFT_ILZ3[2]	A	
3547	float	_FFT_ILZ3[3]	A	
3549	float	_FFT_ILZ3[4]	A	

Address	Format	Designation	Unit	Note
3551	float	_FFT_ILZ3[5]	A	
3553	float	_FFT_ILZ3[6]	A	
3555	float	_FFT_ILZ3[7]	A	
3557	float	_FFT_ILZ3[8]	A	
3559	float	_FFT_ILZ3[9]	A	
3561	float	_FFT_ILZ3[10]	A	
3563	float	_FFT_ILZ3[11]	A	
3565	float	_FFT_ILZ3[12]	A	
3567	float	_FFT_ILZ3[13]	A	
3569	float	_FFT_ILZ3[14]	A	
3571	float	_FFT_ILZ3[15]	A	
3573	float	_FFT_ILZ3[16]	A	
3575	float	_FFT_ILZ3[17]	A	
3577	float	_FFT_ILZ3[18]	A	
3579	float	_FFT_ILZ3[19]	A	
3581	float	_FFT_ILZ3[20]	A	
3583	float	_FFT_ILZ3[21]	A	
3585	float	_FFT_ILZ3[22]	A	
3587	float	_FFT_ILZ3[23]	A	
3589	float	_FFT_ILZ3[24]	A	
3591	float	_FFT_ILZ3[25]	A	
3593	float	_FFT_ILZ3[26]	A	
3595	float	_FFT_ILZ3[27]	A	
3597	float	_FFT_ILZ3[28]	A	
3599	float	_FFT_ILZ3[29]	A	
3601	float	_FFT_ILZ3[30]	A	
3603	float	_FFT_ILZ3[31]	A	
3605	float	_FFT_ILZ3[32]	A	
3607	float	_FFT_ILZ3[33]	A	
3609	float	_FFT_ILZ3[34]	A	
3611	float	_FFT_ILZ3[35]	A	
3613	float	_FFT_ILZ3[36]	A	
3615	float	_FFT_ILZ3[37]	A	
3617	float	_FFT_ILZ3[38]	A	
3619	float	_FFT_ILZ3[39]	A	
3621	float	_FFT_ILZ3[40]	A	
3623	float	_FFT_ILZ3[41]	A	
3625	float	_FFT_ILZ3[42]	A	
3627	float	_FFT_ILZ3[43]	A	
3629	float	_FFT_ILZ3[44]	A	
3631	float	_FFT_ILZ3[45]	A	
3633	float	_FFT_ILZ3[46]	A	
3635	float	_FFT_ILZ3[47]	A	
3637	float	_FFT_ILZ3[48]	A	
3639	float	_FFT_ILZ3[49]	A	
3641	float	_FFT_ILZ3[50]	A	
3643	float	_FFT_ILZ3[51]	A	
3645	float	_FFT_ILZ3[52]	A	
3647	float	_FFT_ILZ3[53]	A	
3649	float	_FFT_ILZ3[54]	A	
3651	float	_FFT_ILZ3[55]	A	
3653	float	_FFT_ILZ3[56]	A	
3655	float	_FFT_ILZ3[57]	A	
3657	float	_FFT_ILZ3[58]	A	
3659	float	_FFT_ILZ3[59]	A	
3661	float	_FFT_ILZ3[60]	A	
3663	float	_FFT_ILZ3[61]	A	
3665	float	_FFT_ILZ3[62]	A	
3667	float	_FFT_ILZ4[0]	A	



Address	Format	Designation	Unit	Note
3669	float	_FFT_ILZ4[1]	A	
3671	float	_FFT_ILZ4[2]	A	
3673	float	_FFT_ILZ4[3]	A	
3675	float	_FFT_ILZ4[4]	A	
3677	float	_FFT_ILZ4[5]	A	
3679	float	_FFT_ILZ4[6]	A	
3681	float	_FFT_ILZ4[7]	A	
3683	float	_FFT_ILZ4[8]	A	
3685	float	_FFT_ILZ4[9]	A	
3687	float	_FFT_ILZ4[10]	A	
3689	float	_FFT_ILZ4[11]	A	
3691	float	_FFT_ILZ4[12]	A	
3693	float	_FFT_ILZ4[13]	A	
3695	float	_FFT_ILZ4[14]	A	
3697	float	_FFT_ILZ4[15]	A	
3699	float	_FFT_ILZ4[16]	A	
3701	float	_FFT_ILZ4[17]	A	
3703	float	_FFT_ILZ4[18]	A	
3705	float	_FFT_ILZ4[19]	A	
3707	float	_FFT_ILZ4[20]	A	
3709	float	_FFT_ILZ4[21]	A	
3711	float	_FFT_ILZ4[22]	A	
3713	float	_FFT_ILZ4[23]	A	
3715	float	_FFT_ILZ4[24]	A	
3717	float	_FFT_ILZ4[25]	A	
3719	float	_FFT_ILZ4[26]	A	
3721	float	_FFT_ILZ4[27]	A	
3723	float	_FFT_ILZ4[28]	A	
3725	float	_FFT_ILZ4[29]	A	
3727	float	_FFT_ILZ4[30]	A	
3729	float	_FFT_ILZ4[31]	A	
3731	float	_FFT_ILZ4[32]	A	
3733	float	_FFT_ILZ4[33]	A	
3735	float	_FFT_ILZ4[34]	A	
3737	float	_FFT_ILZ4[35]	A	
3739	float	_FFT_ILZ4[36]	A	
3741	float	_FFT_ILZ4[37]	A	
3743	float	_FFT_ILZ4[38]	A	
3745	float	_FFT_ILZ4[39]	A	
3747	float	_FFT_ILZ4[40]	A	
3749	float	_FFT_ILZ4[41]	A	
3751	float	_FFT_ILZ4[42]	A	
3753	float	_FFT_ILZ4[43]	A	
3755	float	_FFT_ILZ4[44]	A	
3757	float	_FFT_ILZ4[45]	A	
3759	float	_FFT_ILZ4[46]	A	
3761	float	_FFT_ILZ4[47]	A	
3763	float	_FFT_ILZ4[48]	A	
3765	float	_FFT_ILZ4[49]	A	
3767	float	_FFT_ILZ4[50]	A	
3769	float	_FFT_ILZ4[51]	A	
3771	float	_FFT_ILZ4[52]	A	
3773	float	_FFT_ILZ4[53]	A	
3775	float	_FFT_ILZ4[54]	A	
3777	float	_FFT_ILZ4[55]	A	
3779	float	_FFT_ILZ4[56]	A	
3781	float	_FFT_ILZ4[57]	A	
3783	float	_FFT_ILZ4[58]	A	
3785	float	_FFT_ILZ4[59]	A	

Address	Format	Designation	Unit	Note
3787	float	_FFT_ILZ4[60]	A	
3789	float	_FFT_ILZ4[61]	A	
3791	float	_FFT_ILZ4[62]	A	

Address	Format	Designation	Unit	Note
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