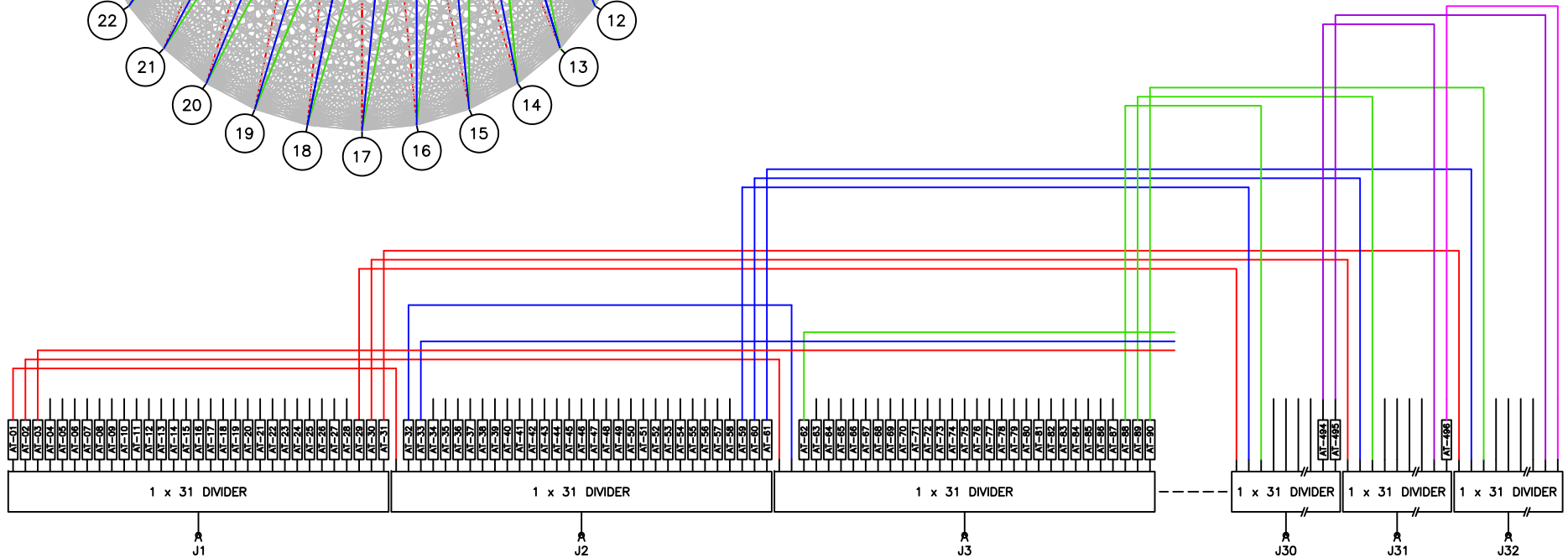


THIS IS A 32 PORT TRANSCEIVER TEST SYSTEM.
 IT IS FULL FAN-OUT.
 EVERY PORT CONNECTS TO EVERY OTHER PORT VIA 496 INTERNAL RF PATHS.
 IT HAS A PROGRAMMABLE ATTENUATOR ON EVERY RF PATH (496 TOTAL ATTENUATORS)



| REV | ECN | DATE | BY | APPR | MATERIAL | | PROPRIETARY INFORMATION | |
|-----|-----|----------------|--------|------|---------------------------------|--|---|--|
| - | | ISSUED 3-17-11 | BY TRS | | | | ALL SURFACES ARE TO BE FREE OF BURRS, PITS, & SCRATCHES | |
| A | | 26712 6-13-16 | DTS | TRB | | | TOLERANCES: .XX±.010 .XXX±.005 .XX±.5' | |
| | | | | | DESCRIPTION | | DWG # | |
| | | | | | 32 PORT TRANSCEIVER TEST SYSTEM | | 092-6888 | |



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