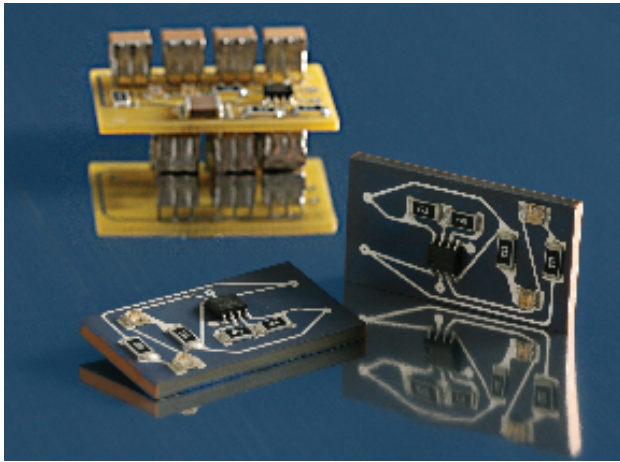


## CAPSTRATE® CAPACITOR SUBSTRATES



JDI's new line of CapStrate® products integrate large amounts of bulk capacitance into a ceramic substrate allowing elimination of large discrete capacitive components saving critical space and simplifying your assembly process.

JDI's extensive experience in design and manufacture of large format, custom geometries allows us to develop unique and innovative solutions which successfully solve a wide variety of our customer's design challenges.

We'll work proactively with you to fully understand your requirements and recommend the best solution compatible with your application needs.

### KEY FEATURES

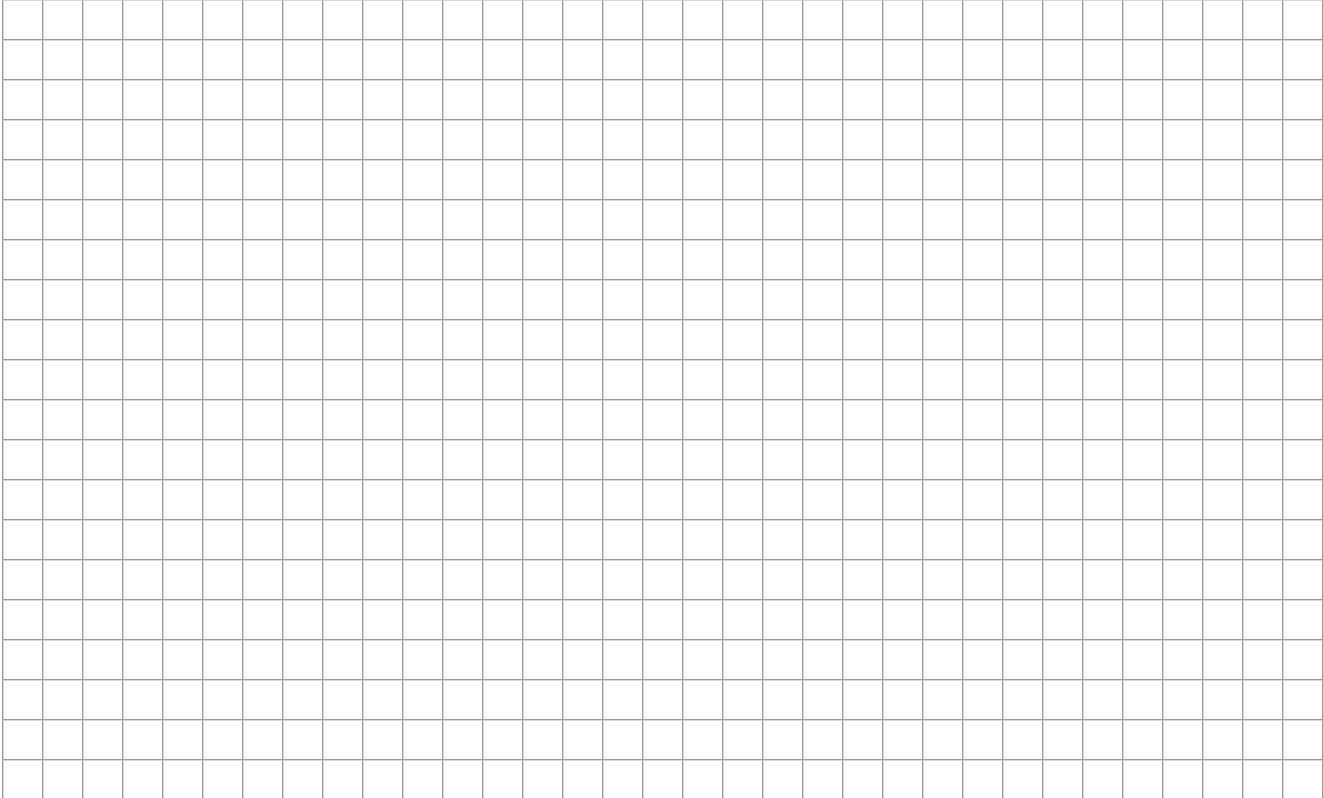
- Integrated Capacitance Substrates
- Rated Working Voltages from 50V to 500V, specials from 1kV to 7.5kV
- Temperature ranges: -55C to 125C (specials to 200C and 250C)
- Compact Designs Utilizing Military Grade Ceramics
- Custom Sizes, Values, and Voltages Available

### SIZE / CAPACITANCE CAPABILITY EXAMPLES

Substrate Size		Length	Width	Thick	NPO 50V	NPO 100V	NPO 200V	NPO 500V	X7R 50V	X7R 100V	X7R 200V	X7R 500V
CapStrate 4	in	0.400	0.400	0.120	0.22 µF	0.15µF	0.12µF	0.07µF	9.0µF	6.0µF	3.0µF	1.5µF
	mm	10.2	10.2	3.1								
CapStrate 3	in	0.450	1.00	0.120	0.70µF	0.50µF	0.39µF	0.22µF	28.0µF	20.0µF	9.0µF	4.7µF
	mm	11.43	25.4	3.1								
CapStrate 1	in	0.450	2.00	0.120	1.40µF	1.00µF	0.75µF	0.44µF	50.0µF	40.0µF	18.0µF	9.4µF
	mm	11.4	50.8	3.1								
CapStrate 2	in	0.800	1.50	0.120	2.00µF	1.40µF	1.00µF	0.60µF	70.0µF	55.0µF	25.0µF	14.0µF
	mm	20.3	38.1	3.1								
CapStrate 6	in	1.250	2.00	0.120	4.00µF	2.80µF	2.00µF	1.20µF	150.0µF	110.0µF	50.0µF	28.0µF
	mm	31.8	50.8	3.1								
Circular CapStrate® Capacitance Formula					1.3 - 1.6 µF / ln2	0.9- 1.1 µF / ln2	0.7 - 0.8 µF / ln2	50 - 62 µF / ln2	35 - 45 µF / ln2	18- 20 µF / ln2	1.3 - 1.6 µF / ln2	9- 10 µF / ln2

This chart is intended to provide capability examples. Not all possibilities are shown and we invite application specific inquiries. Circular CapStrate example lists available capacitance per area.

CONCEPT DRAWING OF DESIRED \_\_\_ OR MAXIMUM \_\_\_ COMPONENT DIMENSIONS  
(SCALE NOT REQUIRED)



Thickness of Substrate: \_\_\_\_\_ Max. Height of Components: \_\_\_\_\_  
Capacitors (or bulk capacitance value), other components

Capacitors

	Value	Tol. (+/-%)	Voltage (DC)
1.			
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			

Other Components

Description: Type, Value, Voltage, Tolerance, Etc.	Reference P/N