Aeroflex Colorado Springs
SμMMIT Handbook
Edits since 4-02 Handbook Production

Edits made 1-13
Page 156
Updated package drawing in Figure 59

Previous Edits

Page 13   Chapter 2
2.1.8, 3rd paragraph changed to: The Time-Tag counter begins operation on the rising edge or MRST# or within 64us after the receipt of a valid Reset Remote Terminal Mode Code, Synchronize with Data Mode Code, or Synchronize without Data Mode Code.

Page 21   Chapter 2
Bit Number 6 added after 2nd line word command - this includes illegal and broadcast commands.

Page 22   Chapter 2
Bit Number 6 added after 2nd line word command - this includes illegal and broadcast commands.

Page 23   Chapter 2
Bit Number 6 added after 2nd line word command - this includes illegal and broadcast commands.
Page 24   Chapter 2
Bit Number 6 added after 2nd line word command - this includes illegal and broadcast commands.

Page 102   Chapter 11
RRD, RWR, RCS types Note 1 was removed.

Page 107   Chapter 12
12.1.2 added Note 2 - This output may tri-state and should be connected to an external bias resistor.

Page 109   Chapter 12
DMA Signals DMACK type- TUI to TTO
Changed Note 1 to - This output may tri-state and should be connected to an external bias resistor.

Page 110   Chapter 12
RRD, RWR, RCS - Note 1 was added - This output may tri-state and should be connected to V_{DD} through an external bias resistor.

Page 122   Chapter 14
Absolute Maximum Ratings - line PD Limit from 25 to 2.5

Edits made 11-11

Page 35   Chapter 3
Edited Register #2 description to Current Command Block

Page 51   Chapter 4
Edited Register #2 description to Current Command Block

Page 81   Chapter 8
Added new note #6

Page 82   Chapter 8
Added note #6

Page 98   Chapter 11
Edited TI description and add OD = Open Drain

Page 99   Chapter 11
Removed note #2

Page 100   Chapter 11
TCK description added "Can operate up to 1 MHz

Page 101   Chapter 11
11.1.7 changed TYPE TTO to OD on DMAR and DMAC, edited note #1 to This output will drive
low and three-state only

Page 102   Chapter 11
RD, RWR, RCS types note 1 removed. Changed ROMEN type to OD. Edited note #2 to This out-
put will drive low and three-state only

Page 103   Chapter 11
11.1.9 changed MSG_INT, YF_INT from TTO to OD. Edited note #1 to This output will drive
low and three-state only

Page 105   Chapter 12
Edited Pin Description
Page 106   Chapter 12
Edited TI description and add OD = Open Drain

Page 107   Chapter 12
Removed note #2

Page 108   Chapter 12
TCK description added "Can operate up to 1 MHz

Page 108   Chapter 11
12.1.6 changed TYPE TTO to OD on DMAR and DMAC, edited note #1 to This output will drive
low and three-state only

Page 110   Chapter 12
RRD, RWR, RCS types note 1 removed. Changed ROMEN type to OD and SSYSF to TUI.
Edited note #1 to This output will drive low and three-state only

Page 111   Chapter 12
Changed MSG_INT and YF_INT type to OD. Edited note #1 to This output will drive low and
three-state only

Page 112   Chapter 12
Edited V_{CC} Description LX to LXE, DX to DXE and changed recommended decoupling capaci-
tor values from 4.7 to 47

Page 113   Chapter 13
Edited Pin Description

Page 114   Chapter 13
Edited TI description and add OD = Open Drain

Page 117   Chapter 13
TCK description added "Can operate up to 1 MHz

Page 120   Chapter 13
13.1.9 changed TYPE TTO to OD on YF_INT and MSG_INT. Removed note 1 for RDY and
BIST. Edited note #1 to This output will drive low and three-state only

Page 123   Chapter 16
SuMMIT E DC Table - edited V_{OH} condition to -4.0 and -1.0, added on S_{IDD} MRST = V_{DD} and
MRST-V_{SS}. Edited note 7 to guaranteed by characterization, not tested.

Page 125   Chapter 19
Corrected V_{OL} Condition and V_{OH} condition to -4.0 and -1.0. Edited note #3
Page 126   Chapter 19
Edited V_{OH} condition to -4.0 and -1.0. Edited note #3 to read "Guaranteed by design but not
tested".

Page 127   Chapter 20
Edited note #3 to state Guaranteed by characterization but not tested

Page 129   Chapter 20
Added note #1 to t_g. Edited note #3 to state Guaranteed by characterization but not tested

Page 130   Chapter 20
Edited note #2 to state Guaranteed by characterization but not tested

Page 131   Chapter 20
Edited note #2 to state Guaranteed by characterization but not tested

Page 132   Chapter 20
Added note #1 to t_a, t_b and t_f. Edited note #1 to state Guaranteed by characterization but not tested

Page 133   Chapter 20
Edited note #4 to state Guaranteed by characterization but not tested. Added note 4 to t_a, t_b, t_c, and t_d.

Page 134   Chapter 20
Edited note #1 to state Guaranteed by characterization but not tested.

Page 144   Chapter 20
Edited t_f, t_g, t_h to 250 min versus max

Page 145   Chapter 21
Inserted figure on JTAB timing.

Page 135-142   Chapter 21
Changed Minimum for t_{WP} from 1700 [3,4] to 2060 [3,4].

Changed Maximum for t_{RDYL} from 1700 [3] to 2040 [3].

Page 154
Updated package drawing in Figure 57

Page 156
Updated package drawing in Figure 59

Page 158
Updated package drawing in Figure 60

Page 159, 160, 161
Updated order information

EDITS made 5-14

Page 84
Deleted UT54ACS220 and referred to web page

Page 85
Added UT54ACTS220 block diagram

Page 112
correct 4.7μF to ≥4.7μF

Page 120
correct 4.7μF to ≥4.7μF

Page 160
Added 139-Pingrid array (Case Z)

Page 163
Edited SuMMIT XTE Order Entry adding Z package

EDITS made 10-15

Removed 139-pingrid Case X package