Frequency Shift Keyed Silicon Oscillators "FH32" and "FH53" Series



The FH series is an extremely low power precision frequency shift keyed silicon oscillator with a total frequency error less than 1.0%. This Si-gate CMOS oscillator produces a square wave output and requires no external components other than power supply bypass capacitors. The operating voltage range is 1.5V to 3.3V which allows operation from a single Li-Ion cell or 2 AA alkaline cells.



This frequency shift keyed oscillator has two selectable output frequencies. The output frequency is selected by an external logic level signal applied to its frequency control pin (pin 1). A low level input increases the output frequency by approximately 5% compared to a high level or no input to pin 1. This allows binary data signals to be transmitted as two distinct frequencies. As an example, the output frequency of a 32.768 KHz silicon oscillator will shift to 34.406 KHz when its pin 1 is taken to logic low. The output frequency returns back to 32.768 KHz if the logic low is removed or logic high is applied.

If a frequency shift is not required, please select the "<u>SH32</u>" or "<u>SH53</u>" series. Their pin 1 is no connection.

Features:

- Hermetically sealed & metal-lid grounded ceramic leadless package
- Superior moisture resistant, compared to plastic molded packaging
- Internally frequency tuned to the specified voltage (no external components needed)
- Ultra-low supply current [10 µA typical at +2.5V]
- Fast start up time
- Fast frequency shift rate
- Low supply current
- +1.55V to +3.3V single supply
- Withstands high vibration and harsh environments
- Suitable for light weight, compact consumer electronic devices
- Ideal for high density boards
- RoHS compliant and (Pb) lead-free product
- No mechanical parts. No PLL
- US Patent: 6,281,732. A collaboration with Micro Oscillator Inc. <u>www.micro-oscillator.com</u>

Applications:

- ▲ Microprocessor Clocks
- ▲ Remote Controls
- ▲ Data transmission

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Absolute Maximum Ratings

Power Supply Voltage V _{DD}	+3.6 V max.
Input Voltage	-0V min.; V _{DD} V max.
Output Voltage	-0V min.; V _{DD} V max.
Operation Junction Temperature	-55°C min.; +125°C max.

General Specifications: Ta=+25°C

Product Series	"FH32" series. Package size: 3.2x2.5x1.0 mm CLCC 4 pads					
Floduct Series	"FH53" series. Package size: 5x3.2x1.2 mm CLCC 4 pads					
Frequency Range	20 KHz to 80 KHz					
Popular Frequency	32.768 KHz					
Frequency Shift		1	5% typical	1		
Input Voltage (V _{DD}) D.C.	+1.55 V±5%	+1.8 V±5%	+2.5 V±5%	+3.0 V±5%	+3.3 V±5%	
Voltage Code for Part No.	15	18	25	3	33	
FH32 Availability		FH32		Not available	Not available	
FH53 Availability			FH53			
Current Consumption	3 µA typical	5 µA typical	10 µA typical	16 µA typical	18 µA typical	
Initial Frequency Accuracy		±	0.5% max. at +25°	°C		
Frequency Error vs Supply Voltage	±0.2% typical	±0.2% typical	±0.3% typical	±0.2% typical	±0.2% typical	
Frequency Error vs Temperature (0°C to +70°C)	±0.1% typical	±0.1% typical	±0.3% typical	±0.2% typical	±0.2% typical	
Frequency Standard Deviation	0.5 Hz. typ.	0.5 Hz. typ.	1 Hz. typ.	2 Hz. typ.	2 Hz. typ.	
Output Logic and Waveform		C	MOS. Square wav	'e.		
Output "High" Voltage; V _{он}			0.9^*V_{DD} min.			
Output "Low" Voltage; VoL			0.1*V _{DD} max.			
Duty Cycle		50)% ±5% at 50% V	DD		
Rise Time (Tr)/ Fall Time (Tf) Condition: 12 pF Load	38n sec. typ.	33n sec. typ.	25n sec. typ.	24n sec. typ.	24n sec. typ.	
Start-up Time (Ts) (note 1)			30 µ sec. max.			
Oscillator Turn On Time (note 2)			0.3 sec. min			
Supply Voltage Rise Time			1 µ sec. min.			
Notos:						

Notes:

1/ Output signal frequency is stable by the second pulse cycle, after the supply voltage is stable. This time is dependent on the oscillator frequency. 32.768 KHz is given.

2/ Oscillator start up requires a relatively clean supply voltage that does not drop down towards zero volts during turn on. After the supply voltage drops below about 1v, the voltage should go to 0v for 0.3 sec. min. before the oscillator is turned on again.

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Environmental Performance Specifications

	RoHS compliant, Pb (lead) free in accordance with EU Directive 2002/95/EC
Green Requirement	6/6 (2002/95/EC) and WEEE (2002/96/EC).
	Free of halide, cadmium, hexavalent chromium, lead, mercury, PBB's and PBDE's.
Moisture Sensitivity Level	Level 1 (infinite) according to IPC/JEDEC J-STD-020D.1
Second Level Interconnect	e4
Operating Temp. Range	0 to +70°C
Storage Temp. Range	-55 to +125°C
Humidity	85% RH, 85°C, 48 hours
Fine Leak / Gross Leak	MIL-Std-883, method 1014, condition A / MIL-Std-883, method 1014, condition C
Solderability	MIL-STD-202F method 208E
Reflow	260°C max. for 10 sec.max. 2 reflows.
Vibration	MIL-STD-202F method 204, 35G, 50 to 2000 Hz
Shock	MIL-STD-202F method 213B, test condi. E, 1000GG ¹ / ₂ sine wave
Resistance to Solvent	MIL-STD-202, method 215
Temperature Cycling	MIL-STD-883, method 1010
Pad Surface Finish	Gold (0.3 to 1.0 μm) over nickel (1.27 to 8.89 μm)
Weight of the Device	FH32: 0.042grams typical; FH53: 0.078grams typical

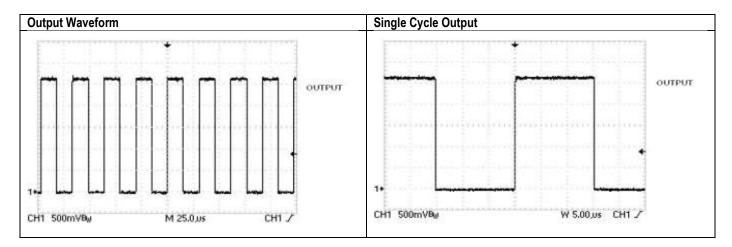
Part Number Format and Example:

Example: 18FH32-327

⊯: User input

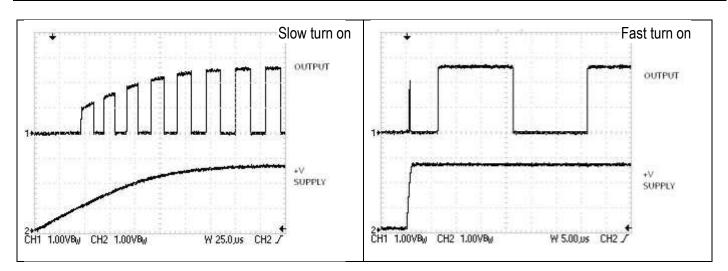
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18	FH	32	_	327
Supply voltage code "33" for 3.3VV _{DD} "3" for 3.0 V V _{DD} "25" for 2.5V V _{DD} "18" for 1.8V V _{DD} "15" for 1.55V V _{DD}	Product series	Package size. "32" for 3.2x2.5x1.0 mm "53" for 5x3.2x1.2 mm		Frequency in KHz. 327 = 32.768 KHz

Typical Output Waveform



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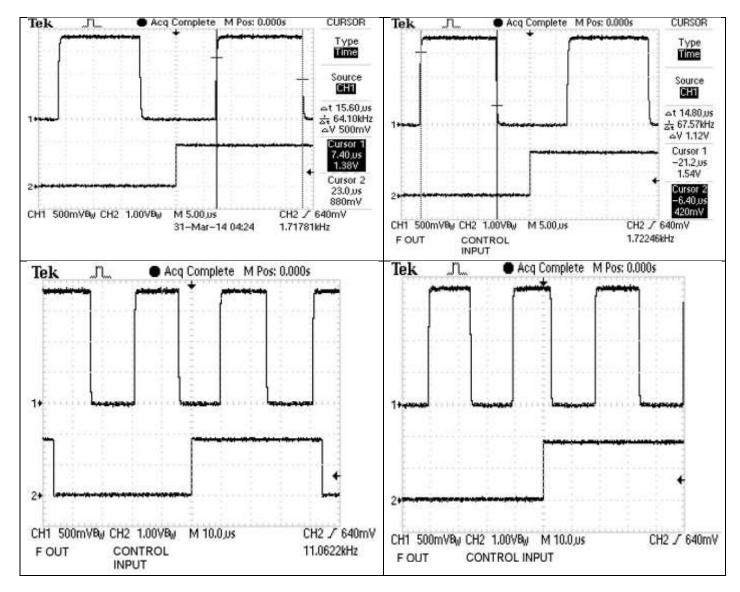


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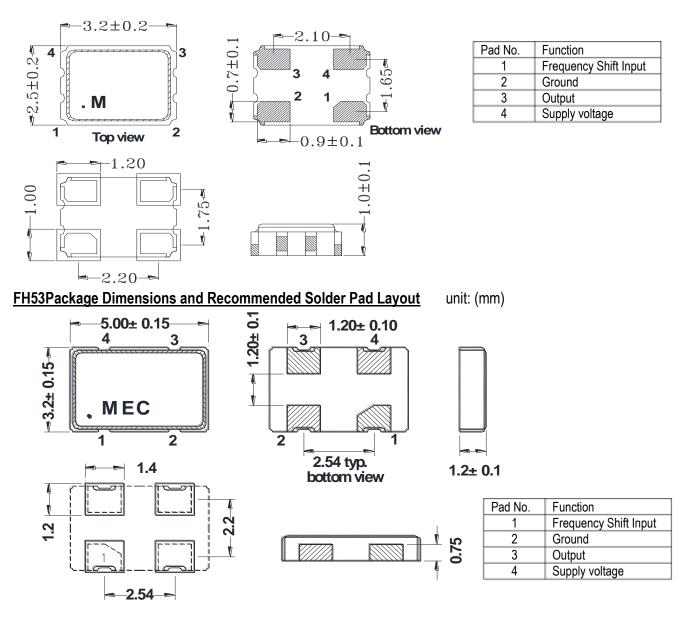
Frequency Shift Control Typical Waveform



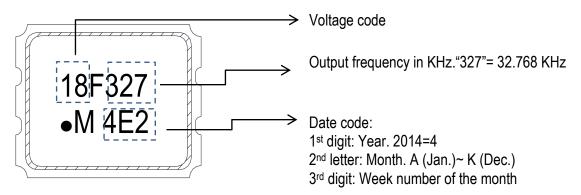
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FH32Package Dimensions and Recommended Solder Pad Layout unit: (mm)



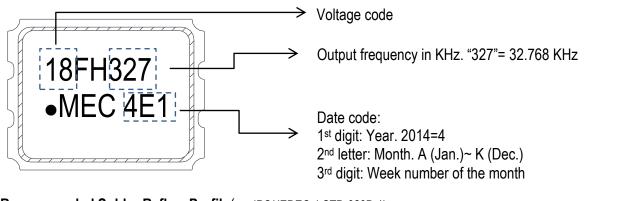
FH32Product Marking "F": Frequency shift keyed silicon oscillator. "•": Pad 1 index (top view); "M": Mercury



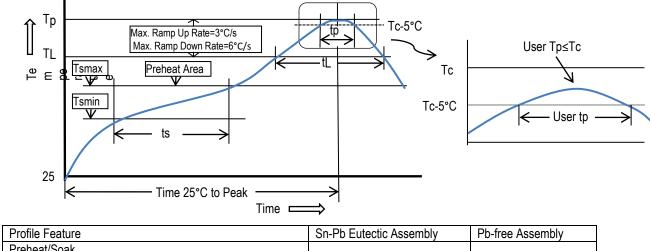
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FH53 Product Marking "FH": Frequency shift keyed silicon oscillator; "•": Pad 1 index (top view); "MEC": Mercury



Recommended Solder Reflow Profile(perIPC/JEDEC J-STD-020D.1)



Prome reature	SIT-PD EULECLIC ASSEMDLY	PD-free Assembly
Preheat/Soak		
- Temperature min. (Ts min.)	100°C	150°C
- Temperature max. (Ts max.)	150°C	200°C
- Time (ts) (Ts min. to Ts max.)	60 to 120 seconds	60 to 180 seconds
Ramp-up rate (T∟to Tp)	3°C / sec. max.	3°C / sec. max.
Liquidous temperature (TL)	183°C	217°C
Time (t _L) maintained above T_L	60 to 150 seconds	60 to 150 seconds
Peak package body temperature (Tp)	235°C	260°C
Time (Tp) within 5°C of the classification temperature Tc	10 to 30 seconds	20 to 40 seconds
Ramp-down rate (Tp to TL)	6°C / second max.	6°C / second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.

All temperatures refer to topside of the package, measured on the package body surface.

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