# A Network Analyzer For Active Components



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DSW RestSystems



Version 2

## Outline

- Review of S-parameters
- Theory of Large-Signal Network Analysis
- The Large-Signal Network Analyzer
- The Calibration
- Active Component Characterization
  - under CW stimulus
  - under modulation stimulus
  - in non-50 Ohm environment
- Modeling
  - Model verification
  - Measurement-based behavioral model
- Conclusions



### **Definition of S-parameters**

As function of frequency  $f_0$ 



$$b_1(f_0) = S_{11}(f_0) a_1(f_0) + S_{12}(f_0) a_2(f_0)$$
  
$$b_2(f_0) = S_{21}(f_0) a_1(f_0) + S_{22}(f_0) a_2(f_0)$$

- S-parameters are a behavioral model
- S-parameters describe completely the linear behavior
- S-parameters cannot be determined properly with one measurement



### **Measuring S-parameters**





### **Example: Packaged FET**





### S-parameter Measurement



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### **Nonlinear Behavior - Harmonic Generation**





 $a_1 = 0 \ dBm$ 











What happens at the input?

Vector Signal Analyzer Display What is the meaning of this measurement?



# **Complete Characterization of Active Components**



# Small-Signal Network Analysis: S-parameters





### Large-Signal Network Analysis



# Large-Signal Network Analysis in a nutshell



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### Vector Network Analyzer





## Large-Signal Network Analyzer



## Acquisition in LSNA



Capturing broadband HF signals (harmonics and modulation) using low-frequency data - acquisition requires samplers



### **Representation Domain: Continuos Wave Signal**



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# Representation Domain: Amplitude and Phase Modulation of Continuos Wave Signal



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## Representation Domain: Periodic Modulated Signals



With proper processing the class of signal is extendable to any type of multi-tone signal



## **Practical Limitations of LSNA**

- Large-Signal Network analysis will be performed using periodic stimuli
  - one tone and harmonics
  - periodic modulation and harmonics
  - other types of multi tones are possible
- The devices under test maintain periodicity in their response



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## LSNA Calibration





Measurement Traceability



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### Available Large-Signal Network Analyzers





### MT4463B - 50 GHz

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# Active HF Component Characterization Requirements

DC IV Characterization

MT4463A/B

Modulation Characterization



Adding ... DC Capability



*Adding* ... Modulation Capability



Adding ... Tuners © Copyright 2005 NMDG Engineering 28 Certain slides are with permission from Agilent Technologies



Adding ... Pre-match Tuner MDG Engineering Leading beyond S-parameters



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### Large-Signal Measurements - CW - Voltage/Current



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### Large-Signal Measurements - CW - Voltage/Current





**Frequency domain** 

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### Large-Signal Measurements - CW - Voltage Waves



NMDG Engineering Leading beyond S-parameters **Time domain** 

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#### 50 Ohm Termination



Large-signal - 50 Ohm Termination







#### **Open Termination**



Large-signal - non-50 Ohm Termination



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#### **Generation of Multi-tone**



### Large-Signal Measurements - Modulation - Voltage/Current



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#### **Frequency domain**

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### Large-Signal Measurements - Modulation - Voltage/Current

Zoom into one of the spectral components (fundamental frequency)



**Frequency domain** 



### Large-Signal Measurements - Modulation - Voltage/Current



NMDG Engineering Leading beyond S-parameters Frequency - Time domain

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### Dynamic AM/AM and AM/PM (two-tone 10kHz spacing)





### Dynamic HDA (two-tone 10kHz spacing)



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## **Component Characterization with Passive Tuners**



### Input and Load Impedance under CW







### DC-IV curve and RF Load Line with "Short"





"Short" Terminated

### Input and Load Impedance under CW

![](_page_45_Figure_1.jpeg)

![](_page_45_Picture_2.jpeg)

![](_page_45_Picture_4.jpeg)

# DC-IV curve and RF Load Line with "Towards Open"

![](_page_46_Figure_1.jpeg)

"Towards Open" Terminated

![](_page_46_Picture_3.jpeg)

### Integration with Load-Pull Software

### LSNA Power Block Diagram

![](_page_47_Picture_2.jpeg)

The waveforms contain \_\_\_\_\_ all aspects of the component behavior

![](_page_47_Figure_4.jpeg)

### **Real-Time Component Characterization - Setup** Circulator Vector Synthesis Signal Generator of Load Impedances Manual $2f_0$ at $f_0$ Tuner (optional) Triplexer Offset + $f_0$ DUT $f_0$ smart signal V, $3f_0$ to scan amplitude and phase (optional)

![](_page_48_Picture_1.jpeg)

## **Real-Time Component Characterization**

![](_page_49_Picture_1.jpeg)

Impedances

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![](_page_50_Picture_13.jpeg)

# **Transistor Model Verification in ADS**

![](_page_51_Figure_1.jpeg)

- The model is brought into the same state as the real component
- The simulated response should be equal to the measured response for a good model
- The comparison is done at **the essential data**, common to both: Voltage and Currents

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![](_page_52_Picture_13.jpeg)

### **Measurement-based Behavioral Modeling**

 $b_1(f_0) = f(a_1(f_0), a_2(f_0))$  $b_2(f_0) = g(a_1(f_0), a_2(f_0))$ 

Valid for

- Range of a1
- Closed area on Smith Chart at output

![](_page_53_Figure_5.jpeg)

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### Measurement-based Behavioral Model in ADS

![](_page_54_Figure_1.jpeg)

Third-Order Intercept Point

![](_page_54_Picture_3.jpeg)

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### Conclusion

- A Network Analyzer for active component characterization has been presented
- It measures the essential information (voltage and currents at component ports) to characterize the nonlinear behavior
  - accurately and completely
  - in a unified way from transistor to system
  - under realistic conditions
- The Network Analyzer is ideal to study the behavior from small-signal to large-signal with one connection using simple and complex signals
- The Network Analyzer is ideal to certify models
- The Network Analyzer is ideal to create behavioral models

Technical information

Sales information

![](_page_56_Picture_11.jpeg)

![](_page_56_Picture_12.jpeg)

http://www.nmdg.be/

![](_page_56_Picture_14.jpeg)

![](_page_56_Picture_16.jpeg)