

This presentation provides a brief overview (excerpts) of an automated test system developed by R. A. Wood Associates

Topics

- Test System Block Diagram
- Main Test Panel
- Calibration Panel
 - View Plots of Current Cal Data Panel
- Configuration Panels
 - Various tests and parameters panels, test limits



LDMOS RF Power Amp Test System





LDMOS Power Amplifier Test Setup (during software development)





2 Types of Main Test Panel Interfaces, selectable from a pull-down menu

- "View Full Test Data" Interface
 - Shows full-page test results for each part tested
 - Used primarily for Engineering tests
 - No Pass/Fail determination
 - Can also be used to view previous device measurements (administrator level)
- "View Summary Results" Interface
 - Shows a line by line summary of each measurement in a text window
 - Pass/Fail determination is made for each test
 - Used primarily for Production tests
 - Shows history of each part in a Lot test (scrolling text window)
 - Yields and failure bins are also shown as the Lot test progresses



Administrator and Operator Levels

• The program has two levels of operation: "Administrator" and "Operator"

Operator Level:

- Primarily for running tests, for production personnel or technicians
- Does not provide capability to set up test conditions
- Calibration allowed

Administrator Level:

- Can be used for running tests or setting up test parameters and conditions
- Configuration Panels allowed
- Calibration allowed
- Utilities Panel allowed
- Capability to view previous part data allowed



Main Test Panel >> View Full Test Data Display (Administrator Level, before test start)

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2/17/2002 Page 6



Main Test Panel >> View Full Test Data Display (Administrator Level, after test)

Note: Some measured data blurred to protect customer info

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Main Test Panel >> View Summary Results Display (Administrator Level, after test)

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Calibration Panel is used to calibrate the test station losses

- All Cal Steps must be run
- Use default signal levels shown if possible

 During Calibration, the software provides prompts and tells the operator what to connect

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<u>File Edit Operate Tools Browse Window H</u> elp	Ţop.
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Calibration Panel Waiting Note: All powers are in units of dBm	
Cal Step 1: Loss to Input Power Meters Most Recent Cal 1 Date 7/3/01 1:02 PM	
Cal 1 Sig Gen 1 Power Cal 1 PM 1A Min Power Cal 1 PM 1A Max Power	
Cal Step 2: Signal 1 vs Signal 2 Balance Most Recent Cal 2 Date 7/3/01 1:20 PM	
Note: Program uses same settings as Cal Step 1 View Plots of Cal Data 2	
Cal Step 3a: 0 dB Return Loss Reference (Short) Most Recent Cal 3a Date 7/3/01 2:08 PM	
Cal 3a Sig Gen 1 Power -13.00 View Plots of Cal Data 3a	
Cal Step 3b: 0 dB Return Loss Reference (Open) Most Recent Cal 3b Date 7/3/01 2:20 PM	
Cal 3b Sig Gen 1 Power	
Cal Step 4: DUT Output Calibration Most Recent Cal 4 Date 7/3/01 2:45 PM	
Cal 4 Sig Gen 1 Power Frequency Tolerance (MHz)	
Set Up Cal Frequencies View Test Setup View Plots of Current Cal Data Run Selected Calibrations Save Cal Data EXIT	
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Main Test Panel >> Calibration Panel





Calibration >> View Plots of Current Cal Data Panel

🔁 Cal_data_plot.vi

<u>File E</u>dit <u>Operate Tools Browse Window Help</u>



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LDMOS RF Power Amp System Automated Measurements

The Configuration Panel allows tests to be selected and defines the test parameters

- RF Measurements are performed at low power and high power
- Currently available tests are shown
- All test information and parameters are stored in Configuration files



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Main Test Panel >> Configuration Panel







Configure IMD 3 5 7 Low

Two Tone Low Start Gain (dB)		
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Two Tone Low Desired Output Power (dBm) 34.00		
Two Tone Low Output Power Tolerance (+/-dB)		
0.10		
Two Tone Low Frequency Offset (MHz)		
5.00		
Two Tone Low Power Offset(dBm)		
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Note: "Two Tone Low Desired Output Power (dBm)" is	Define IMD Low Test Limits and More	
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SAVE and EXIT CANCEL Test Setup Conliguied?		

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