



**SPECIFICATION
FOR
LCD Module
RX043A-0204TP**

MODULE:	RX043A-0204TP
CUSTOMER:	

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General Description

* Description

RX043A-0204TP is a color active matrix TFT (Thin Film Transistor) LCD (liquid crystal display) that uses amorphous silicon TFT as a switching device. This model is composed of a Transmissive type TFT-LCD Panel, driver circuit, back-light unit. The resolution of a 4.3" TFT-LCD contains 480RGB x 272 pixels, and can display up to 16.7M colors.

* Features

- Low Input Voltage: VCC:3.0-V~3.6V
- Display Colors of TFT LCD: 16.7M colors
- CPU Interface: 24bit parallel (RGB) input timing
- Internal Power Supply Circuit.

General Information Items	Specification	Unit	Note
	Main Panel		
Display area(AA)	95.04(H) * 53.86(V) (4.3 inch)	mm	-
Driver element	a-Si TFT active matrix	-	-
Display colors	16.7M	colors	-
Color Gamut	50	%	-
Number of pixels	480(RGB) *272	dots	-
Pixel arrangement	RGB vertical stripe	-	-
Pixel pitch	0.198(H) *0.198(V)	mm	-
Viewing angle	6:00	o'clock	-
Drive IC	ILI6480Q	-	-
Display mode	Transmissive/ Normally White	-	-
Operating temperature	-20~+70	℃	-
Storage temperature	-30~+80	℃	-

* Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal(H)	-	105.5	-	mm	-
	Vertical(V)	-	67.2	-	mm	-
	Depth(D)		3.95		mm	-
Weight		-	TBD	-	g	-



1. Optical Characteristics

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥ 10)	θ_L	$\Phi=180^\circ$ (9 o'clock)	60	70	-	degree	Note 1
	θ_R	$\Phi=0^\circ$ (3 o'clock)	60	70	-		
	θ_T	$\Phi=90^\circ$ (12 o'clock)	40	50	-		
	θ_B	$\Phi=270^\circ$ (6 o'clock)	60	70	-		
Response time	T_{ON}	Normal $\theta=\Phi=0^\circ$	-	10	20	msec	Note 3
	T_{OFF}		-	15	30	msec	Note 3
Contrast ratio	CR		400	500	-	-	Note 4
Color chromaticity	W_X		0.26	0.31	0.36	-	Note 2 Note 5
	W_Y		0.28	0.33	0.38	-	
Transmittance	Tr		-	6.26	-	%	

Test Conditions:

1. $V_{DD}=3.3V$, $I_L=20mA$ (Backlight current), the ambient temperature is $25^\circ C$.
2. The test systems refer to Note 2.



Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

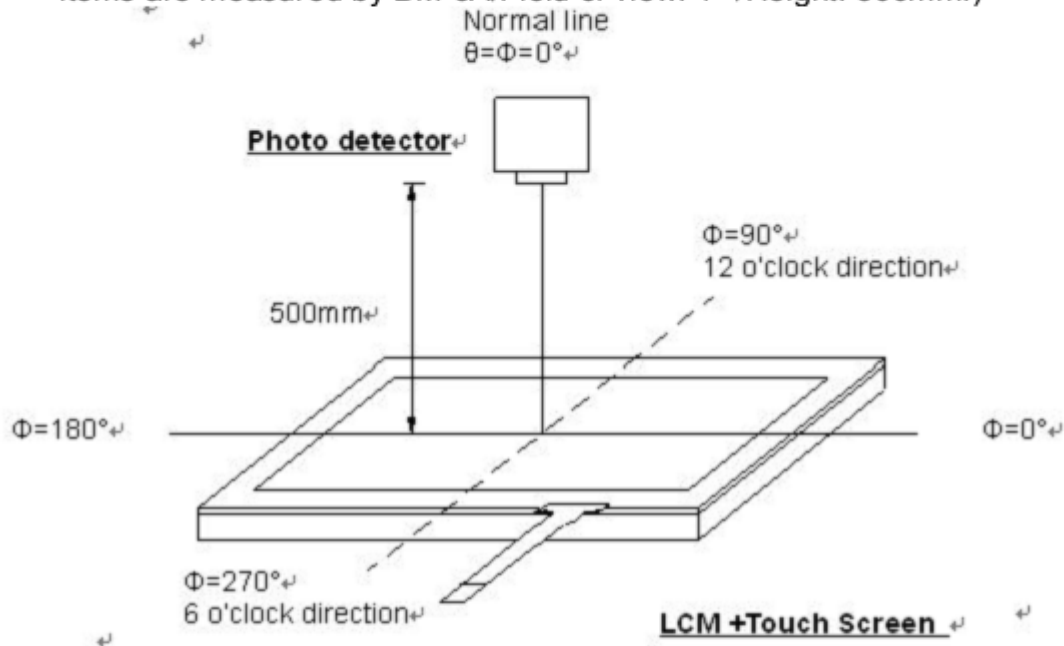


Fig. 4-2 Optical measurement system setup

Fig. Optical measurement system setup

Note 2: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 3: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is $V_{LED}=5.0V$.



2. Electrical Characteristics

2.1 Absolute Maximum Rating (Ta=25 GND=0V)

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Digital Supply Voltage	VDDIO	-0.5	-	+ 5.0	V	-
Analog Supply Voltage	VDD	-0.3	-	+ 7.0	V	-

2.2 DC Electrical Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Digital Supply Voltage	VDD	3.0	3.3	3.6	V	-
Input high Voltage	VIH	0.8VDD	-	VDD	V	-
Input low Voltage	VIL	GND	-	0.2VDD	V	-
Clock frequency	DCLK	5	9	12	MHZ	-

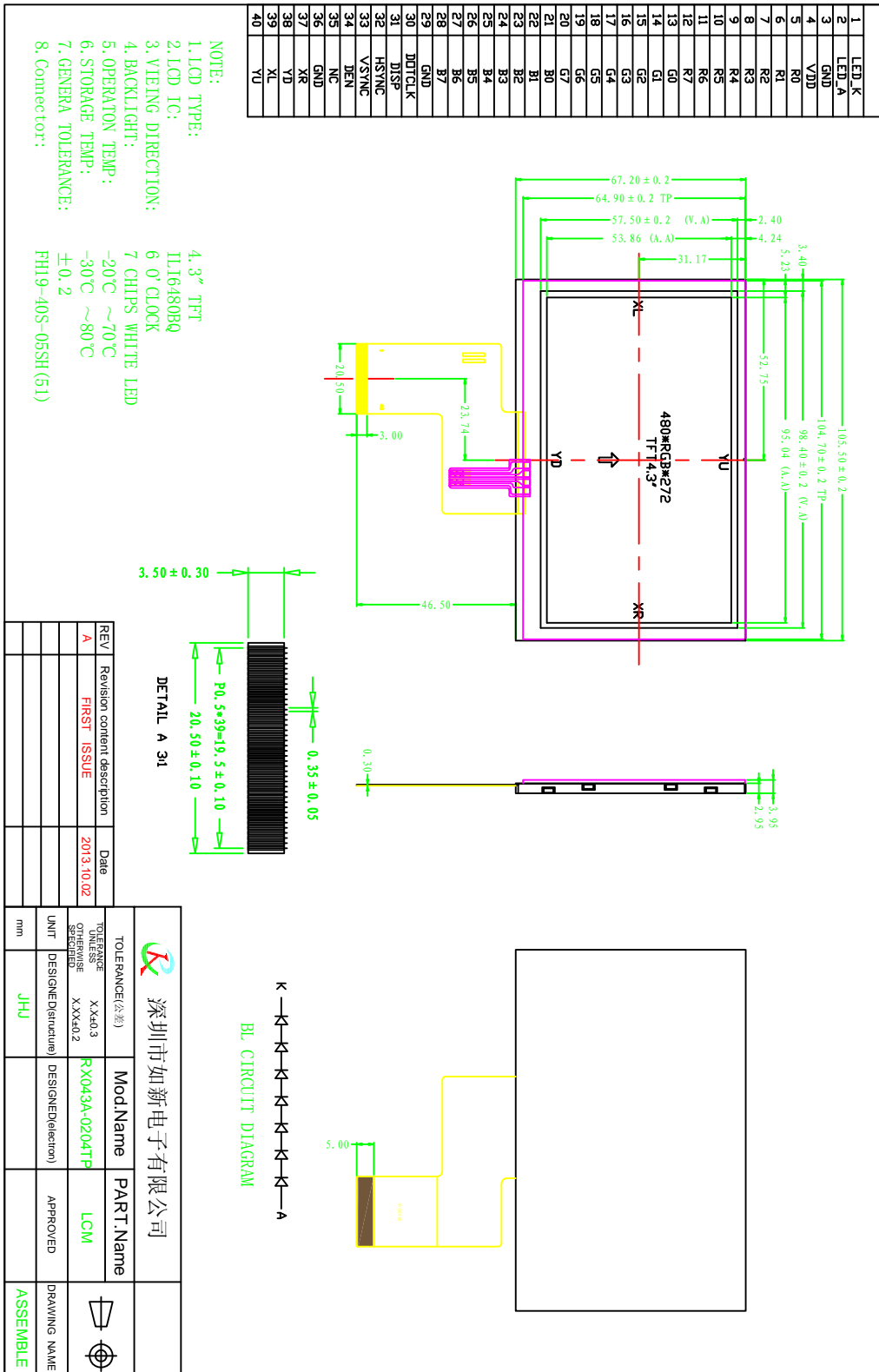
2.3 LED Backlight Characteristics

The back-light system is edge-lighting type with 7chips White LED in serial

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Forward Current	I _F	-	20	-	mA	
Forward Voltage	V _F		20.5		V	-
LCM Luminance	L _V		180	-	cd/m ²	
Uniformity	AV _g	70	-	-	%	-



3. Outline dimension





4. Input terminal Pin Assignment

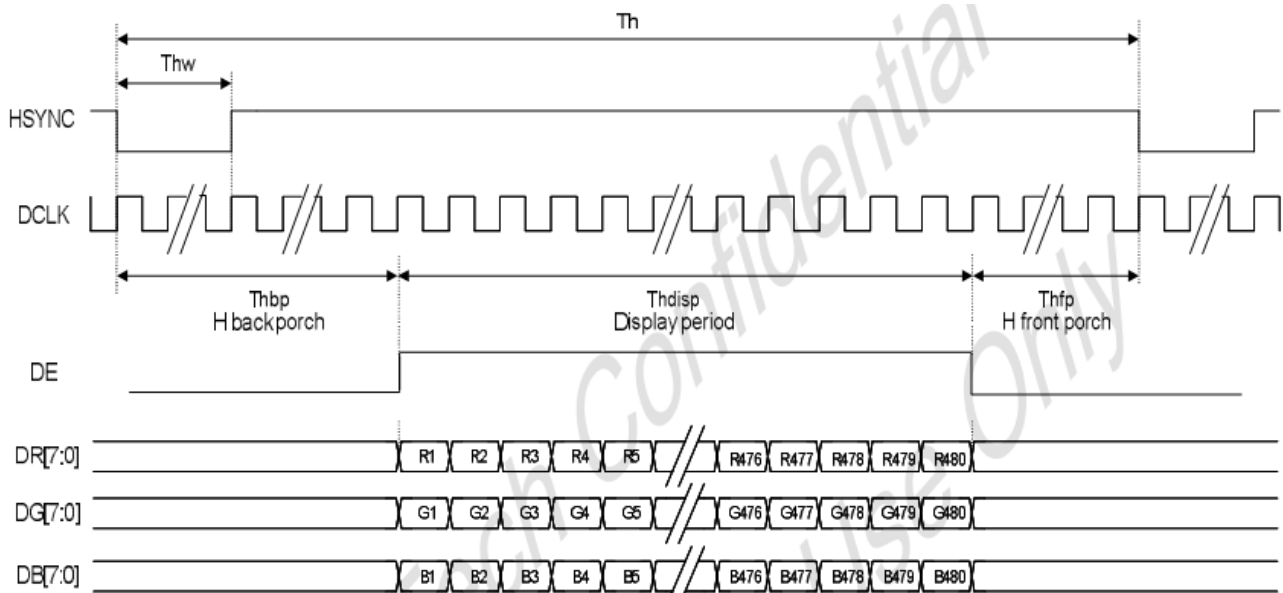
Pin NO.	Symbol	Function
1	LED_K	Backlight-
2	LED_A	Backlight+
3	GND	Ground
4	VDD	Power for Digital Circuit
5	R0	Red Date Bit0
6	R1	Red Date Bit1
7	R2	Red Date Bit2
8	R3	Red Date Bit3
9	R4	Red Date Bit4
10	R5	Red Date Bit5
11	R6	Red Date Bit6
12	R7	Red Date Bit7
13	G0	Green Date Bit0
14	G1	Green Date Bit1
15	G2	Green Date Bit2
16	G3	Green Date Bit3
17	G4	Green Date Bit4
18	G5	Green Date Bit5
19	G6	Green Date Bit6
20	G7	Green Date Bit7
21	B0	Blue Date Bit0
22	B1	Blue Date Bit1
23	B2	Blue Date Bit2
24	B3	Blue Date Bit3
25	B4	Blue Date Bit4
26	B5	Blue Date Bit5
27	B6	Blue Date Bit6
28	B7	Blue Date Bit7
29	GND	Ground
30	DOTCLK	Dot Date Clock
31	DISP	Display ON/OFF
32	HSYNC	Horizontal SYNC input
33	VSYNC	Vertical SYNC input
34	DEN	Data enable input
35	NC	NC
36	GND	Ground
37	XR	TP XR
38	YD	TP YD
39	XL	TP XL
40	YU	TP YU



5. Timing Characteristic

Please refer to ILI6480Q data sheet for more details.

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Horizontal display area	thdisp	480			DCLK
DCLK frequency	Fclk	5	9	12	MHZ
1 Horizontal line	th	520	525	800	DCLK
HSYNC pulse width	thw	1	2	-	
HSYNC Back Porch (blanking)	thbp	3	8	31	
HSYNC front porch	thfp	4	5	65	
Vertical display area	tvdisp	272			H
VSD period time	tv	277	288	400	
VSD pulse width	tvw	1	10	-	
VSD Back Porch (blanking)	tvbp	3	8	31	
VSD front porch	tvfp	2	8	97	





6. Reliability Test Result

Item	Condition	Sample Size	Test Result	Note
Low Temperature Operating Life test	-20°C, 96HR	3ea	pass	-
Thermal Humidity Operating Life test	40°C, 90%RH, 96HR	3ea	pass	-
Temperature Cycle ON/OFF test	-20°C ↔ 70°C, ON/OFF, 20CYC	3ea	pass	(1)
High Temperature Storage test	80°C, 96HR	3ea	pass	-
Low Temperature Storage test	-30°C, 96HR	3ea	pass	-
Thermal Shock Resistance	The sample should be allowed to stand the following 5 cycles of operation: TSTL for 30 minutes -> normal temperature for 5 minutes -> TSTH for 30 minutes -> normal temperature for 5 minutes, as one cycle, then taking it out and drying it at normal temperature, and allowing it stand for 24 hours	3ea	pass	
Box Drop Test	1 Corner 3 Edges 6 faces, 66cm(MEDIUM BOX)	1box	pass	-

Note (1) ON Time over 10 seconds, OFF Time under 10 seconds

7. Packing

TBD



8. Cautions and Handling Precautions

8.1 Handling and Operating the Module

(1) When the module is assembled, it should be attached to the system firmly.

Do not warp or twist the module during assembly work.

(2) Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.

(3) Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.

(4) Do not allow drops of water or chemicals to remain on the display surface.

If you have the droplets for a long time, staining and discoloration may occur.

(5) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

(6) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane.

Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth.

In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.

(8) Protect the module from static; it may cause damage to the CMOS ICs.

(9) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(10) Do not disassemble the module.

(11) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.

(12) Pins of I/F connector shall not be touched directly with bare hands.

(13) Do not connect, disconnect the module in the "Power ON" condition.

(14) Power supply should always be turned on/off by the item 6.1 Power On Sequence & 6.2 Power Off Sequence

8.2 Storage and Transportation.

(1) Do not leave the panel in high temperature, and high humidity for a long time.

It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%

(2) Do not store the TFT-LCD module in direct sunlight.

(3) The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.

(4) It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module.



In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.

(5) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.