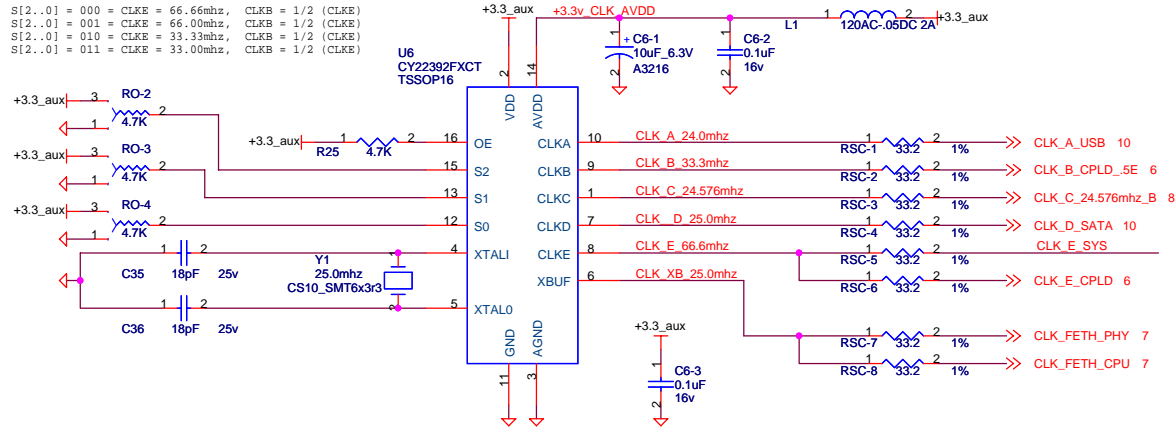
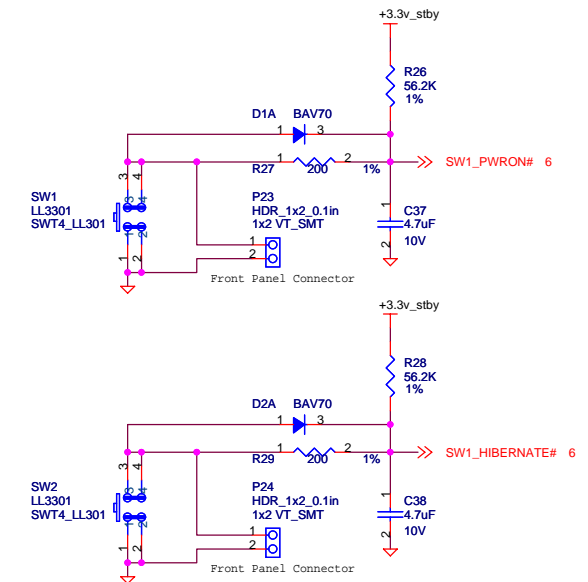


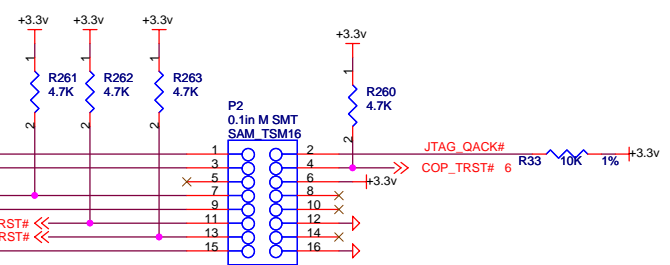
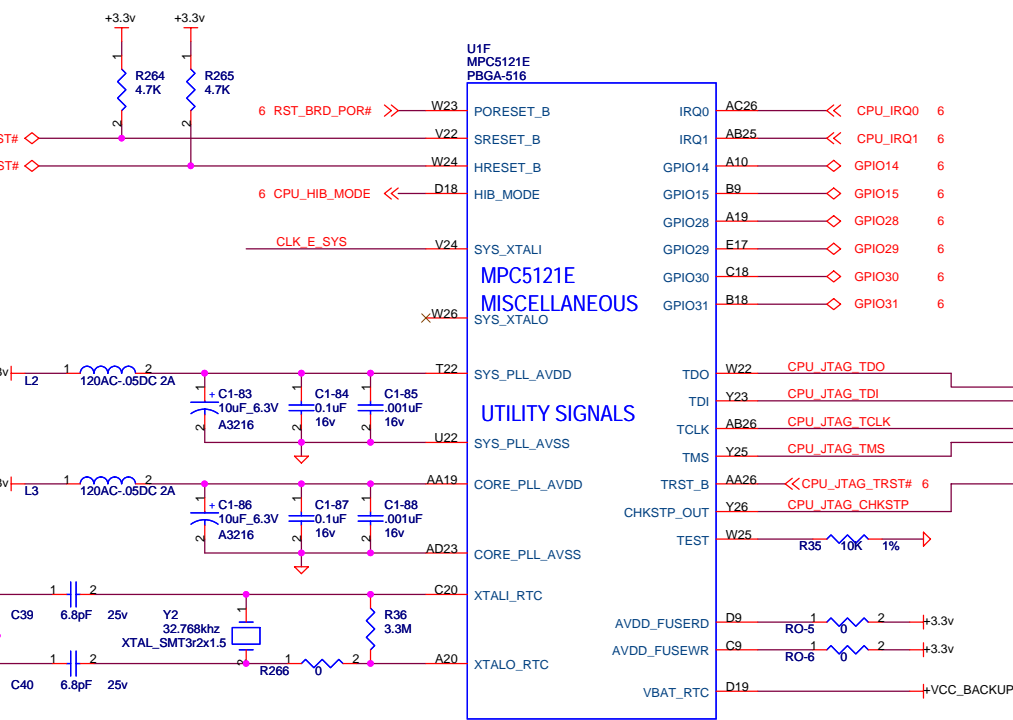
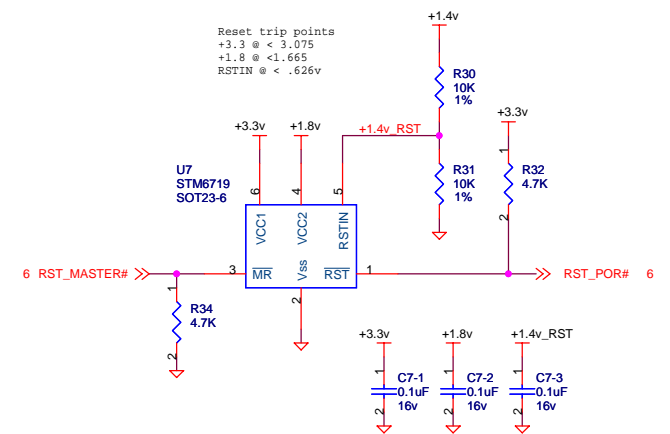
S[2..0] = 000 = CLKE = 66.66mhz, CLKB = 1/2 (CLKE)  
S[2..0] = 001 = CLKE = 66.00mhz, CLKB = 1/2 (CLKE)  
S[2..0] = 010 = CLKE = 33.33mhz, CLKB = 1/2 (CLKE)  
S[2..0] = 011 = CLKE = 33.00mhz, CLKB = 1/2 (CLKE)



CLOCK RULES:  
All Clock traces are to be as short as possible  
CLK\_E\_SYS = CLK\_E\_CPLD  
CLK\_FETH\_PHY = CLK\_FETH\_CPU



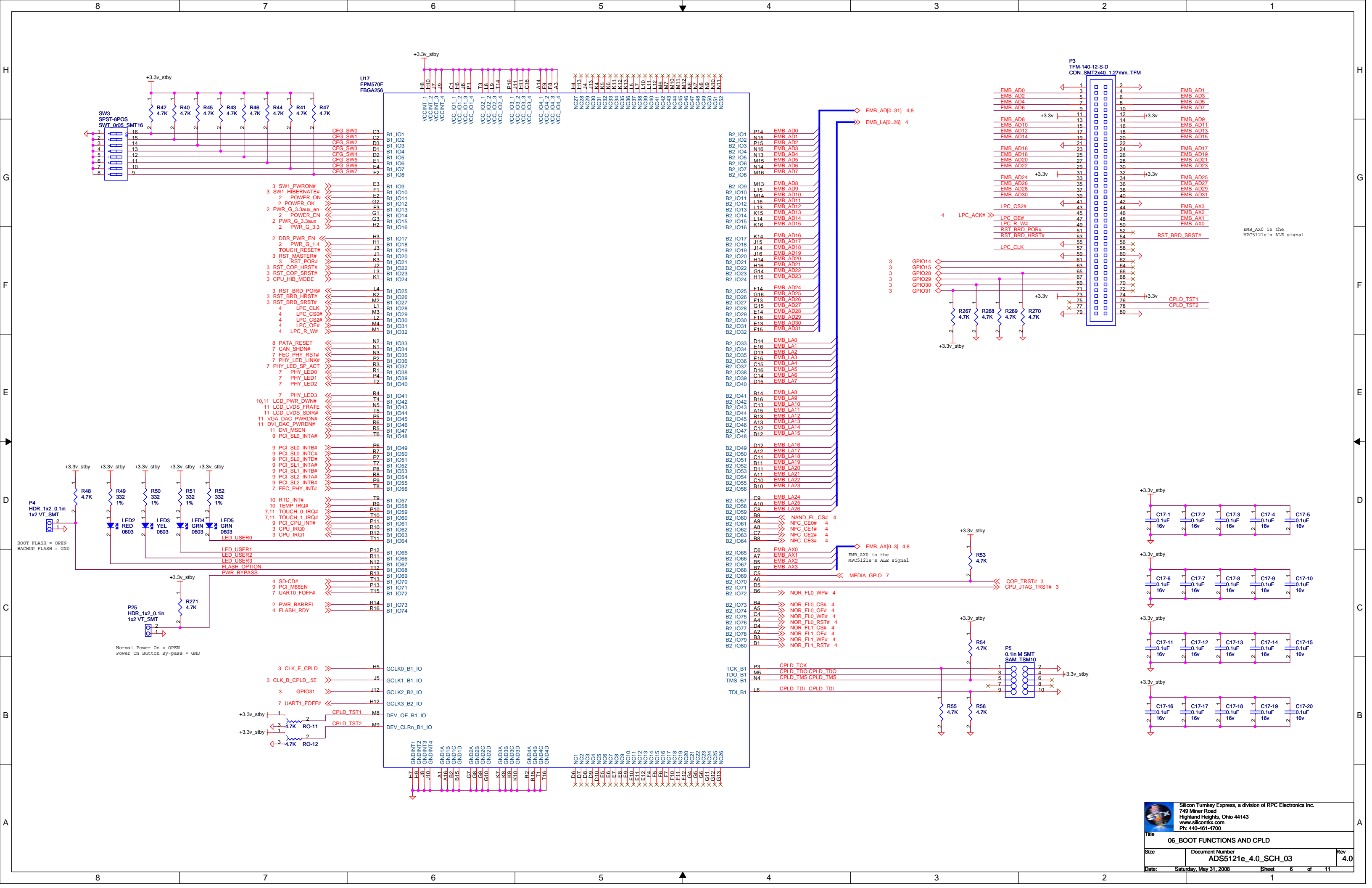
Reset trip points  
+3.3 @ < 3.075  
+1.8 @ < 1.665  
RSTIN @ < .626v





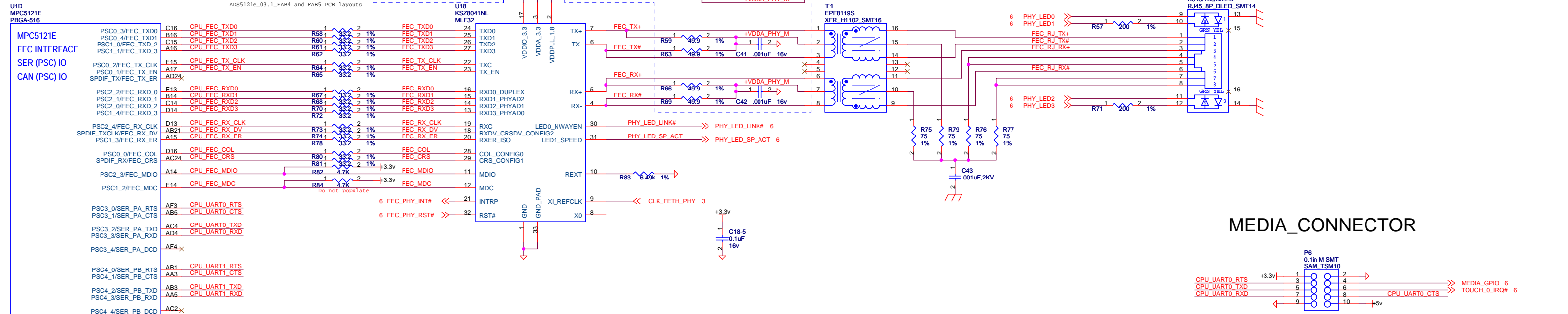




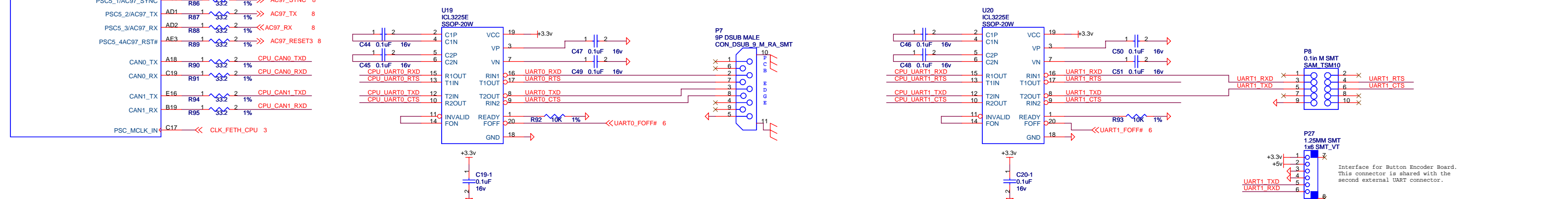


Silicon Turnkey Express, a division of RPC Electronics Inc.  
749 Miner Road  
Highland Heights, Ohio 44113  
www.silicontx.com  
Ph: 440-461-4700

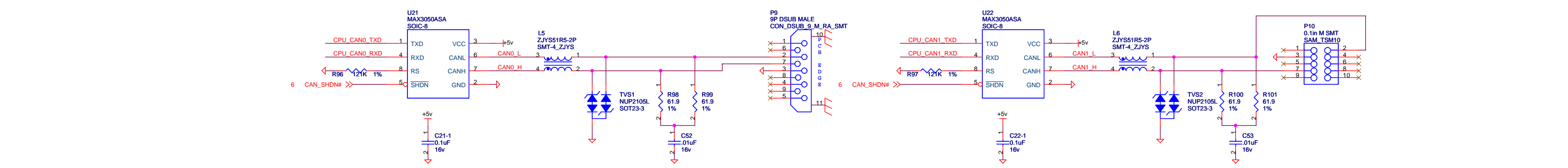
# 10/100 BASET ETHERNET PHY INTERFACE AND CONNECTOR



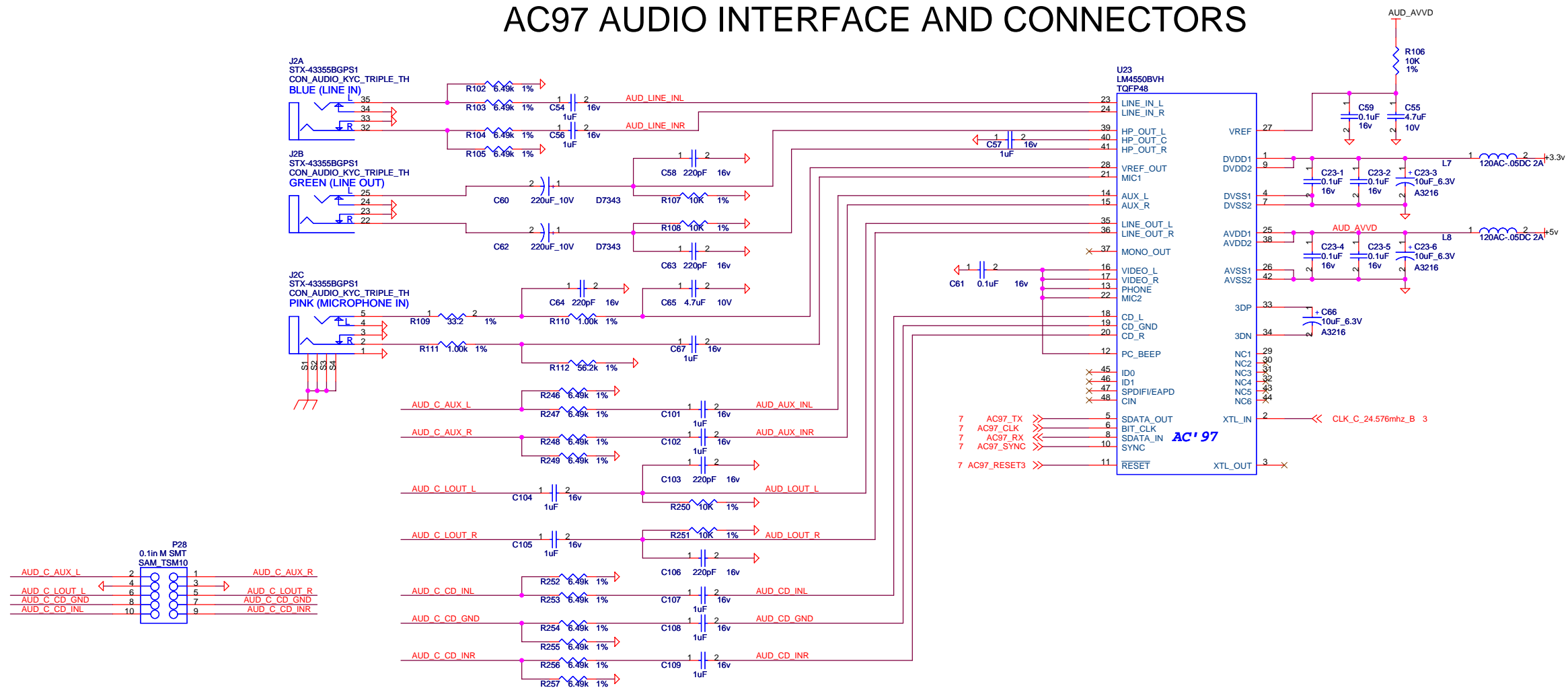
## RS232 UARTS



## CAN BUS



## AC97 AUDIO INTERFACE AND CONNECTORS



## PATA INTERFACE AND CONNECTOR

