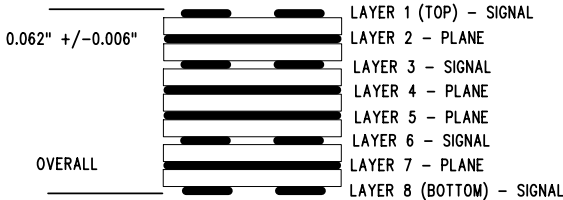


NOTES:

- FABRICATE BOARD PER:  
MIL-P-55110  
IPC TYPE 3, CLASS 2.
- MATERIAL:  
LAMINATE AND PREPREG SHALL CONFORM TO IPC-4101/26 OR BETTER. MUST BE "LEAD FREE COMPATIBLE", MEET ROHS DIRECTIVE 2002/95/EC, AND HAVE A FLAMMABILITY RATING OF UL 94V-1 MINIMUM. SEE "DETAIL A" FOR LAYERING STRUCTURE.
- COPPER THICKNESS:  
OZ. EXTERNAL OZ. INTERNAL LAYERS
- FINISH:  
SMOBC WITH HASL PLATING.  
TIN/LEAD PLATING.  
HARD GOLD PLATING (DEPICTED IN AREA SHOWN)  
ENIG (Electroless Nickel, Immersion Gold) per IPC-4552
- SOLDER MASK:  
LPI PER IPC-SM-840 (CURRENT REV) WITH SEMI-GLOSS FINISH. MUST BE "LEAD-FREE PROCESS COMPATIBLE"  
Encroach the s/mask onto the via pads, leaving a 3-mil clearance per side of the drill to keep the s/mask out of these holes. Then plug/ cap these vias on the top side (where the BGAs are located) per print instruction "after" the application of the Gold immersion. The bottom side will remain encroached.
- SILKSCREEN:  
NONE  
WHITE (ALL SILKSCREEN TO BE REMOVED FROM ANY EXPOSED PLATED AREAS) MUST BE "LEAD-FREE PROCESS COMPATIBLE"
- DIMENSIONING AND TOLERANCING PER ANSI-Y-14.5 (1982).
- ALL UNUSED INTERNAL PADS SHALL BE REMOVED.
- COPPER ON ANY LAYER CLOSER THAN .015 FROM THE EDGE SHALL BE TRIMMED BACK A/R.
- COPPER THEIVING MAY BE ADDED TO TOP AND/OR BOTTOM SIDE. THE CLOSEST TO ANY COPPER FEATURE IS .050.
- MUST ADD BREAK AWAY RAILS AS A MINIMUM, OR PREFERED A PANEL SIZE OF 18x24.
- PANEL DRAWING MUST BE RETURNED
- ALL VIAS SHALL BE PLUGGED
- manufacture with rout tolerance at +/--.005"
- The via tolerance (holes .015" and under) will be +.003" - finished hole size- the component holes at +/--.003" and the unplated holes at +/--.002"

LAYERING STRUCTURE DETAIL

NOTE: THIS IS A CONTROLLED IMPEDANCE PRINTED CIRCUIT BOARD. ASSUMED VALUES AND EXPECTED IMPEDANCE ARE AS FOLLOWS:  
\* 1.00 OZ COPPER - ALL LAYERS  
\* 4 MIL TRACES, 50 OHMS +/- 5% - ALL SIGNAL LAYERS  
\* 4 MIL TRACES (DIFF PAIRS), 100 OHMS +/- 5% - ALL SIGNAL LAYERS  
\* TRACE WIDTHS MAY BE MODIFIED IN ORDER TO MEET THE IMPEDANCE REQUIREMENTS (0.00325" MIN FINISHED)



FILM TOOL REFERENCE CHART

QTY	DESCRIPTION
1	DRILL DRAWING
8	MASTER LAYER PATTERNS
2	MASTER SOLDER MASK, BOTH SIDES
2	MASTER SILKSCREEN, BOTH SIDES

UNLESS OTHERWISE SPECIFIED  
ALL DIMENSIONS IN INCHES  
TOLERANCES  
FRACTIONS +/-1/64  
ANGLES +/-2 DEG.  
DEC XX +/--.02 XXX +/--.005

PROPRIETARY DATA

DRAWN  
CHECKED  
ENGR  
APPROVED  
RLSE DATE

TITLE	
ADS5121e	
SIZE	PART NUMBER
D	ADS5121e_3.2_PCB_01
SCALE	REV
1/1	4.0
SHEET	8 OF 1