

SONET/SDH(E4/STM-1) DUAL LINE INTERFACE TRANSFORMERS

FEATURES

- Compliant with ITU-T/G.703
- Supports 139.264 (E4) and 155.52 (STM-1) Mbps for Coded Mark Inversion (CMI) interfaces
- Designed to interface with AMCC's chip sets S3015/S3016 and S3031B for use with 75 Ω coaxial cable
- Dual design supports transmit and receive circuitry or PHY chip transmit cable and monitor outputs
- Fast rise time and low signal distortion

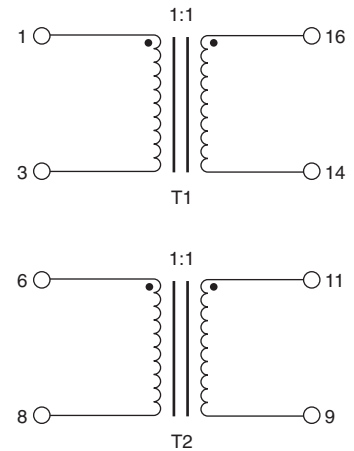
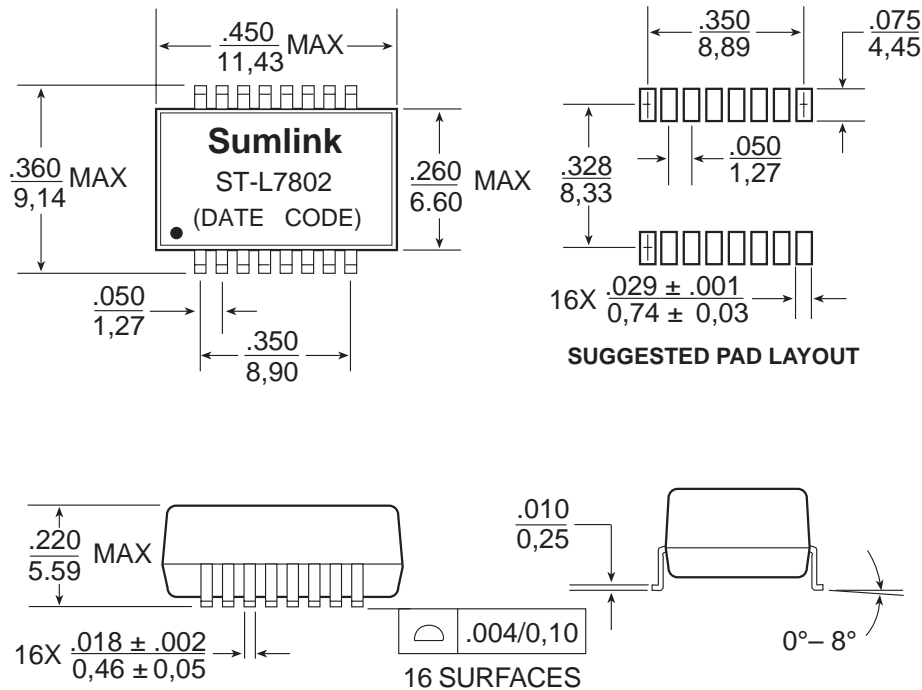


Electrical Specifications @ 25°C — Operating Temperature -40°C to +85°C

Part Number	Turns Ratio (±5%)	Primary Inductance OCL (μH MIN) @ 20m Vrms, 100 kHz	Leakage Inductance L _L (nH MAX)	Interwinding Capacitance C _{ww} (pF MAX)	DC Resistance DCR (Ω MAX)	Hipot (Vrms MIN)
ST-L7802	1 : 1	42.0	100	6.0	.25	1500

Mechanical

Schematic



Dimensions: Inches
mm
Unless otherwise specified, all tolerances are ± .010 / 0.25