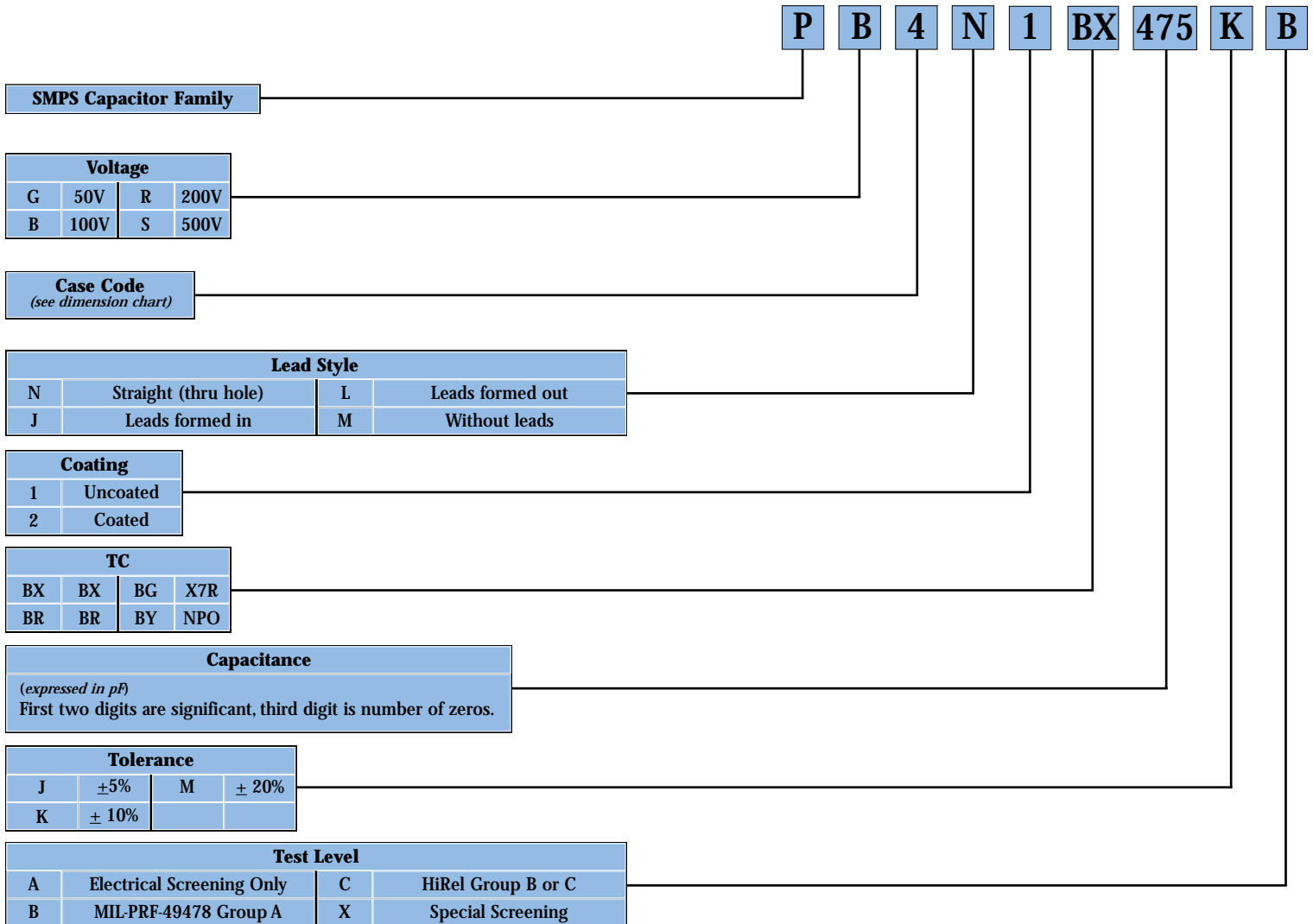
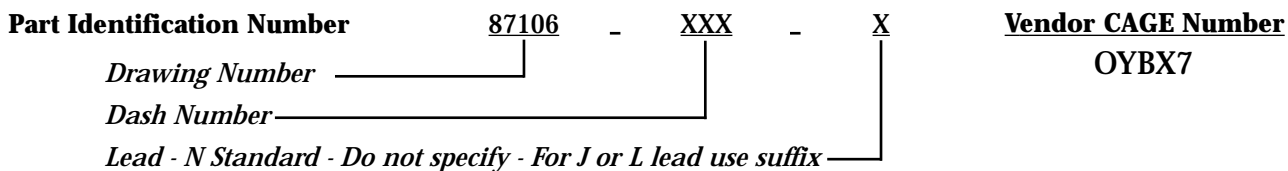


SMPS Ceramic Capacitors - Ordering Information



Ordering Information for DSCC Drawings 87106 & 88011



Notes:

1. Dimensions are in inches.
2. Unless otherwise specified, tolerances are ± .010 inch (0.25 mm).
3. Lead frame configuration is shown as typical above the seating plane.
4. See Table I for specific maximum A dimension. For maximum B dimension, add .065 inch (1.65 mm) to the appropriate A dimension. For all lead styles, the number of chips is determined by the capacitance and voltage rating.
5. For case code 5, dimensions shall be .100 inch (2.54 mm) maximum and .012 inch (0.30 mm) minimum.
6. Lead alignment within pin rows shall be within ± .005 inch (0.13 mm).



Military Series DSCC Drawings 87106 and 88011 - PERFORMANCE REQUIREMENTS

Design, construction, and physical dimensions: The design, and construction, shall be as specified in MIL-PRF-49470 and physical dimensions shall be as specified in Outline Drawing and Dimension Chart.

Temperature Coefficient

DSCC Drawing	Voltage	Bias = 0 Voltage	Bias = Rated Voltage
87106	50/100V	± 15%	+ 15, - 25%
87106	200V	± 15%	+ 15, - 40%
87106	500V	± 15%	+ 15, - 50%
88011	All Voltages	0 ± 30 ppm/°C	0 ± 30 ppm/°C

Capacitance - See Table 1: Measured in accordance with MIL-STD-202, method 305 (1kHz ± 100Hz at 1.0 Vrms at 25 °C).

Dissipation Factor: Dissipation factor shall be 2.5 percent maximum (measured under the same conditions as capacitance).
COG: Dissipating factor shall be 0.15% maximum for 88011.

Insulation Resistance: a) At + 25 °C, rated voltage : 100KMΩ or 1,000MΩ - μF, whichever is less
b) At +125 °C, rated voltage : 10KMΩ or 100MΩ - μF, whichever is less

Dielectric Withstanding Voltage: Dielectric withstanding voltage shall be 2.5 times rated voltage except 500 V rated parts at 1.5 times rates voltage. In accordance with MIL-PRF-49470.

Aging Rate: Aging rate shall be -2.0 percent maximum for each decade-hour for 87106. N/A for 88011.

Capacitor Tolerance: K = ± 10 percent, M = ± 20 percent for 87106.
J = ± 5 percent, K = ± 10 percent for 88011.

Solderability of terminals: In accordance with MIL-PRF-49470.

Resistance to Soldering Heat: In accordance with MIL-PRF-49470.

Shock: In accordance with MIL-PRF-49470.

Immersion Cycling: In accordance with MIL-PRF-49470.

Moisture Resistance: In accordance with MIL-PRF-49470.

Life: Life shall be 200 percent of voltage except 500 V rated parts at 120 percent of rated voltage applied at +125 °C for 1,000 hours in accordance with MIL-PRF-49470.

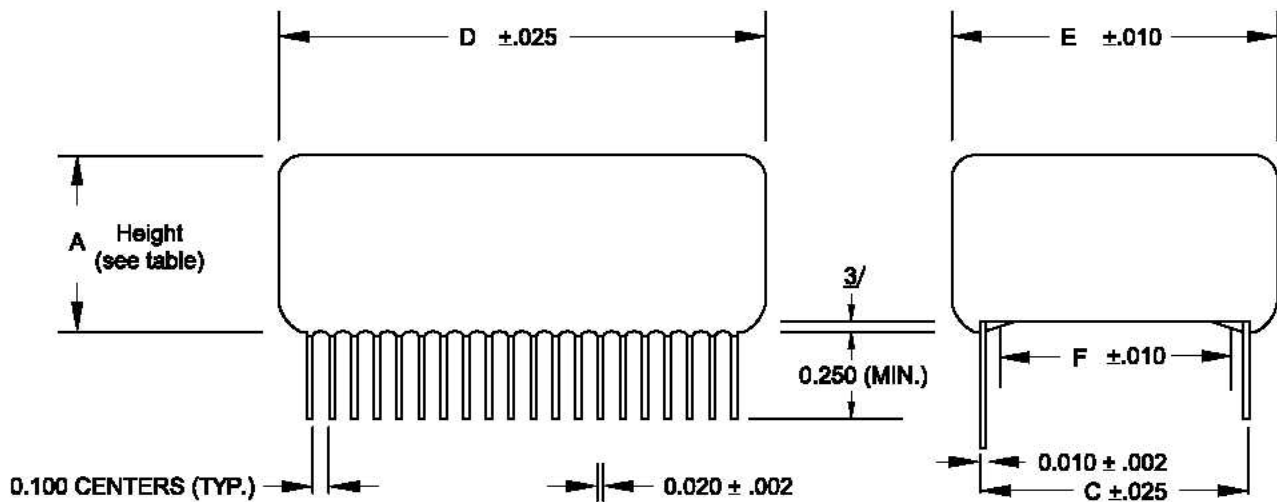
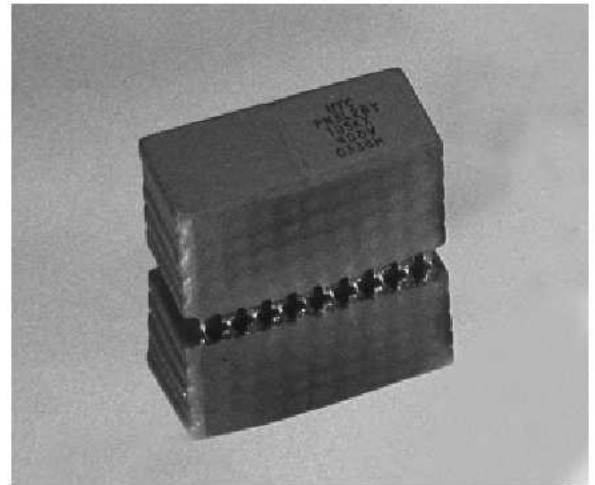
Thermal Shock: In accordance with MIL-PRF-49470.

Voltage Conditioning: In accordance with MIL-PRF-49470 except 500V rated parts at 120% of rated voltage at 125 °C.

Terminal Strength: In accordance with MIL-PRF-49470 (each lead shall be bent away from capacitor body 90° from the original position and then tested in accordance with Method 211 of MIL-STD-202).

Marking: Marking shall be in accordance with MIL-STD-1285, except the P/N shall be as specified in paragraph 1.2 of 87106, or 88011. The manufacture's name or code and date code as a minimum, except case size 4 and 5 shall be marked with coded cap and tolerance minimum. Full marking shall be included on the package.

- Epoxy Conformal Coated High Frequency Switch Mode Power Supply Capacitor
- Intended for use in airborne electronic equipment
- Rugged epoxy coating
- Increased mechanical protection and environmental protection
- NPO and X7R Dielectrics
- 50-500 VDC Ratings
- N, J or L Lead styles available
- Low ESR and ESL



Dimension Chart

CASE CODE	DIM A MAX 1/	DIM B MAX 2/	DIM C $\pm .025$	DIM D		DIM E MAX	DIM F MIN	LEADS PER SIDE
				MIN	MAX			
1	.655	.715	.450	1.950	2.100	.525	.180	20
2	.655	.715	.800	1.450	1.560	.595	.530	15
3	.655	.715	.450	0.950	1.080	.525	.180	10
4	.655	.715	.400	0.350	0.45	.465	.180	4
5	.655	.715	.250	0.224	0.300	.325	.080	3
6	.655	.715	1.250	1.950	2.100	1.375	.980	20

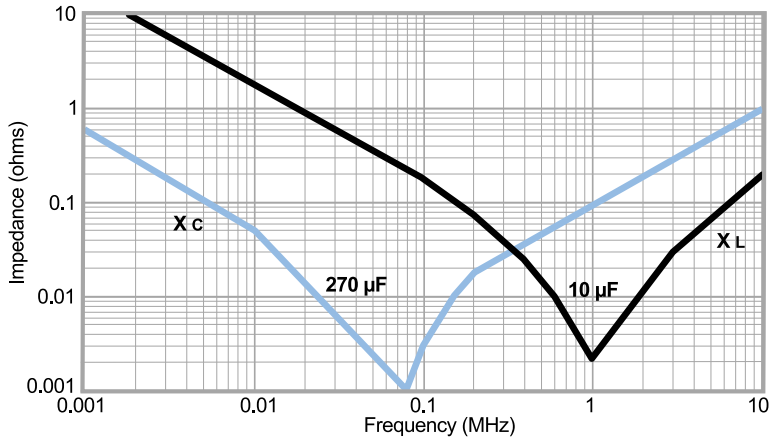
1/ Reference Table 1 for number of chips in the stack ("Chip #") and multiply by 0.120" for the actual DIM A.

2/ Add 0.050" to the value obtained in 1/ for the actual DIM B.

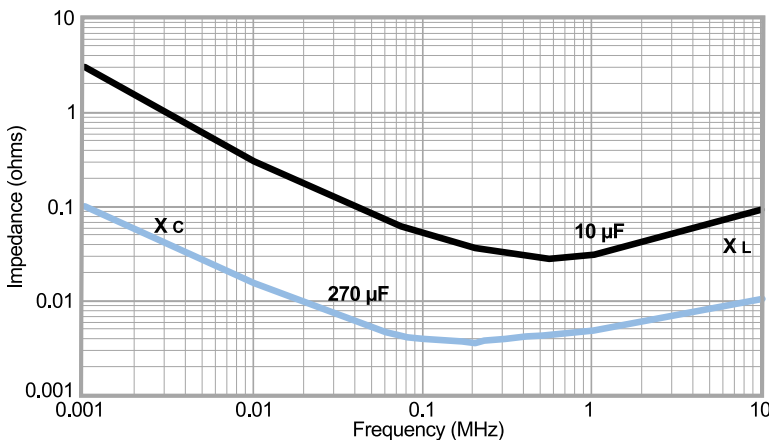
3/ No coating below seating plane.

Charts for SMPS - Typical

ESR vs. Frequency



ESL vs. Frequency



MIL-PRF-49470 (Equivalent)

Union Technology Corp. is a qualified supplier using MIL-STD-790 inspection criteria. We have manufactured thousands of parts to DSCC drawings 87106 and 88011 during the past years. A number of key customers have asked us to produce these parts with additional high reliability screening to their requirements or developed specification control drawings. We can screen products in conformance to MIL-PRF-49470.

Custom Products and Application Specific Devices

Our engineering staff is ready to address your need for application specific products. We have assembled a wide variety of devices meeting unique customer requirements. Our staff is ready to address your special requirements by modifying existing designs or creating a new design. We can employ a variety of chip sizes and configurations. Please contact the factory for additional information.

DSCC Drawing 87106

High Frequency SMPS Ceramic Capacitor - Table I



Cap. uF	50VDC			100VDC			200VDC			500VDC		
	N Lead +/- 10% +/- 20%	J Lead +/- 10% +/- 20%	MAX DIM A	N Lead +/- 10% +/- 20%	J Lead +/- 10% +/- 20%	MAX DIM A	N Lead +/- 10% +/- 20%	J Lead +/- 10% +/- 20%	MAX DIM A	N Lead +/- 10% +/- 20%	J Lead +/- 10% +/- 20%	MAX DIM A
0.15												
0.18												
0.22												
0.27												
0.33												
0.39												
0.47												
0.56												
0.68												
0.82												
1.0	001	241	.120	055	301	.120	113	361	.240	173	421	.120
1.2	003	243	.120	057	303	.120	115	363	.240	175	423	.240
1.5	005	245	.240	059	305	.240	117	365	.360	177	425	.240
1.8	007	247	.240	061	307	.240	119	367	.360	179	427	.240
2.2	009	249	.240	063	309	.360	121	369	.360	181	429	.360
2.7	011	251	.360	065	311	.360	123	371	.480	183	431	.360
3.3	013	253	.360	067	313	.480	125	373	.480	185	433	.360
3.9	015	255	.480	069	315	.480	127	375	.650	187	435	.480
4.7	017	257	.480	071	317	.480	129	377	.650	189	437	.650
5.6	019	259	.560	073	319	.560	131	379	.650	191	441	.650
6.8	223	261	.360	075	321	.360	133	381	.480	193	443	.360
8.2	021	263	.360	077	323	.480	135	383	.480	195	445	.480
10	023	265	.480	079	325	.480	137	385	.480	197	447	.480
12	025	267	.480	081	327	.480	139	387	.480	199	449	.480
15	027	269	.650	083	331	.480	141	389	.480	201	451	.480
18	029	271	.240	085	333	.360	143	391	.360	203	453	.360
22	031	273	.360	087	335	.360	145	393	.360	205	455	.360
27	033	275	.360	089	337	.360	147	395	.360	207	457	.360
33	035	277	.360	091	339	.360	149	397	.360	209	459	.360
39	037	279	.480	093	341	.480	151	399	.480	211	461	.480
47	039	281	.650	095	343	.480	153	401	.480	213	463	.480
56	225	283	.360	097	345	.360	155	403	.480	215	465	.360
68	041	285	.480	099	347	.480	157	405	.480	217	467	.480
82	043	287	.480	101	349	.480	159	407	.480	219	469	.480
100	045	289	.650	103	351	.650	161	409	.650	221	471	.650
120	227	291	.480	105	353	.480	163	411	.480	223	473	.480
150	047	293	.650	107	355	.650	165	413	.650	-	-	-
180	049	295	.480	109	357	.480	167	415	.480	-	-	-
220	051	297	.480	111	359	.480	169	417	.480	-	-	-
270	053	299	.650	-	-	.650	171	419	.650	-	-	-

Note: "N" lead configuration is standard.
 "J" or "L" lead configurations can be obtained by adding the letter as a suffix to the dash number.



DSCC Drawing 88011 High Frequency SMPS Ceramic Capacitor - Table II

Cap. µF	50VDC				100VDC				200VDC				500VDC			
	N Lead		CASE CODE	MAX DIM A	N Lead		CASE CODE	MAX DIM A	N Lead		CASE CODE	MAX DIM A	N Lead		CASE CODE	MAX DIM A
	+/- 5%	+/- 10%			+/- 5%	+/- 10%			+/- 5%	+/- 10%			+/- 5%	+/- 10%		
0.01													181	182	5	.120
0.012													183	184	5	.120
0.015													185	186	5	.240
0.018													187	188	5	.240
0.022													189	190	5	.360
0.027													191	192	5	.360
0.033													193	194	5	.480
0.039													195	196	5	.480
0.047													197	198	5	.650
0.056	001	002	5	.120	061	062	5	.240	121	122	5	.120	199	200	4	.360
0.068	003	004	5	.240	063	064	5	.240	123	124	5	.240	201	202	4	.360
0.082	005	006	5	.240	065	066	5	.240	125	126	5	.240	203	204	4	.480
0.1	007	008	5	.240	067	068	5	.240	127	128	5	.240	205	206	4	.480
0.12	009	010	5	.360	069	070	5	.360	129	130	5	.360	207	208	4	.650
0.15	011	012	5	.360	071	072	5	.360	131	132	5	.360	209	210	3	.240
0.18	013	014	5	.480	073	074	5	.480	133	134	5	.480	211	212	3	.240
0.22	015	016	5	.480	075	076	5	.480	135	136	5	.480	213	214	3	.360
0.27	017	018	5	.650	077	078	5	.650	137	138	5	.650	215	216	3	.360
0.33	019	020	4	.360	079	080	4	.360	139	140	4	.360	217	218	3	.480
0.39	021	022	4	.480	081	082	4	.480	141	142	4	.360	219	220	3	.650
0.47	023	024	4	.480	083	084	4	.480	143	144	4	.480	221	222	1	.360
0.56	025	026	4	.240	085	086	4	.650	145	146	4	.480	223	224	1	.480
0.68	027	028	3	.240	087	088	4	.650	147	148	4	.650	225	226	1	.480
0.82	029	030	3	.240	089	090	3	.240	149	150	3	.240	227	228	1	.650
1	031	032	3	.360	091	092	3	.360	151	152	3	.240	229	230	2	.480
1.2	033	034	3	.360	093	094	3	.360	161	162	3	.650	231	232	2	.650
1.5	035	036	3	.480	095	096	3	.480	163	164	1	.480	233	234	6	.360
1.8	037	038	3	.480	097	098	3	.480	165	166	1	.480	235	236	6	.480
2.2	039	040	3	.650	099	100	3	.650	167	168	1	.650	237	238	6	.650
2.7	041	042	1	.360	101	102	1	.480	169	170	2	.480				
3.3	043	044	1	.480	103	104	1	.480	171	172	2	.650				
3.9	045	046	1	.480	105	106	1	.650	173	174	6	.360				
4.7	047	048	1	.650	107	108	2	.480	175	176	6	.360				
5.6	049	050	2	.650	109	110	2	.650	177	178	6	.480				
6.8	051	052	6	.360	111	112	6	.360	179	180	6	.650				
8.2	053	054	6	.360	113	114	6	.650								
10	055	056	6	.480	115	116	6	.480								
12	057	058	6	.480	117	118	6	.650								
15	059	060	6	.650	119	120	6	.650								

Note: "N" lead configuration is standard.
"J" or "L" lead configurations can be obtained by adding the letter as a suffix to the dash number.

General Soldering Guidelines

The SMPS series capacitors are generally quite large relative to other types of MLC Capacitors. Because of the size, precautions must be taken before introducing the SMPS capacitors to any soldering operation in order to prevent thermal shock. Preheating the SMPS prior to soldering is essential. The heating rate of the SMPS ceramic body during the preheat must not exceed 2°C/per second. The maximum preheat temperature must be below, but within 50°C of the soldering temperature (solder bath, soldering iron tip, etc.) and the SMPS temperature should be stable at the maximum preheat temperature prior to soldering. Assembly of the SMPS is done with Sn10/Pb88/Ag2/solidus 268°C, liquidus 299°C.

Reliability Program

Product reliability is a high priority at UTC. We design our products with robust construction. As a result many of our products go into systems, that are “mission critical”, which may be non-retrievable, and / or manned flight or space flight. A reliable part is one that can withstand installation, testing, and long term field use, without degradation of the part mechanically or electrically, in the specified environment (i.e., shock, vibration, high moisture, extreme thermal change, etc.). To determine if a part will meet these requirements, continuous and periodic testing is conducted at Union Technology.

Manufacturing Yield losses and Infant Mortality Figures

Manufacturing yield losses and infant mortality figures (group A inspection, specifically voltage conditioning) are recorded in a database for the purpose of reviewing historical data, discovering quality trends and implementing preventative action before they become a reliability concern. A product lot can be compared with similar design lots that have been produced in a certain time period, to detect if product quality is changing. Statistical process control, with upper and lower control limits, is in place to alert operators, inspectors and technicians of any potential substandard product performance.

Life Test for Intrinsic Failure Rates and Wear Out Patterns

Intrinsic failure rates and wear out patterns are monitored closely by recording life test information into a database and reviewing the historical data. Long-term reliability is calculated on the number of failures experienced in the total number of test hours. Both increased temperature and voltage, accelerate the condition of life test, and are expressed in the following formulas:

$$\text{Temperature acceleration} = 10((TT - TA)/25)$$

Where:

TT = test temperature in °C

TA = application temperature in °C

$$\text{Voltage acceleration} = (TT - TA)^3$$

Where:

VT = test voltage

VA = application voltage