

UNISONIC TECHNOLOGIES CO., LTD

BTA320A Preliminary TRIAC

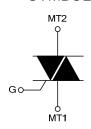
20A TRIACS

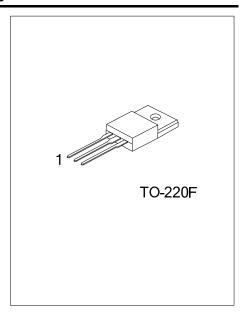
DESCRIPTION

The UTC BTA320A is a 20A triacs which can be operated in 3 quadrants only, it uses UTC's advanced technology to provide customers with high commutation performances, etc.

The UTC BTA320A is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.

SYMBOL

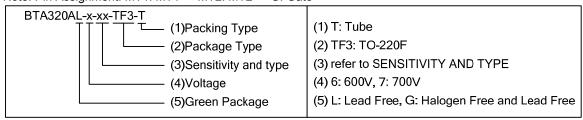




ORDERING INFORMATION

Ordering Number Lead Free Halogen Free		Dookogo	Pin	Assignn	Dooking	
		Package	1	2	3	Packing
BTA320AL-x-xx-TF3-T	BTA320AG-x-xx-TF3-T	TO-220F	MT1	MT2	G	Tube

Note: Pin Assignment: MT1: MT1 MT2: MT2 G: Gate

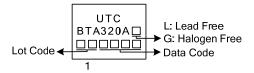


SENSITIVITY AND TYPE

PART NUMBER VOLTAGE 600V 70	VOLT	AGE	SENSITIVITY	TYPF		
	700V	SENSITIVITY	ITPE			
BW		0	50mA	SNUBBERLESS		
CW	0	0	35mA	SNUBBERLESS		

: Available

MARKING



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ABSOLUTE MAXIMUM RATINGS

PARAMETER			SYMBOL	RATINGS	UNIT
RMS On-State Current (Full Sine Wave)		T _C =70°C	I _{T(RMS)}	20	Α
Non Repetitive Surge Peak On-State Current	F=50 Hz	t=10ms	I _{TSM}	210	Α
(Full Cycle, T _J initial=25°C)	F=60 Hz	t=8.3ms		200	Α
I ² t Value for Fusing	t _P =10ms		l ² t	200	A ² s
Critical Rate of Rise of	Repetitive, F=50 Hz		all/alt	50	A/µs
On-State Current I _G =500mA, dI _G /dt =1A/µs	Non Repetitive	T _J =125°C	dl/dt	100	A/μs
Non Repetitive Surge Peak Off-State Voltage	t _P =10ms	TJ=25°C	V_{DSM}/V_{RSM}	V _{DSM} /V _{RSM} +100	٧
Peak Gate Current	t _P =20µs	T _J =125°C	I_{GM}	4	Α
Peak Positive Gate Voltage	t _P =20µs		V_{GM}	16	V
Average Gate Power Dissipation T _J =125°C		T _J =125°C	$P_{G(AV)}$	1	W
Operating Junction Temperature			T_J	-40~+125	°C
Storage Junction Temperature			T_{STG}	-40~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	60	°C/W
Junction to Case (AC)	0	2.0	°C/W
Junction to Case (DC)	θ _{JC}	2.8	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J =25°C unless otherwise specified.)

FOR SNUBBERLESS (3 QUADRANTS)

PARAMETER	SYMBOL	TEST CONDITIONS		BW			CW			UNIT
PARAMETER	STIVIBUL			MIN	TYP	MAX	MIN	TYP	MAX	OINIT
Gate Trigger Current (Note 1)	I_{GT}	V _D =12V, R _L =33Ω	1-11-111	2		50	1		35	mA
Gate Trigger Voltage	V_{GT}	VD 12V, IXE 0032	1-11-111			1.5			1.5	V
Gate Non-Trigger Voltage	V_{GD}	$V_D=V_{DRM}$, $R_L=3.3k\Omega$, $T_J=125^{\circ}C$	1-11-111	0.2			0.2			V
Holding Current (Note 2)	l _Η	I _T =500mA, Gate Open				75			50	mA
Latching Current	l _L	I _G =1.2I _{GT}	1-111 11		50 90					mA mA
	·L		I-II-III		- 00				80	mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V_D =67% V_{DRM} , Gate Open, T_J =125° C		500	750		250	500		V/µs
Critical Rate of Rise of Off-State Voltage at Commutation (Note 2)	(dV/dt)c	(dl/dt)c=20A/ms, T _J =125°C		18	36		11	22		V/µs

■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS			TYP	MAX	UNIT
Peak On-State Voltage (Note 2)	V_{TM}	I _{TM} =28A, t _p =380μs	T _J =25°C			1.70	V
Repetitive Peak Off-State	I_{DRM}	\/ -\/	T _J =25°C			10	μΑ
Current	I_{RRM}	$V_{DRM}=V_{RRM}$	T _J =125°C			3	mA

Note: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.



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