

# Quick Installation Guide PL-80430/MB-80430

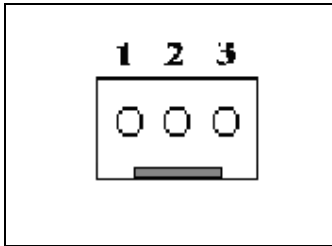
Version A0

## Connectors/Jumper Location & Define

Connector	Define
FAN 1	FAN connector
FAN 2	FAN connector
FAN 3	FAN connector
FAN 4	SMART FAN connector
FAN 5	SMART FAN connector
FAN 6	SMART FAN connector
FAN 7	SMART FAN connector
FAN 8	SMART FAN connector
FAN 9	FAN connector(N/A)
FAN 10	FAN connector(N/A)
CN1	DDR power select pin header(N/A)
CN2	+12V power connector(8Pin)
CN3	+12V power connector(8Pin)
CN4	ATX switch
CN5	PCI-Ex16-RA
CN6	PCI-Ex16-RA
CN7	SATA power connector
CN8	CFast connector (Co-layout CF)
CN9	SATA power connector
CN10	CPLD pin header
CN11	ATX power connector(24Pin)
CN12	SATA connector
CN13	MINI-PCIE(IPMI)

CN14	ATX I2C pin header
CN15	SATA Connector
CN16	RESET pin header
CN17	SATA connector
CN18	ATX GPI(N/A)
CN19	SPI pin header
CN20	SATA connector
CN21	GPO pin header
CN22	GPI pin header
CN23	Port80 pin header
CN24	PS2 KB/MS pin header
CN25	LCM pin header (N/A)
CN26	LCM keypad header
CN27-38	SFP+ connector
CN39	COM2 pin header
CN40	IPMI LAN pin header
CN41	SFP connector
CN48	Bypass LED pin header(N/A)
CN49	Giga LAN RJ45 connector *2port
CN50	Giga LAN RJ45 connector *4port
CN51	COM1 RJ45+USB*2 connector
CN52	CPU XDP pin header
CN53	PCH XDP pin header
SW1	CMOS MODE
SW2	Watchdog function
SW3	LAN1-2 Bypass (RJ45:CN50 Right)
SW4	LAN3-4 Bypass (RJ45:CN50 Left)
LED25	Power/HDD/ Status LED

**FAN 1/2/3/9/10 : FAN Connector**




Pin	Define
1	GND
2	+12V
3	Speed Detect

**FAN 4-8: Smart FAN Connector**

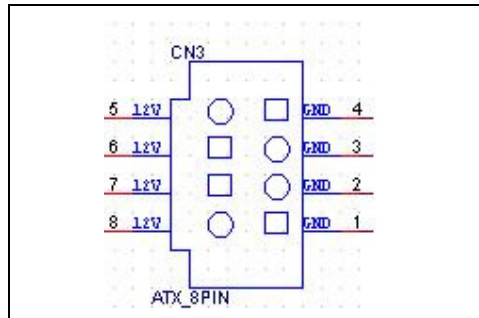
Pin	Define
1	GND
2	N/A
3	+12V
4	N/A
5	Speed Detect
6	N/A
7	Speed Control

**CN1 : DDR power select pin header(N/A)**




Pin	Define
1-2	1.35V
N/A	1.5V

**CN2/3 : +12V power connector (8Pin)**



Pin	Define	Pin	Define
1	Ground	5	+12V
2	Ground	6	+12V
3	Ground	7	+12V
4	Ground	8	+12V

**CN4 : ATX switch**



Pin	Define
1	GND
2	<b>SIGNAL</b>

**CN5/6 : PCI-Ex16-RA**

Standard PCI-E x16 pin define

**CN7/9 : SATA power connector**

Pin	Define
1	+12V
2	GND
3	GND
4	+5V

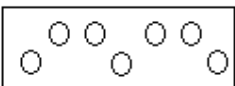
**CN8 : CFAST Connector (Co-layout CF)**

Pin	Define
S1	GND
S2	TXP
S3	TXN
S4	GND
S5	RXN
S6	RXP
S7	GND
PC1	N/A
PC2	GND
PC3	N/A
PC4	N/A
PC5	N/A
PC6	N/A
PC7	GND
PC8	N/A
PC9	N/A
PC10	N/A
PC11	N/A
PC12	N/A
PC13	+3.3V
PC14	+3.3V
PC15	GND
PC16	GND
PC17	N/A

Pin	Define	Pin	Define
13	+3.3V	1	+3.3V
14	-12V	2	+3.3V
15	GND	3	GND
16	PS_ON*	4	+5V
17	GND	5	GND
18	GND	6	+5V
19	GND	7	GND
20	RSVD	8	POWER GOOD
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	GND	12	+3.3V

**CN12,15,17,20 : SATA Connector**

pin	Define
1	Ground
2	TXP
3	TXN
4	Ground
5	RXN
6	RXP
7	Ground



**CN10 CPLD pin header**

Pin	Define	Pin	Define
1	TCK	2	GND
3	TDO	4	+3.3VAUX
5	TMS	6	N/A
7	N/A	8	N/A
9	TDI	10	GND

**CN13 : MINI-PCIE (IPMI CARD)**

Pin	Define	Pin	Define
1	WAKE#	2	+3.3VAUX
3	USBOC#	4	GND
5	IRQ_SERIAL	6	+5V
7	N/A	8	LFRAME#
9	GND	10	LAD0
11	CLK_100MN	12	LAD1
13	CLK_100MP	14	LAD2
15	GND	16	LAD3


**CN11 : ATX Power Connector**

17	USF_2N	18	GND
19	USB_2P	20	RESERVED
21	GND	22	RESET#
23	PCIE_RXN	24	+3.3VAUX
25	PCIE-RXP	26	GND
27	GND	28	+5V
29	GND	30	SMBCLK
31	PCIE-TXN	32	SMBDATA
33	PCIE-TXP	34	GND
35	GND	36	USB_3N
37	GND	38	USB_3P
39	+5VAUX	40	GND
41	+5VAUX	42	CLK33M
43	GND	44	PCH_GP1
45	ATX_SW#	46	PCH_PWROK
47	RST_SW#	48	+5V
49	PCH_GP6	50	GND
51	PCH_GP7	52	+3.3VAUX


**CN14 : ATX I2C Pin Header**

Pin	Define
1	SMBCLK
2	SMBDATA
3	PCH_GP1
4	N/A
5	N/A

**CN16 : Reset Pin Header**

	
Pin	Define
1	Ground
2	Reset #

**CN18 : ATX GPI (N/A)**

	
Pin	Define
1	PCH_GP1_PU
2	Ground

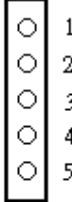
**CN19 : SPI Pin Header**

Pin	Define	Pin	Define
1	+3.3V	2	Ground
3	CS#	4	SCLK
5	MISO	6	MOSI
7	N/A	8	IO

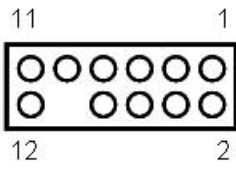
**CN21 : GPO Pin Header**

Pin	Define	Pin	Define
1	GPIO34#	2	GPIO34+
3	GPIO35#	4	GPIO35+
5	GPIO36#	6	GPIO36+
7	GPIO37#	8	GPIO37+
9	GND	10	+3.3V

**CN22 : GPI Pin Header**

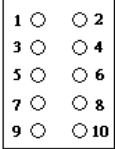
	
Pin	Define
1	GPI20
2	GPI21
3	GPI22
4	GPI23
5	GND

**CN23 : LPC Pin Header**



Pin	Define	Pin	Define
1	+3.3V	2	AD 0
3	AD 1	4	AD 2
5	AD 3	6	Frame#
7	PCIRST#	8	+5V
9	CLOCK	10	N/A
11	Ground	12	Ground

**CN24 : PS2 KB/MS Pin Header**




Pin	Define	Pin	Define
1	KCLK	2	MCLK
3	KDAT	4	MDAT
5	N/A	6	N/A
7	PS2_GND	8	PS2_GND
9	PS2_VCC	10	PS2_VCC

**CN25 : LCM Pin Header(N/A)**

Pin	Define	Pin	Define
1	+5V	2	GND
3	AFD#	4	N/A
5	INIT#	6	SLIN#
7	PD1	8	PD0

9	PD3	10	PD2
11	PD5	12	PD4
13	PD7	14	PD6
15	BLN	16	BLP

**CN26 : LCM Keypad Header**



Pin	Define
1	ACK#
2	BUSY
3	PE
4	SLCT
5	GND

**CN27-38 : SFP+ Connector**

Pin	Define	Pin	Define
1	GND	20	<b>GND</b>
2	TX fault	19	TD-
3	TX disable	18	TD+
4	SDA	17	<b>GND</b>
5	SCL	16	VccT
6	MOD0	15	VccR
7	Rate Select	14	<b>GND</b>
8	LOS	13	RD+
9	Veer1	12	RD-
10	GND	11	<b>GND</b>

**CN39 : COM2 Pin Header**

<pre> 1 0 0 6 2 0 0 7 3 0 0 8 4 0 0 9 5 0 0 10 </pre>			
Pin	Define	Pin	Define
1	DCD#	6	DSR#
2	RXD#	7	RTS#
3	TXD#	8	CTS#
4	DTR#	9	RI#2
5	Ground	10	N/A

**CN40 : IPMI LAN pin header**

Pin	Define	Pin	Define
1	LAN1_OP	7	LINK1000#
2	LAN1_ON	8	LINK100#
3	LAN1_1P	9	ACT#
4	LAN1_1N	10	LAN1_2P
5	LAN1_3P	11	LAN1_2N
6	LAN1_3N	12	N/A

**CN41 : SFP Connector\*2**

Pin	Define	Pin	Define
T1	GND	L1	GND
T2	TX fault	L2	TX fault
T3	TX disable	L3	TX disable
T4	MOD2	L4	MOD2
T5	MOD1	L5	MOD1
T6	MOD0	L6	MOD0
T7	Rate Select	L7	Rate Select
T8	RX_LOS	L8	RX_LOS
T9	GND	L9	GND
T10	GND	L10	GND
T11	GND	L11	GND
T12	RX -	L12	RX -
T13	RX+	L13	RX+
T14	GND	L14	GND

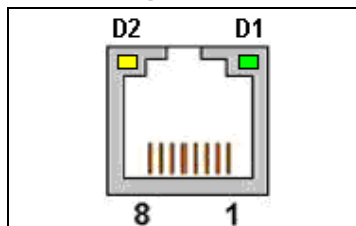
T15	+3.3V	L15	+3.3V
T16	+3.3V	L16	+3.3V
T17	GND	L17	GND
T18	TX+	L18	TX+
T19	TX-	L19	TX-
T20	GND	L20	TX_GND2

**CN48 : Bypass LED Pin Header (N/A)**

Pin	Define
1	BYPS-LED1#
2	+5V (Pull hi 220 Ohm)
3	BYPS-LED2#
4	+5V (Pull hi 220 Ohm)

**CN49 : Giga LAN RJ45 connector \*2port**

**CN50 : Giga LAN RJ45 connector \*4port**



Pin	Define
1	MDI0+
2	MDI0-
3	MDI1+
4	MDI2+
5	MDI2-
6	MDI1-
7	MDI3+
8	MDI3-

**LED:**

<b>D2 : Link/Activity LED</b>	
Link	Yellow
Activity	Blinking
<b>D1 : Bi-Color Speed LED</b>	
10 Mbps	Off
100 Mbps	Green

1000Mbps	Yellow
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**CN51 : COM1 RJ45+USB\*2 Connector**

RJ45	
Pin	Define(ODM)
1	TXD#2
2	RXD#2
3	TXD#1
4	GND
5	GND
6	RXD#1
7	N/A
8	N/A
USB*2	
Pin	Define
13	+5V_USB
14	USBDT0-
15	USBDT0+
16	Ground
17	+5V_USB
18	USBDT1-
19	USBDT1+
20	Ground

**SW1: CMOS MODE**

Pin	Setting
B-A	Normal(Default)
B-C	Clear CMOS

**SW2: Watchdog Function**

Pin	Setting
B-A	LAN Bypass
B-C	Reset (Default)

**SW3/SW4 : LAN1-2 or LAN3-4 Bypass**

Pin	Setting
B-A	Bypass Always Disable
B-C	Normal (Default)

**LED25:**

Pin	Define	Pin	Define
2	GND	1	Power_LED+
4	HDD LED-	3	HDD LED+
6	PCH_GPIO32	5	PCH_GPIO33

**VGA Kit Installation:**

