

3SRT6674REG4G
240-Pin PC2-5300 Registered ECC DIMM
4GB 512M x 72-bit CL5

DESCRIPTION:

This data sheet describes 512Mx72-bit (4GB) DDR2-667 registered SDRAM (Synchronous DRAM) memory module. It contains 36 256Mx4-bit DDR2 SDRAM devices in 60 ball FBGA packages. This module uses gold plated contact fingers and requires power supply of 1.8V. The electrical and mechanical specifications are shown below:

PERFORMANCE:

- Clock Cycle Time (tCK) 3ns (min) / 8ns (max)
- Row Cycle Time (tRC) 60ns (min.)
- Row Active Time (tRAS) 45ns (min.) / 70,000ns (max.)
- Active to Read/Write delay (tRCD) 15ns
- Precharge command period (tRP) 15ns
- Operating Temperature 0°C to +70°C

LAYOUT:

