

3SRT6679REG4GK

240-Pin PC2-5300 Registered ECC DIMM 2x2GB 256M x 72-bit CL5

DESCRIPTION:

This data sheet describes two 256Mx72-bit (2GB) DDR2-667 registered SDRAM (Synchronous DRAM) memory modules as a kit. It contains 18 128Mx8-bit DDR2 SDRAM devices in 60 ball FBGA packages. This module uses gold plated contact fingers and requires power supply of 1.8V. The electrical and mechanical specifications are shown below:

PERFORMANCE:

- | | |
|-------------------------------------|-------------------------------|
| ➤ Clock Cycle Time (tCK) | 3ns (min) / 8ns (max) |
| ➤ Row Cycle Time (tRC) | 60ns (min.) |
| ➤ Row Active Time (tRAS) | 45ns (min.) / 70,000ns (max.) |
| ➤ Active to Read/Write delay (tRCD) | 15ns |
| ➤ Precharge command period (tRP) | 15ns |
| ➤ Operating Temperature | 0°C to +70°C |

LAYOUT:

