



# **SPECIFICATION**

(Reference sheet)

· Supplier : Samsung electro-mechanics · Samsung P/N : CL03A105KP3NSNC

Product : Multi-layer Ceramic Capacitor

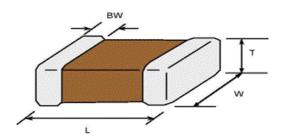
Description : CAP, 1/4F, 10V, ±10%, X5R, 0201

### A. Samsung Part Number

<u>CL</u> <u>03</u> <u>A</u> <u>105</u> <u>K</u> <u>P</u> <u>3</u> <u>N</u> <u>S</u> <u>N</u> <u>C</u> ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪

① Series	Samsung Multi-layer Ceramic Capacitor				
② Size	0201 (inch code)	L: 0.60 ± 0.05 mm	W : $0.30 \pm 0.05 \text{ mm}$		
③ Dielectric	X5R	8 Inner electrode	Ni		
④ Capacitance	1 <i>µ</i> F	Termination	Cu		
⑤ Capacitance	±10 %	Plating	Sn 100% (Pb Free)		
tolerance		9 Product	Size control code		
6 Rated Voltage	10 V	Special	Reserved for future use		
7 Thickness	$0.30 \pm 0.05 \text{ mm}$	① Packaging	Cardboard Type, 7" reel		

#### **B. Structure & Dimension**



Samsung P/N	Dimension(mm)				
Samsung F/N	L	W	Т	BW	
CL03A105KP3NSNC	0.60 ± 0.05	0.30 ± 0.05	0.30 ± 0.05	0.15 ± 0.05	

#### C. Samsung Reliablility Test and Judgement Condition

	Judgement	Test condition	
Capacitance	Within specified tolerance	1kHz ±10% / 0.5±0.1Vrms	
Tan δ (DF)	0.125 max.	*A capacitor prior to measuring the capacitance is heat treated at 150 ℃+0/-10 ℃ for 1hour and maintained in ambient air for 24±2 hours.	
Insulation	10,000Mohm or 10Mohm× <i>μ</i> F	Rated Voltage 60~120 sec	
Resistance	Whichever is smaller		
Appearance	No abnormal exterior appearance	Microscope (×10)	
Withstanding	No dielectric breakdown or	250% of the rated voltage	
Voltage	mechanical breakdown		
Temperature	X5R		
Characteristics	(From -55℃ to 85℃, Capacitance change should be within ±15%)		
Adhesive Strength	No peeling shall be occur on the	200g·f, for 10±1 sec.	
of Termination	terminal electrode		
Bending Strength	Capacitance change : within ±12.5%	Bending to the limit (1 <sup>mm</sup> )	
		with 1.0mm/sec.	
Solderability	More than 75% of terminal surface	SnAg3.0Cu0.5 solder	
	is to be soldered newly	245±5°C, 3±0.3sec.	
		(preheating : 80~120°C for 10~30sec.)	
Resistance to	Capacitance change : within ±7.5%	Solder pot : 270±5°C, 10±1sec.	
Soldering Heat	Tan δ, IR : initial spec.		
Vibration Test	Capacitance change : within ± 5%	Amplitude : 1.5mm	
	Tan δ, IR : initial spec.	From 10Hz to 55Hz (return : 1min.)	
		2hours × 3 direction (x, y, z)	
Moisture	Capacitance change : within ±12.5%	With rated voltage	
Resistance	Tan δ : 0.25 max	40±2°C, 90∼95%RH, 500+12/-0hrs	
	IR : 500Mohm or 1Mohm × $\mu$ F		
	Whichever is smaller		
High Temperature	Capacitance change : within ±12.5%	With 100% of the rated voltage	
Resistance	Tan δ : 0.25 max	Max. operating temperature	
	IR : 1,000Mohm or 2Mohm × $\mu$ F	1,000+48/-0hrs	
	Whichever is smaller		
Temperature	Capacitance change : within ±15%	1 cycle condition	
Cycling	Tan δ, IR : initial spec.	Min. operating temperature → 25°C	
		→ Max. operating temperature → 25°C	
		5 cycle test	

<sup>\*</sup> The reliability test condition can be replaced by the corresponding accelerated test condition.

#### D. Recommended Soldering method:

Reflow ( Reflow Peak Temperature : 260±5℃, 30sec. )



Product specifications included in the specifications are effective as of March 1, 2013.

Please be advised that they are standard product specifications for reference only.

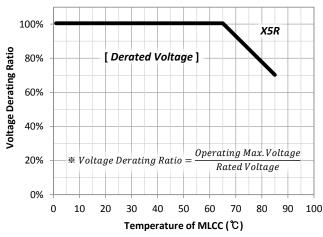
We may change, modify or discontinue the product specifications without notice at any time.

So, you need to approve the product specifications before placing an order.

Should you have any question regarding the product specifications,

please contact our sales personnel or application engineers.

## Derating



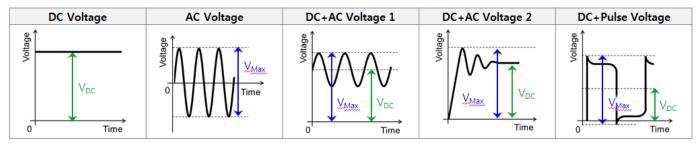
This product ,which guarantees High Temperature Reliability Test with 100% of rated voltage at the maximum temperature, is recommended to be used in the circuit with derated voltage compared to the rated voltage of the capacitor for long lifetime.

Max.  $voltage(V_{Max})$  and DC  $voltage(V_{DC})$  applied to this product shown in the table below are recommended to be used under the following conditions for long lifetime, respectively.

[Recommendations for long lifetime]

- $\cdot V_{Max} \le$  (Derated Voltage on the left graph)
- $\cdot V_{DC} \le 70\% \times (Derated Voltage on the left graph)$

#### [Types of voltage applied to the capacitor]



# Disclaimer & Limitation of Use and Application

The products listed in this Specification sheet are **NOT** designed and manufactured for any use and applications set forth below.

Please note that any misuse of the products deviating from products specifications or information provided in this Spec sheet may cause serious property damages or personal injury.

We will **NOT** be liable for any damages resulting from any misuse of the products, specifically including using the products for high reliability applications as listed below.

If you have any questions regarding this 'Limitation of Use and Application', you should first contact our sales personnel or application engineers.

- Aerospace/Aviation equipment
- 2 Automotive or Transportation equipment (vehicles, trains, ships, etc)
- 3 Medical equipment
- 4 Military equipment
- 5 Disaster prevention/crime prevention equipment
- (6) Any other applications with the same as or similar complexity or reliability to the applications set forth above.

 $<sup>\</sup>mbox{\ensuremath{^{\ast}}}$  Temperature of MLCC : Surface temperature of MLCC in the circuit.