

## FEATURES

6 Decade Operation — 1nA to 1mA  
 ½% Log Conformity — 10nA to 100µA  
 Symmetrical FET Inputs  
 Voltage or Current Operation  
 Temperature Compensated  
 Antilog Operation

## APPLICATIONS

Absorbance Measurements  
 Log/Antilog Ratios of Voltages or Currents  
 Data Compression  
 Transducer Linearization

## GENERAL DESCRIPTION

Model 757 is a complete, temperature compensated, dc-coupled log ratio module. It is comprised of two input channels for processing signals spanning up to 6 decades in dynamic range (1nA to 1mA). By virtue of its symmetrical FET input stages, the 757 can accommodate this 6 decade signal range at either channel. Log conformity is maintained to within ½% over 4 decades of input (10nA to 100µA) and to within 1% over the full input range. Unlike other log ratio designs, model 757 does not restrict the relative magnitude of the two signal inputs to achieve rated performance. Either input can be operated within the specified range regardless of the signal level at the other channel.

The model 757 log-ratio module design makes available both input amplifier summing junctions. As a result, it can directly interface with photo diodes operating in the short-circuit current mode without the need of additional input circuitry.

The excellent performance of model 757 can be further improved by means of external scale factor and output offset adjustments. A significant feature of model 757 not found on competing devices is that, when the offset adjustment is used to establish a fixed bias at the output, the output offset level does not vary as a function of input signal magnitude. On other designs, the sensitivity of output offset to input levels results in output effects resembling log conformity errors.

Model 757 can operate with either current or voltage inputs. Its excellent performance makes it ideally suited for log ratio applications such as blood analysis, chromatography, chemical analysis of liquids and absorbance measurements.

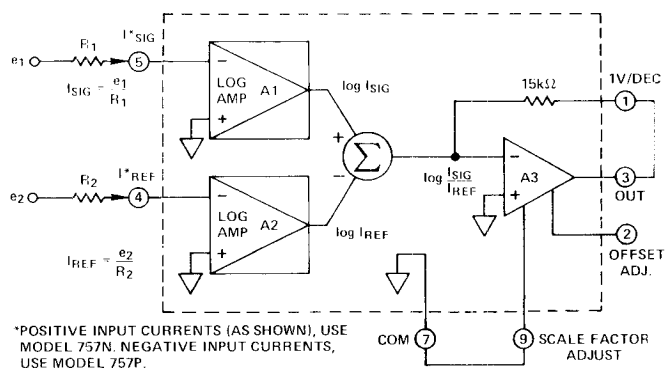


Figure 1. Functional Block Diagram of Model 757

## CURRENT LOG RATIO

Current log ratio is accomplished by model 757 when two currents,  $I_{SIG}$  and  $I_{REF}$ , are applied directly to the input terminals (see Figure 1). The two log amps process these signals providing voltages which are proportional to the log of their respective inputs. These voltages are then subtracted and applied to an output amplifier. The scale factor, when connected as shown, is 1V/dec. However, higher scale factors may be achieved by connecting external scale factor adjusting resistors. (See section on optional adjustments and trims.)

## VOLTAGE LOG RATIO

The principle of operation for voltage log ratio is identical to that of current log ratio after the voltage signal has been converted to a current. To accomplish this conversion, an external

# SPECIFICATIONS

(typical @ +25°C and  $V_S = \pm 15V$  dc unless otherwise noted)

MODEL	757N/P	
TRANSFER FUNCTION <sup>1</sup>		
Current Mode	$e_o = -K \log_{10} \frac{I_{SIG}}{I_{REF}}$	
Voltage Mode	$e_o = -K \log_{10} \left[ \frac{e_1}{e_2} \times \frac{R_2}{R_1} \right]$	
ACCURACY		
Log Conformity <sup>2</sup>		
$I_{SIG}, I_{REF} = 100\mu A$	$\pm 0.5\%$ , max	
$I_{SIG}, I_{REF} = 1nA$ to $1mA$	$\pm 1\%$ , max	
Scale Factor (1V/Dec)	$(+0, -2\%) \pi$ ax	
vs. Temperature (0 to $+70^{\circ}C$ )	$\pm 0.04\%/^{\circ}C$ max	
INPUT SPECIFICATIONS Both Input Channels		
Current		
Signal Range, Rated Performance		
Model 757N	$+1nA$ to $+1mA$	
Model 757P	$-1nA$ to $-1mA$	
Max Safe	$\pm 10mA$	
Bias Current, @ $+25^{\circ}C$	$-10pA$	
vs. Temperature (0 to $+70^{\circ}C$ )	$\times 2/+10^{\circ}C$	
Offset Voltage, @ $+25^{\circ}C$	$\pm 1mV$ max	
vs. Temperature (0 to $+70^{\circ}C$ )		
$I_{SIG}$ Channel	$\pm 25\mu V/^{\circ}C$ max	
$I_{REF}$ Channel	$\pm 85\mu V/^{\circ}C$ max	
vs. Supply Voltage	$\pm 5\mu V/\%$	
FREQUENCY RESPONSE, Sinewave		
Small Signal Response (-3dB)		
Signal Channel		
$I_{SIG} = 1nA$	300Hz	
$I_{SIG} = 1\mu A$	25kHz	
$I_{SIG} = 100\mu A$	50kHz	
Reference Channel		
$I_{REF} = 1nA$	3kHz	
$I_{REF} = 1\mu A$	25kHz	
$I_{REF} = 100\mu A$	50kHz	
RISE TIME		
Increasing Input Current	Signal Channel ( $I_{REF} = 10\mu A$ )	Reference Channel ( $I_{SIG} = 10\mu A$ )
$1nA$ to $10nA$	250 $\mu s$	80 $\mu s$
$10nA$ to $100nA$	50 $\mu s$	40 $\mu s$
$100nA$ to $1\mu A$	30 $\mu s$	30 $\mu s$
$1\mu A$ to $100\mu A$	25 $\mu s$	25 $\mu s$
Decreasing Input Current		
$100\mu A$ to $1\mu A$	25 $\mu s$	25 $\mu s$
$1\mu A$ to $100nA$	30 $\mu s$	30 $\mu s$
$100nA$ to $10nA$	100 $\mu s$	40 $\mu s$
$10nA$ to $1nA$	600 $\mu s$	70 $\mu s$
INPUT NOISE		
Voltage (10Hz to 10kHz)	3 $\mu V$ rms	
Current (10Hz to 10kHz)	0.1pA rms	
OUTPUT SPECIFICATIONS		
Rated Output		
Voltage	$\pm 10V$ min	
Current		
Log Mode	$\pm 5mA$ , min	
Antilog Mode	$\pm 4mA$ , min	
Resistance	0.1 $\Omega$	
Offset Voltage <sup>3</sup> (K = 1V/Decade)	$\pm 10mV$ max	
vs. Temperature (0 to $+70^{\circ}C$ )	$\pm 0.3mV/^{\circ}C$	
vs. Supply	$\pm 5\mu V/V$	
POWER SUPPLY <sup>4</sup>		
Rated Performance	$\pm 15V$ dc	
Operating	$\pm (12$ to $18)V$ dc	
Current, Quiescent	$\pm 8mA$	
TEMPERATURE RANGE		
Rated Performance	0 to $+70^{\circ}C$	
Operating	$-25^{\circ}C$ to $+85^{\circ}C$	
Storage	$-55^{\circ}C$ to $+125^{\circ}C$	
MECHANICAL		
Case Size	1.5" x 1.5" x 0.4"	
Mating Socket	AC1048	
Weight	21 grams	

<sup>1</sup> For model 757N, K = +1V/Decade and input currents must be positive. For model 757P, K = -1V/Decade and input currents must be negative. (Input currents are defined as positive when flowing into the input terminals, 4 and 5. Refer to TRANSFER CURVES.)

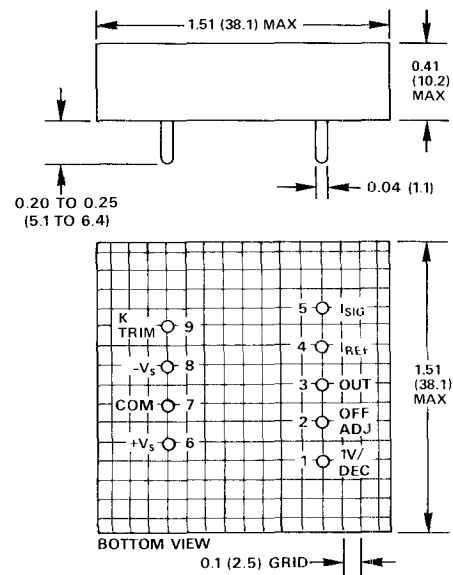
<sup>2</sup> The log conformity error is referred to input (RTI). 1% error RTI is equivalent to 4.3mV of error at the output for K = 1V/Dec.

<sup>3</sup> Externally adjustable to zero.

<sup>4</sup> Recommended power supply: Analog Devices model 904,  $\pm 15V$  @ 50mA  
Specifications subject to change without notice.

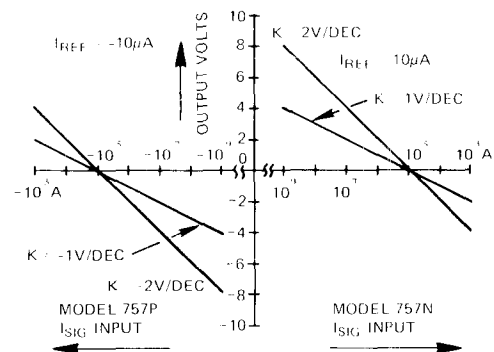
## OUTLINE DIMENSIONS

Dimensions shown in inches and (mm).



Mating Socket AC1048

## TRANSFER CURVES



Log mode output voltage vs. input current for  $I_{REF} = 10\mu A$ . For voltage input calculate  $I_{SIG}$  as  $e_1/R_1$  (see Figure 1).

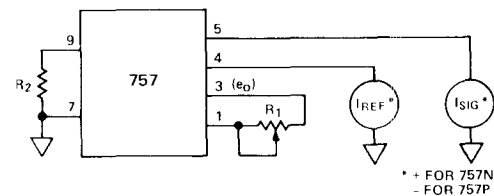


Figure 2. Scale Factor Adjustment

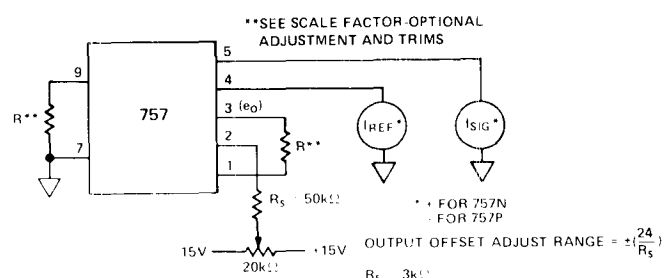


Figure 3. Output Voltage Offset Adjustments

resistor is attached from the voltage signal to the appropriate input current terminal of the 757. Input currents are then determined by:

$$I_{SIG} = \frac{e_1}{R_1}, I_{REF} = \frac{e_2}{R_2}$$

## OPTIONAL ADJUSTMENTS AND TRIMS

**Scale Factor** — A one volt per decade scale factor is available when pin 1 is tied to 3 and pin 7 is connected to 9. Higher scale factors are possible by using a potentiometer,  $R_1$ , between pins 1 and 3 and a resistor,  $R_2$ , between pins 7 to 9 as shown in Figure 2. The value of the required resistor is  $(15k\Omega) (K-1)$  where  $K$  is the desired scale factor. The approximate potentiometer value is also  $(15k\Omega) (K-1)$ . The scale factor adjustment procedure is as follows:

1. Connect the appropriate value of resistor between pins 7 and 9.
2. Set  $I_{REF} = 1\mu A$ ,  $I_{SIG} = 10\mu A$ . Measure  $e_o$ .
3. Set  $I_{REF} = 1\mu A$ ,  $I_{SIG} = 100\mu A$ . Adjust  $R_1$  until the difference in  $e_o$  corresponding to steps 2 and 3 is  $K$  volts.
4. Repeat steps 2 and 3 until the change in  $e_o = K$  volts.

**Output Voltage Offset** — Output voltage offset must be adjusted after the desired scale factor is established as indicated above. To adjust the offset, inject equal dc input currents into the reference and signal channels. The value of the input currents should approximate the average input current levels expected to be encountered in normal operation. Adjust the potentiometer shown in Figure 3 until the output voltage is zero.

## INTERCHANGEABILITY WITH MODEL 756

Model 757 is a functional, pin compatible replacement for model 756 in applications that do not require external trims. The user need only short pin 7 to pin 9 in order to establish a scale factor of 1V/decade. In applications requiring external adjustments, the scale factor and offset trims should be configured to the model 757 requirements as outlined above.

## LOG CONFORMITY

Log conformity in logarithmic devices is a specification similar to linearity in linear devices. Log conformity error is the difference between the theoretical value of the log of a ratio and the actual value that appears at the output of the log-ratio module after scale factor errors have been eliminated. Measurement of this error is made after initially zeroing the module at unity-ratio and adjusting the desired scale factor.

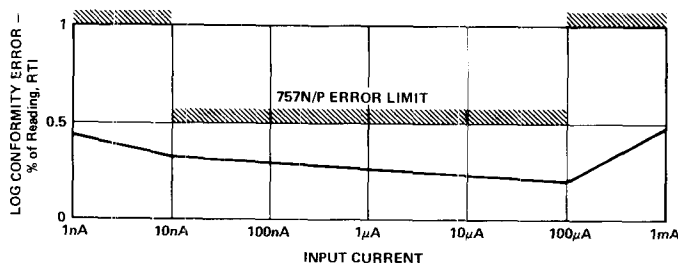


Figure 4. Log Conformity Error for Model 757. Curve is for Either Input Channel with Current Held Constant at  $10\mu A$  On Other Channel

Figure 4 shows the log conformity performance of model 757 over a 6 decade input range. Log conformity for each channel does not vary noticeably as the current is varied in the other channel.

## FREQUENCY CHARACTERISTICS

Figure 5 shows a plot of small signal response ( $-3dB$ ) as a function of input signal current. The graph demonstrates the frequency response performance for each input channel over the range of  $1nA$  to  $1mA$ , independent of current on the other channel.

As shown in the graph, the reference channel is faster than the signal channel at low input levels. If an application requires higher speed in the input signal channel than in the reference channel, then the channels can be interchanged with a resulting polarity reversal of the output signal

$$\left(\log \frac{I_{SIG}}{I_{REF}} = \log I_{SIG} - \log I_{REF} = -\log \frac{I_{REF}}{I_{SIG}}\right).$$

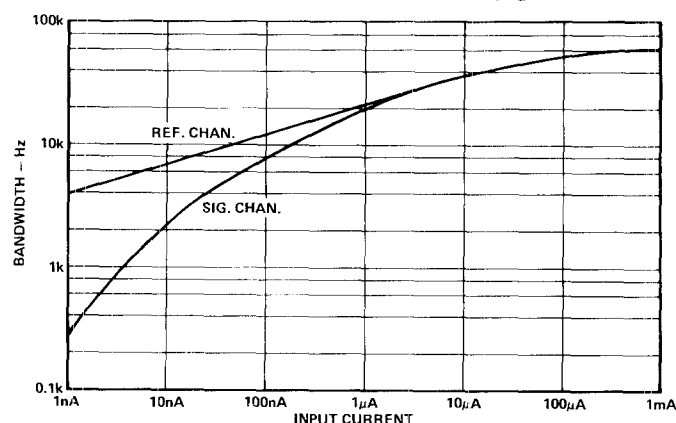


Figure 5. Small Signal Bandwidth ( $-3dB$ ) vs. Input Signal Level

## OUTPUT OFFSET VOLTAGE DRIFT

The curves of Figure 6 illustrate the output offset voltage changes over the operating temperature range of  $0$  to  $+70^\circ C$ , at  $K = 1V/decade$ . Since output offset voltage changes are minimum at  $+25^\circ C$ , best accuracy operation is obtained over the range of  $+25^\circ C \pm 20^\circ C$ .

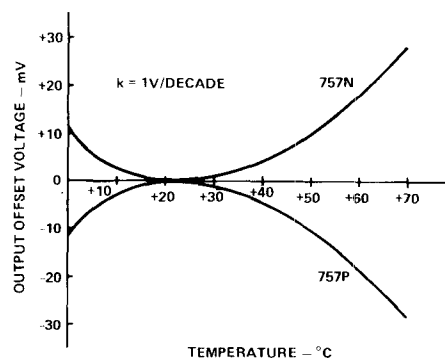


Figure 6. Output Offset Voltage vs. Temperature

## ANTILOG OPERATION

The antilogarithm is the inverse of the logarithm. It is by definition the exponential in which the logarithmic base ( $B$ ) is raised to a power ( $Y$ ). That is,

$$X = \log^{-1}_B (Y) = B^Y$$

The model 757 log ratio module with the transfer function

$$E_O = -K \log_{10} \frac{E_{SIG}}{E_{REF}} \quad (R_1 = R_2)$$

can be connected as shown in Figure 7 to compute

$$E_O = E_{REF} \times 10^{(E_{SIG}/-K)}$$

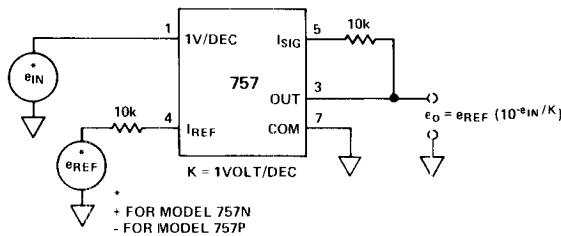


Figure 7. Model 757 Antilog Connection

Thus, when  $K = 1$ , the log ratio operation provides an output of 1 volt per decade of input ratio, while the antilog mode yields an output that changes 1 decade per volt of input.

In antilog operation,  $E_{REF}$  is the normalized value of the exponential and each volt of increase of the input multiplies  $E_{REF}$  by an additional factor of 10.

Exponential devices can be used when:

1. Compound multiplications involving roots and powers are performed.
2. Devices with logarithmic responses must be linearized.
3. Function fitting and generation is required to obtain relationships or generate curves having voltage-programmable rates of growth or decay. (e.g. if  $E_{SIG}$  is a ramp,  $E_O$  will be a widely varying exponential function of time that can be further modified by varying  $E_{REF}$  and  $K$ ).

## APPLICATIONS

**Data Compression** — Processing signals with wide dynamic range is a common problem in instrumentation and data transmission. For example, digitizing an analog signal with a range of 10nA to 100μA with 1% accuracy requires a 20 bit A/D converter. (Required resolution =  $1/100 \times 1/10,000 = 1/10^6 \cong 1/2^{20}$ ).

By using the 757 with  $I_{REF}$  adjusted to 10nA and  $K$  set for 5/4 V/decade, the input data can be compressed into a 5 volt output range. For a 1% resolution of any signal, the allowable output error is  $4.32\text{mV} \times K$ . Log conformity contributes  $2.17\text{mV} \times K$  (0.5%) over this range. The remaining error with  $K = 5/4$  is  $2.69\text{mV}$  and should correspond to less than the LSB of the converter. With a 5 volt output range  $2.69\text{mV}$  corresponds just over the LSB of an 11-bit converter. Thus the 757 module can compress the data for use with a 12 bit A/D (such as Analog Devices ADC-12QZ) to obtain the desired 1% resolution.

**Absorbance Measurements** — Critical properties of materials, which are of particular interest in the fields of chemistry, medicine, spectrometry and pollution control are characterized by absorbance. The relationship between absorbance,  $A$ , and light intensity,  $I$ , is:  $A = \log I_O/I_T$  where  $I_O$  = intensity of incident light, and  $I_T$  = intensity of transmitted light.

Figure 8 shows the 757 log-ratio module used in such a photometer application. Two inputs represent the intensities of light

transmitted through space and through a medium that absorbs light. The absorbance of the medium is given by the formula

$$A = \log \frac{I_{\text{SIGNAL}}}{I_{\text{REFERENCE}}}$$

where  $I_{\text{SIGNAL}}$  and  $I_{\text{REFERENCE}}$  are the currents representing the light intensities.

The transducers used in this application are photodiodes, which provide a short-circuit current proportional to the intensity of applied light. The lowest value of absorbance is determined by the value of  $I_{REF}$ , since when  $I_{SIG} = I_{REF}$ ,  $A = 0$ . The output of the log-ratio module is externally trimmed to 1V/decade and applied to the input of a 3½-digit DPM through the scaling network  $R_1$  and  $R_2$ .

Model 757 was chosen for this design because it makes available both amplifier summing junctions. When the photodiodes are connected to the summing junctions, they are operated in the short-circuit mode, that is, with zero volts across the diodes. Short-circuit loading is necessary, because accuracy of the photodiodes can be degraded several percent when operated with as little as 100mV across the diode junction.

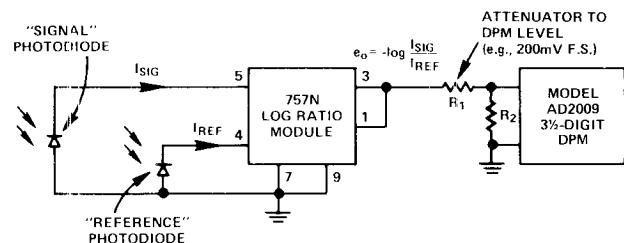


Figure 8. Model 757N Applied to Absorbance Measurements

## INTERCONNECTION GUIDELINES

Model 757 is a complete log ratio module that requires no additional frequency compensation for proper operation.

**Input Capacitance** — Model 757 is able to operate with 1000pF at both input terminals. Therefore, the 757 can be used in applications requiring long cable lengths between the module and the signal transducers.

**Input-to-Output Capacitance** — When using a log ratio module the user should take care in system configurations to avoid excessive stray capacitance between input and output terminals. Such precautions include avoiding running input and output signal lines close together. If long cable runs are required where inputs and output are closely bundled together, it is advisable to enclose the inputs and/or output in separate, grounded electrostatic shields. By observing simple rules of good circuit layout, problems with oscillations that may result from excessive input-to-output capacitance can easily be avoided. Model 757 can accommodate up to 150pF of input-to-output capacitance without oscillation.

**Leakage Resistance** — Since model 757 can operate at extremely low input current levels, precautions must be taken to prevent current leakage into the input terminals. Such leakage can cause errors when small input or reference currents are used. This problem may arise on printed circuit layouts if the inputs are run too close to the power supply busses. Providing an etched guard around the input lines, connected to analog signal ground will also reduce unwanted current leakage.