INTERSIL

ICM7209
CMOS Clock Generator

FEATURES

- **High frequency operation — 10MHz guaranteed**
- **Easy to use oscillator — requires only a quartz crystal and two capacitors**
- **Bipolar, MOS and CMOS compatibility**
- **High output drive capability — 5 x TTL fanout with 10ns rise and fall times**
- **Low power — 50mW at 10MHz**
- **Choice of two output frequencies — osc., and osc. ÷8 frequencies**
- **Disable control for both outputs**
- **Wide industrial temperature range — −20°C to +85°C**
- **All inputs fully protected — circuits may be handled without any special precautions**

GENERAL DESCRIPTION

The Intersil ICM7209 is a versatile CMOS clock generator capable of driving a number of 5 volt systems with a variety of input requirements. When used to drive up to 5 TTL gates, the typical rise and fall times are 10ns.

The ICM7209 consists of an oscillator, a buffered output equal to the oscillator frequency and a second buffered output having an output frequency one-eighth that of the oscillator. The guaranteed maximum oscillator frequency is 10MHz. Connecting the DISABLE terminal to the negative supply forces the ÷8 output into the '0' state and the output 1 into the '1' state.
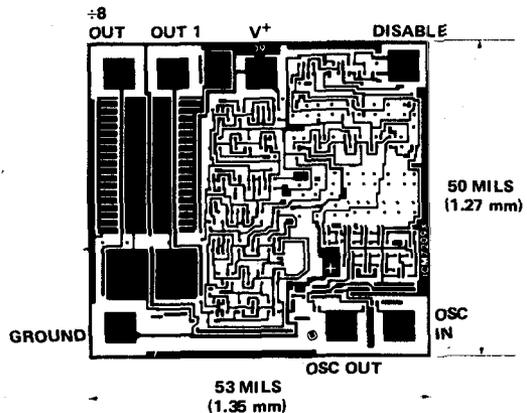
SCHEMATIC DIAGRAM
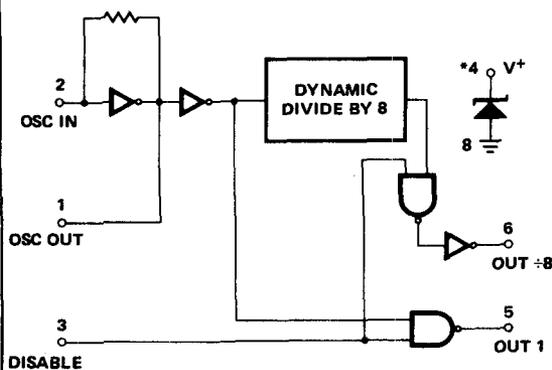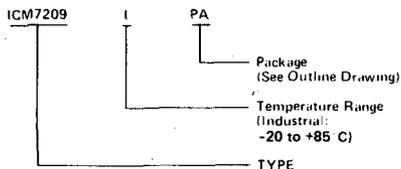

*ZENER VOLTAGE IS TYPICALLY 6.3 VOLTS

CHIP TOPOGRAPHY

50 MILS (1.27 mm)

53 MILS (1.35 mm)

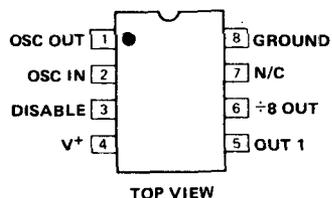PADS 4.2 x 4.2 MILS^2 (.107 mm x .107 mm)

Chip may be die attached using conventional enteric or epoxy procedures. Wire bonding may be either Aluminum ultrasonic or Gold compression.

ORDERING INFORMATION

PIN CONFIGURATION (OUTLINE DRAWING PA)

OSC OUT 1	8 GROUND
OSC IN 2	7 N/C
DISABLE 3	6 ÷8 OUT
V+ 4	5 OUT 1

TOP VIEW

Pin 1 is designated by either a dot or a notch.

ICM7209

ABSOLUTE MAXIMUM RATINGS

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Power Dissipation (25°C) ............................................... 300mW
Supply Voltage ......................................................... 6 V
Output Voltages ................................. Equal to or less than supply
Input Voltages .................................. Equal to or less than supply
Storage Temp. ........................................... −55°C to +125°C
Operating Temp. Range ........:............................. -20°C to +85°C
Lead Temp. (Soldering, 10 seconds) ................................. 300°C
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NOTE: Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL OPERATING CHARACTERISTICS

TEST CONDITIONS: $V+ = 5V \pm 10\%$, test circuit, $f_{osc} = 10MHz$, $T_A = 25°C$ unless otherwise specified.

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP.	MAX	UNITS
Supply Current	I+	Note 1 No Load		11	20	mA
Disable Input Capacitance	C_D				5	pF
Disable Input Leakage	I_{ILK}	Either '1' or '0' state			±10	µA
Output Low State	V_{OL}	Either OUT 1 or OUT ÷8 simulated 5 x TTL loads			0.4	
Output High State	V_{OH}	Either OUT 1 or OUT ÷8 simulated 5 x TTL loads	4.0	4.9		
Output Rise Time (Note 3)	t_r	Either OUT 1 or OUT ÷8 simulated 5 x TTL loads		10	25	
Output Fall Time (Note 3)	t_f	Either OUT 1 or OUT ÷8 simulated 5 x TTL loads		10	25	
Minimum OSC Frequency for ÷8 Output	f_{osc}	Note 2	2			
Output ÷8 duty cycle		Any operating frequency Low state : High state		7:9		

NOTE 1: The power dissipation is a function of the oscillator frequency (1st ORDER EFFECT see curve) but is also effected to a small extent by the oscillator tank components.

NOTE 2: The ÷8 circuitry uses a dynamic scheme. As with any dynamic system, information or data is stored on very small nodal capacitances instead of latches (static systems) and there is a lower cutoff frequency of operation. Dynamic dividers are used in the ICM7209 to significantly improve high frequency performance and to decrease power consumption.

NOTE 3: Rise and fall times are defined between the output levels of 0.5 and 2.4 volts.
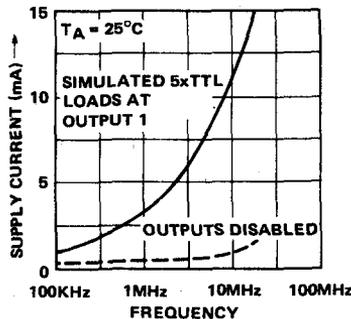
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TEST CIRCUIT

CRYSTAL PARAMETERS:
$C_M = 5mpF$
$R_S = 15$ ohms
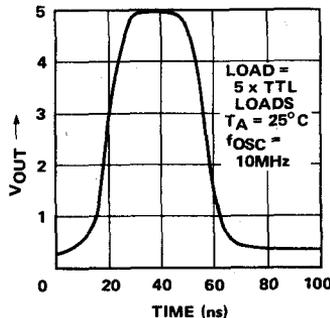$C_O = 3pF$
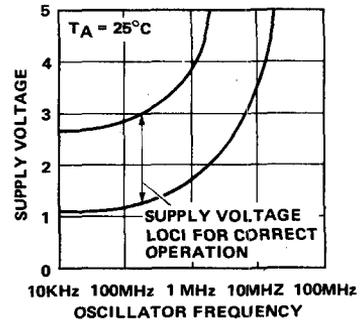$f = 10$ MHz
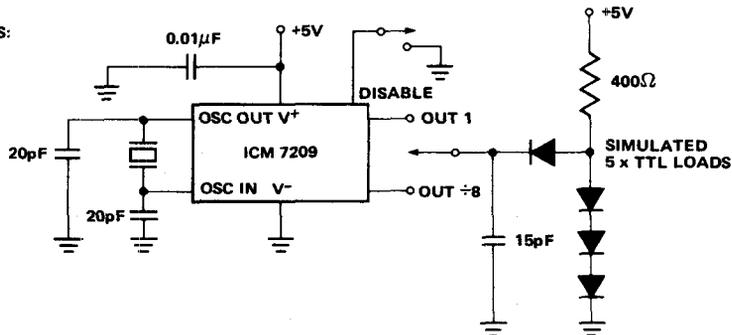
TYPICAL OPERATING CHARACTERISTICS

SUPPLY CURRENT AS A FUNCTION OF OSCILLATOR FREQUENCY

TYPICAL OUT 1 RISE AND FALL TIMES

SUPPLY VOLTAGE RANGE FOR CORRECT OPERATION OF ÷8 COUNTER AS A FUNCTION OF OSCILLATOR FREQUENCY

Rise and fall times of OUT ÷8 are similar to those of OUT 1.

APPLICATION NOTES

OSCILLATOR CONSIDERATIONS

The oscillator consists of a C-MOS inverter with a non-linear resistor connected between the oscillator input and output to provide D.C. biasing. Using commercially obtainable quartz crystals the oscillator will operate from low frequencies (10KHz) to 10MHz.

The oscillator circuit consumes about $500\mu A$ of current using a 10MHz crystal with a 5 volt supply, and is designed to operate with a high impedance tank circuit. It is therefore necessary that the quartz crystal be specified with a load capacitance (C_L) of 10pF instead of the standard 30pF. To maximize the stability of the oscillator as a function of supply voltage and temperature, the motional capacitance of the crystal should be low (5mpF or less). Using a fixed input capacitor of 18pF and a variable capacitor of nominal value of 18pF on the output will result in oscillator stabilities of typically 1ppm per volt change in supply voltage.

THE ÷8 OUTPUT

A dynamic divider is used to divide the oscillator frequency by 8. Dynamic dividers use small nodal capacitances to store voltage levels instead of latches (which are used in static

dividers). The dynamic divider has advantages in high speed operation and low power but suffers from limited low frequency operation. This results in a window of operation for any oscillator frequency (see graph under TYPICAL OPERATING CHARACTERISTICS).

OUTPUT DRIVERS

The output drivers consist of C-MOS inverters having active pullups and pulldowns. Thus the outputs can be used to directly drive TTL gates, other C-MOS gates operating with a 5 volt supply, or TTL compatible MOS gates.

The guaranteed fanout is 5 TTL loads although typical fanout capability is at least 10 TTL loads with slightly increased output rise and fall times.

COMMENTS ON THE DEVICE POWER CONSUMPTION

At low frequencies the principal component of the power consumption is the oscillator. At high oscillator frequencies the major portion of the power is consumed by the output drivers, thus by disabling the outputs (activating the DISABLE INPUT) the device power consumption can be dramatically reduced.