

## TELECOM AND DATA COMMUNICATIONS

### INTEGRATED CIRCUITS

Type	Description	Package
ETC5040FN	PCM filter	PLCC20
ETC5054FN	Mu-law serial output COMBO	PLCC20
ETC5057FN	A-law serial output COMBO	PLCC20
ETC5064FN	Mu-law serial COMBO with power amplifiers	PLCC20
ETC5067FN	A-law serial COMBO with power amplifiers	PLCC20
L3030	SLIC control unit	PLCC44
LS1240	Two tone ringer	SO8
LS1240A	Two tone ringer	SO8
LS1241	Two tone ringer	SO8
LS156	Speech circuit with MF interface (for piezoceramic transducers)	SO20L
LS204CM,M	High performance dual operational amplifier	SO8
LS404CM,M	High performance quad operational amplifier	SO14
LS656	Low drop speech circuit with MF interface (for dynamic transducers)	SO20L
MK5025	X.25 LAPB / ISDN LAPD / HDLC CMOS Hi-speed link level controller with DMA	PLCC52
TEA7531FP	Monitor amplifier with anti-howling (telephone set)	SO16
TEA7532FP	Monitor amplifier with anti-howling (telephone set)	SO16
TEB1033D	High performance dual bipolar operational amplifier	SO8
TEB1033DT	High performance dual bipolar operational amplifier	SO8 tape
TEB4033D	High performance quad bipolar operational amplifier	SO14
TEB4033DT	High performance quad bipolar operational amplifier	SO14 tape
TS5070FN	Universal programmable COMBO II	PLCC28
TS7514CFN	Monochip FSK modem, V.23, DTMF	PLCC28
TS7542CFN	Monochip analog front end	PLCC44

### NPN HIGH FREQUENCY TRANSISTORS - SOT 23

Type	V <sub>CBO</sub> (V)	V <sub>CEO</sub> (V)	I <sub>C</sub> (mA)	P <sub>tot</sub> (mW)	h <sub>FE</sub> @ I <sub>C</sub> / V <sub>CE</sub>		V <sub>CE(sat)</sub> @ I <sub>C</sub> / I <sub>B</sub>			f <sub>T</sub> typ min * (MHz)	Marking		
					min/max	(mA)	(V)	V <sub>CE(sat)</sub> max (mV)	I <sub>C</sub> (mA)		I <sub>B</sub> (mA)	Standard	Reverse
BFR 92	20	15	25	250	25/---	14	10	---	---	---	5000	P1	P4
BFR 92A	20	15	25	250	40/---	14	10	---	---	---	5000	P2	P5
BFR 93	15	12	35	250	25/---	30	5	---	---	---	5000	R1	R4
BFR 93A	15	12	35	250	40/---	30	5	---	---	---	5000	R2	R5
BFS 17	25	15	25	250	20/---	25	1	---	---	---	1300 *	E1	E4
BFS 18	30	20	30	250	35/125	1	10	---	---	---	250	F1	F4
BFS 19	30	20	30	250	65/225	1	10	---	---	---	300	F2	F5
BFS 20	30	20	25	250	40/---	7	10	---	---	---	550	G1	G4
SO 918	30	15	50	250	20/---	3	1	400	10	1	600 *	N10	O10

## DATA PROCESSING

### MEMORIES

Type	Description	Package
ST27C256-20FN	32 K × 8, 200 ns access time, consumption 30/1 mA/100 μA, CMOS OTP ROM	PLCC32
ST27C256-25FN	32 K × 8, 250 ns access time, consumption 30/1 mA/100 μA, CMOS OTP ROM	PLCC32
TS27C64A-20FN	8 K × 8, 200 ns access time, consumption 30/1 mA/100 μA, CMOS OTP ROM	PLCC32
TS27C64A-25FN	8 K × 8, 250 ns access time, consumption 30/1 mA/100 μA, CMOS OTP ROM	PLCC32
M9306	16 × 16, clock frequency 250 kHz, NMOS EEPROM	SO8
M9346	64 × 16, clock frequency 250 kHz, NMOS EEPROM	SO14