

KA5x0165Rxx-SERIES

KA5H0165R/RN, KA5M0165R/RN/RI, KA5L0165R/RN/RI

KA5H0165RV/RVN, KA5M0165RV/RVN, KA5L0165RV/RVN




Fairchild Power Switch(SPS)

Features

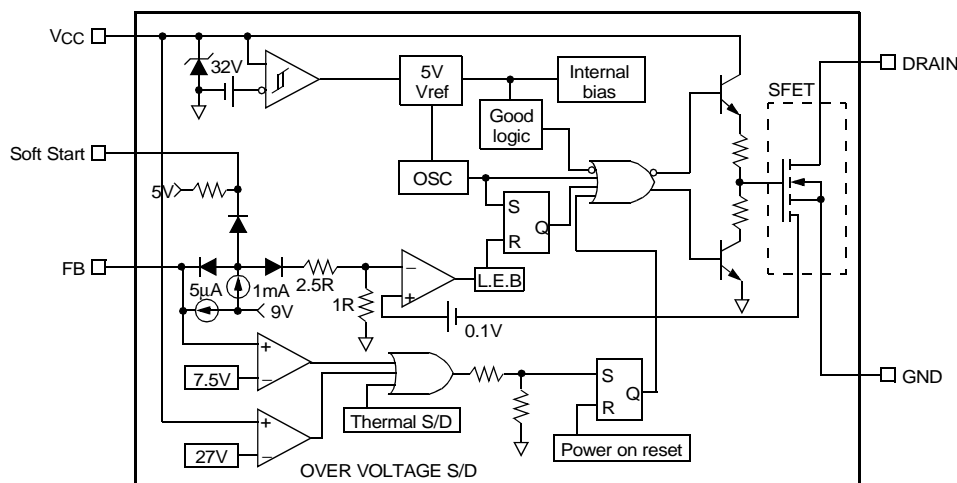
- Precision fixed operating frequency (100 / 67 / 50KHz)
- Pulse by pulse current limiting
- Over current protection
- Over voltage protection (Min. 25V) -KA5x0165R/RN/RI
- Internal thermal shutdown function
- Under voltage lockout
- Internal high voltage sense FET
- Auto-restart mode

Description

The SPS product family is specially designed for an off-line SMPS with minimal external components. The SPS consist of high voltage power SenseFET and current mode PWM IC. Included PWM controller features integrated fixed oscillator, under voltage lock out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shut down protection, over voltage protection, and temperature compensated precision current sources for loop compensation and fault protection circuitry. Compared to discrete MOSFET and controller or RCC switching converter solution, a SPS can reduce total component count, design size, weight and at the same time increase efficiency, productivity, and system reliability. It has a basic platform well suited for cost-effective design in either a flyback converter or a forward converter.

TO-220F-4L	8-DIP	I2-PAK-5L
		
1. GND 2. Drain 3. Vcc 4. FB	1.6.7.8. Drain 2. GND 3. Vcc 4. FB 5. NC	1. GND 2. Drain 3. Vcc 4. FB 5. S/S

Internal Block Diagram



Absolute Maximum Ratings

(Ta=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
Maximum Drain Voltage	V _{D,MAX}	650	V
Drain-Gate Voltage (R _{GS} =1MΩ)	V _{DGR}	650	V
Gate-Source (GND) Voltage	V _{GS}	±30	V
Drain Current Pulsed ⁽¹⁾	I _{DM}	4.0	ADC
Continuous Drain Current (T _C =25°C)	I _D	1.0	ADC
Continuous Drain Current (T _C =100°C)	I _D	0.7	ADC
Single Pulsed Avalanche Energy ⁽²⁾	E _{AS}	95	mJ
Maximum Supply Voltage	V _{CC,MAX}	30	V
Analog Input Voltage Range	V _{FB}	−0.3 to V _{SD}	V
Total Power Dissipation	P _D	40	W
	Derating	0.32	W/°C
Operating Junction Temperature.	T _J	+160	°C
Operating Ambient Temperature.	T _A	−25 to +85	°C
Storage Temperature Range.	T _{STG}	−55 to +150	°C

Notes:

1. Repetitive rating: Pulse width limited by maximum junction temperature
2. L=24mH, starting T_J=25°C

Electrical Characteristics (SFET part)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0V, I _D =50μA	650	-	-	V
Zero gate voltage drain current	I _{DSS}	V _{DS} =Max. Rating, V _{GS} =0V	-	-	50	μA
		V _{DS} =0.8Max. Rating, V _{GS} =0V, T _C =125°C	-	-	200	μA
Static drain-source on resistance ^(note)	R _{DS(ON)}	V _{GS} =10V, I _D =0.5A	-	8	10	Ω
Forward transconductance ^(note)	g _{fs}	V _{DS} =50V, I _D =0.5A	0.5	-	-	S
Input capacitance	C _{iss}	V _{GS} =0V, V _{DS} =25V, f=1MHz	-	250	-	pF
Output capacitance	C _{oss}		-	25	-	
Reverse transfer capacitance	C _{rss}		-	10	-	
Turn on delay time	t _{d(on)}	V _{DD} =0.5BV _{DSS} , I _D =1.0A (MOSFET switching time are essentially independent of operating temperature)	-	12	-	nS
Rise time	t _r		-	4	-	
Turn off delay time	t _{d(off)}		-	30	-	
Fall time	t _f		-	10	-	
Total gate charge (gate-source+gate-drain)	Q _g	V _{GS} =10V, I _D =1.0A, V _{DS} =0.5BV _{DSS} (MOSFET switching time are essentially independent of operating temperature)	-	-	21	nC
Gate-source charge	Q _{gs}		-	3	-	
Gate-drain (Miller) charge	Q _{gd}		-	9	-	

Note:

Pulse test: Pulse width ≤ 300μS, duty cycle ≤ 2%

$$S = \frac{1}{R}$$

Electrical Characteristics (CONTROL part)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
UVLO SECTION						
Start Threshold Voltage	VSTART	VFB=GND	14	15	16	V
Stop Threshold Voltage	VSTOP	VFB=GND	8.4	9	9.6	V
OSCILLATOR SECTION						
Initial accuracy	FOSC	KA5H0165Rxx	90	100	110	kHz
Initial accuracy	FOSC	KA5M0165Rxx	61	67	73	kHz
Initial accuracy	FOSC	KA5L0165Rxx	45	50	55	kHz
Frequency change with temperature ⁽²⁾	ΔF/ΔT	−25°C≤Ta≤+85°C	-	±5	±10	%
Maximum duty cycle	Dmax	KA5H0165Rxx	62	67	72	%
Maximum duty cycle	Dmax	KA5M0165Rxx KA5L0165Rxx	72	77	82	%
FEEDBACK SECTION						
Feedback source current	IFB	Ta=25°C, 0V≤Vfb≤3V	0.7	0.9	1.1	mA
Shutdown feedback voltage	VSD	Vfb≥6.5V	6.9	7.5	8.1	V
Shutdown delay current	Idelay	Ta=25°C, 3V≤Vfb≤VSD	4	5	6	μA
SOFT START SECTION						
Soft Start Voltage	VSS	KA5x0165RI	4.7	5.0	5.3	V
Soft Start Current	ISS		0.8	1.0	1.2	mA
REFERENCE SECTION						
Output voltage ⁽¹⁾	Vref	Ta=25°C	4.80	5.00	5.20	V
Temperature Stability ⁽¹⁾⁽²⁾	Vref/ΔT	−25°C≤Ta≤+85°C	-	0.3	0.6	mV/°C
CURRENT LIMIT(SELF-PROTECTION)SECTION						
Peak Current Limit	I _{OVER}	Max. inductor current	0.53	0.6	0.67	A
PROTECTION SECTION						
Thermal shutdown temperature (Tj) ⁽¹⁾	TSD	-	140	160	-	°C
Over voltage protection	VOVP	KA5x0165R/RN/RI	25	27	29	V
TOTAL STANDBY CURRENT SECTION						
Start Up Current	ISTART	VCC=14V	-	100	170	μA
Operating supply current (control part only)	IOP	VCC≤28	-	7	12	mA

Notes:

1. These parameters, although guaranteed, are not 100% tested in production
2. These parameters, although guaranteed, are tested in EDS (wafer test) process

Typical Performance Characteristics

(These characteristic graphs are normalized at $T_a=25^{\circ}\text{C}$)

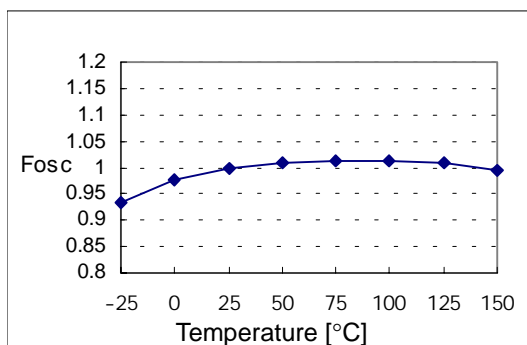


Figure 1. Operating Frequency

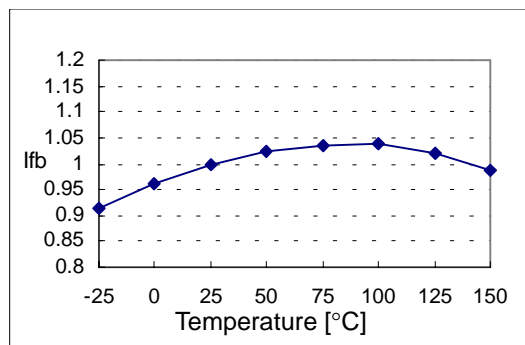


Figure 2. Feedback Source Current

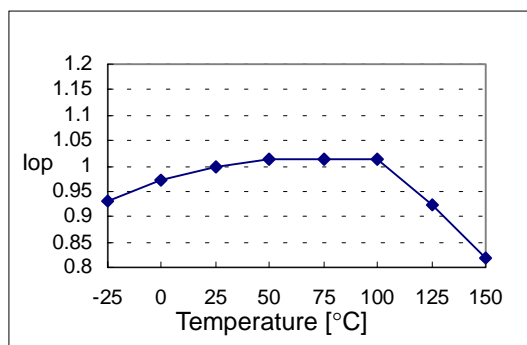


Figure 3. Operating Supply Current

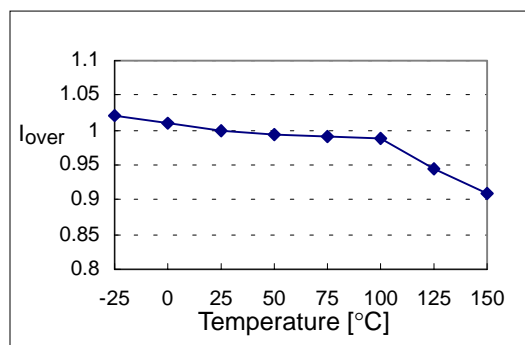


Figure 4. Peak Current Limit

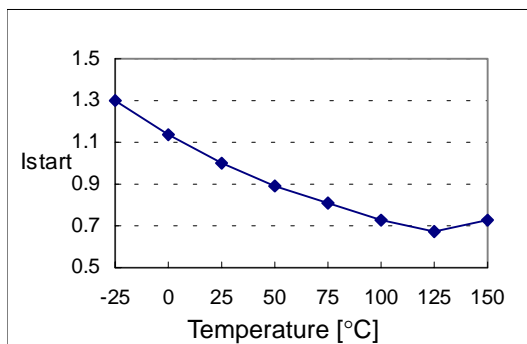


Figure 5. Start up Current

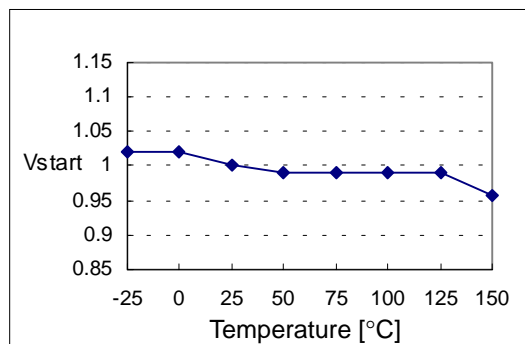


Figure 6. Start Threshold Voltage

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at $T_a=25^{\circ}\text{C}$)

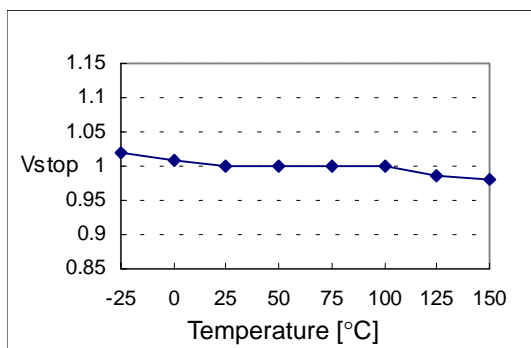


Figure 7. Stop Threshold Voltage

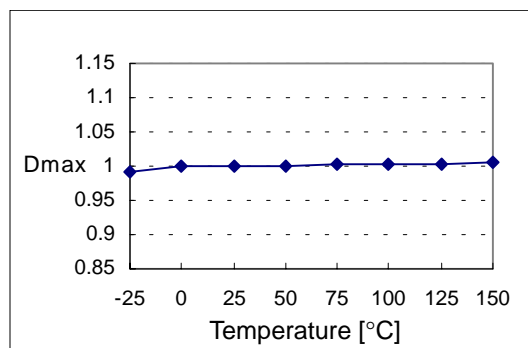


Figure 8. Maximum Duty Cycle

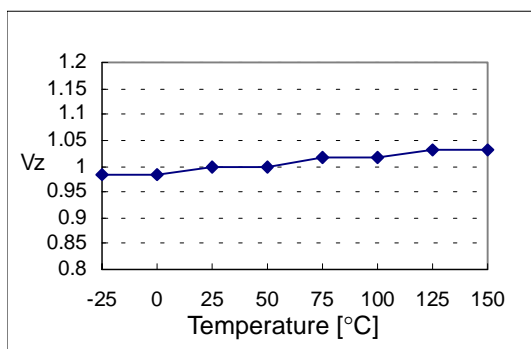


Figure 9. VCC Zener Voltage

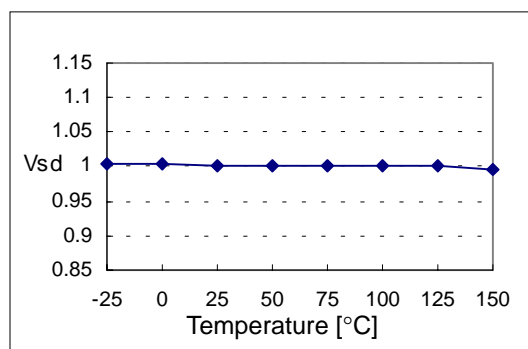


Figure 10. Shutdown Feedback Voltage

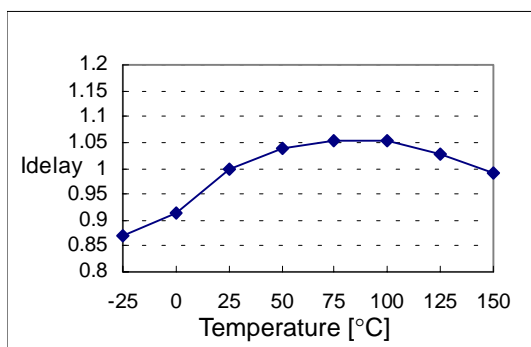


Figure 11. Shutdown Delay Current

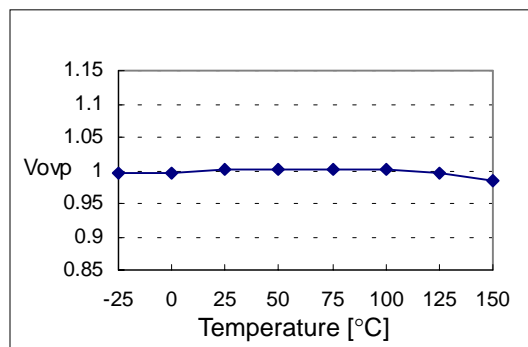


Figure 12. Over Voltage Protection

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at Ta=25°C)

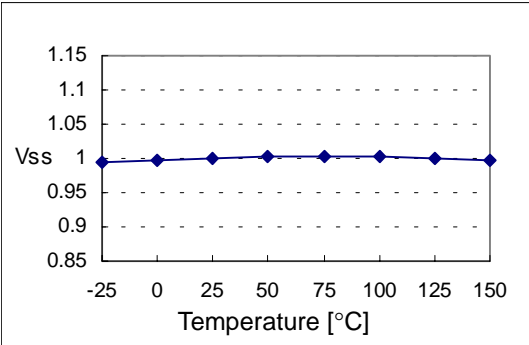


Figure13. Soft Start Voltage

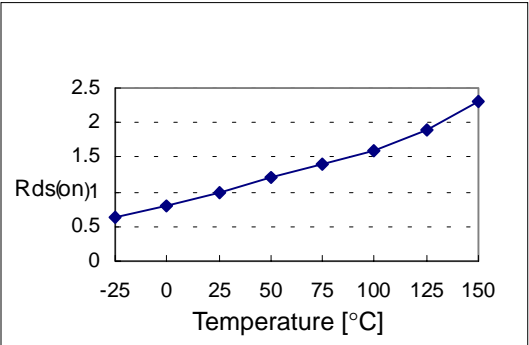
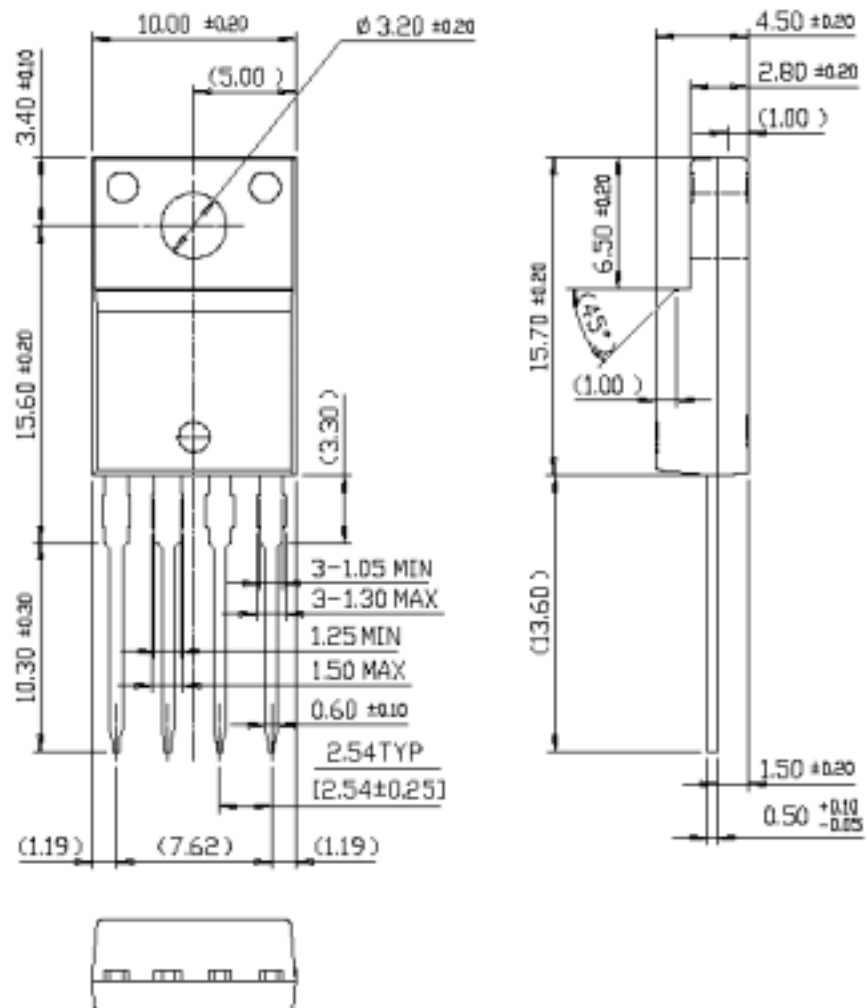


Figure 14. Static Drain-Source on Resistance

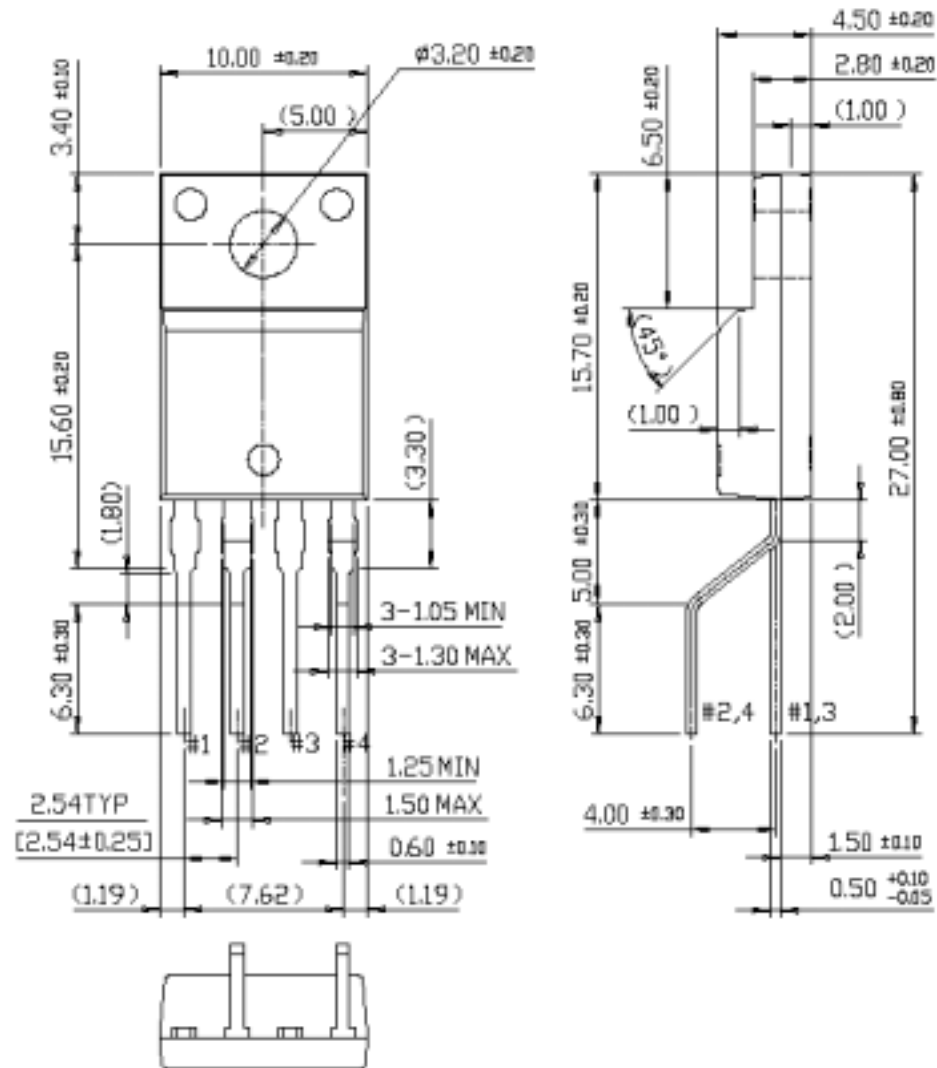
Package Dimensions

TO-220F-4L



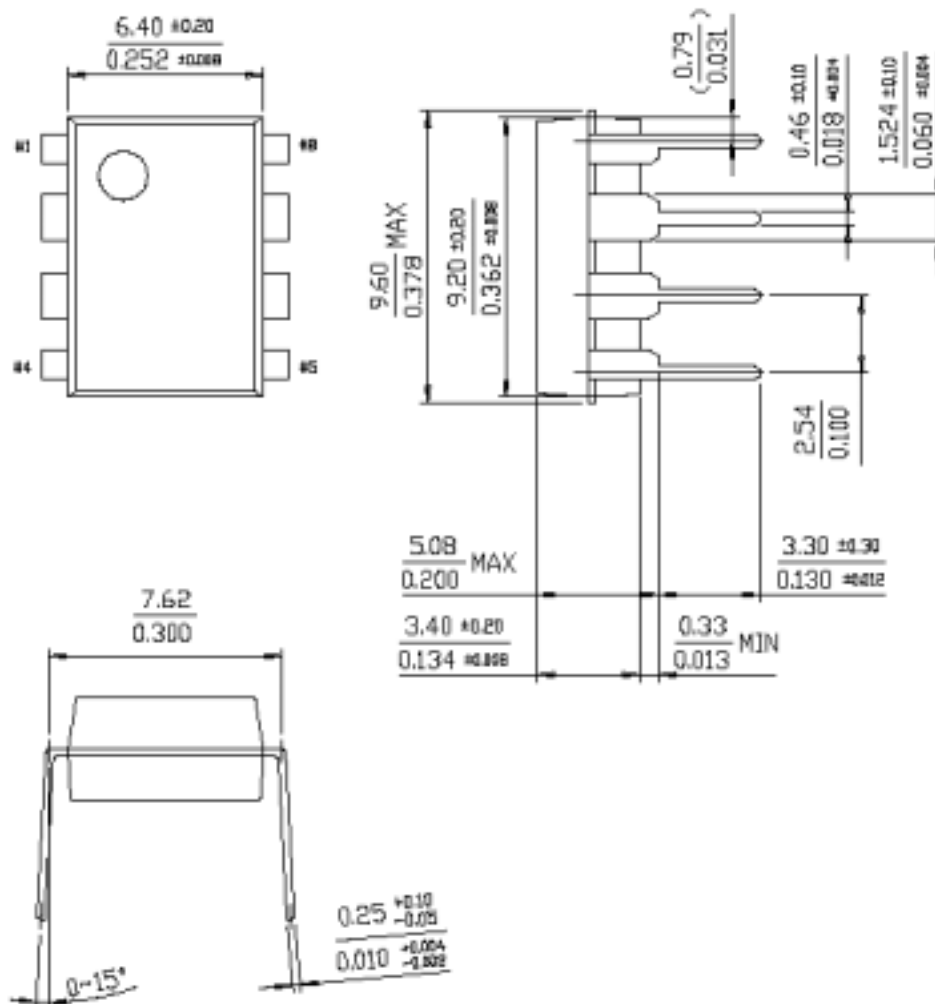
Package Dimensions (Continued)

TO-220F-4L (Forming)



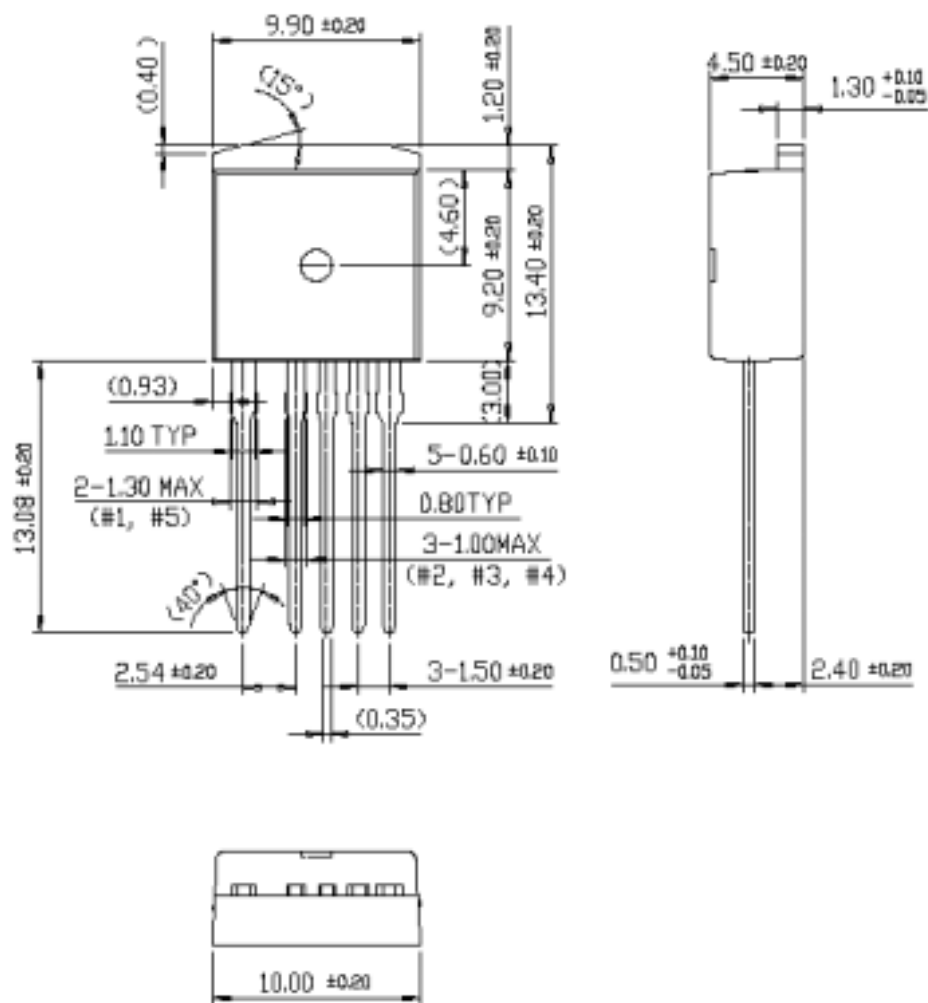
Package Dimensions (Continued)

8-DIP



Package Dimensions (Continued)

I2-PAK-5L



The drawing shows three views of the connector:

- Front View (Top Left):** Shows a rectangular body with a width of 9.90 ± 0.20 mm and a height of 1.20 ± 0.20 mm. A chamfer with a 45° angle is on the top left. A central circular feature has a diameter of $\varnothing 4.60$ mm. The bottom section has a height of 9.20 ± 0.20 mm.
- Side View (Top Right):** Shows the profile of the connector. The top width is 4.50 ± 0.20 mm. A small step has a width of $1.30^{+0.10}_{-0.05}$ mm. The main body height is 5.30 ± 0.50 mm. The bottom section has a height of 5.00 ± 0.50 mm. The total height is 20.70 ± 0.30 mm. A feature on the side has a width of 3.18 ± 0.30 mm. The bottom width is 2.40 ± 0.20 mm. A small gap at the bottom is $0.50^{+0.10}_{-0.05}$ mm.
- Top View (Bottom):** Shows a rectangular footprint with a width of 10.00 ± 0.20 mm. It features two pins and two mounting tabs.

Pin Details:

- Pin #1:** Diameter 1.10 TYP, length $2-1.30$ MAX, position $(\#1, \#5)$.
- Pin #2:** Diameter 0.80 TYP, length $3-1.00$ MAX, position $(\#2, \#3, \#4)$.
- Pin #3:** Diameter 0.80 TYP, length $3-1.00$ MAX, position $(\#2, \#3, \#4)$.
- Pin #4:** Diameter 0.80 TYP, length $3-1.00$ MAX, position $(\#2, \#3, \#4)$.
- Pin #5:** Diameter 0.80 TYP, length $3-1.00$ MAX, position $(\#2, \#3, \#4)$.

Other Dimensions:

- Top left chamfer: 45°
- Top left corner: (0.40)
- Top left hole: (0.93)
- Top left hole: 1.10 TYP
- Top left hole: $2-1.30$ MAX
- Top left hole: $(\#1, \#5)$
- Top left hole: 0.80 TYP
- Top left hole: $3-1.00$ MAX
- Top left hole: $(\#2, \#3, \#4)$
- Top left hole: 10.30 ± 0.30
- Top left hole: $5-0.60 \pm 0.10$
- Top left hole: 3.18 ± 0.30
- Top left hole: $0.50^{+0.10}_{-0.05}$
- Top left hole: 2.40 ± 0.20
- Top left hole: 2.54 TYP
- Top left hole: $[2.54 \pm 0.20]$
- Top left hole: $3-1.50$ TYP
- Top left hole: $[3-1.50 \pm 0.20]$
- Top left hole: 3.50
- Top left hole: $(\#1, \#3, \#5)$
- Top left hole: $(\#2, \#4)$
- Top left hole: 5.00 ± 0.50
- Top left hole: 5.30 ± 0.50
- Top left hole: 20.70 ± 0.30
- Top left hole: $1.30^{+0.10}_{-0.05}$
- Top left hole: 4.50 ± 0.20
- Top left hole: 9.20 ± 0.20
- Top left hole: 9.90 ± 0.20
- Top left hole: 1.20 ± 0.20
- Top left hole: 10.00 ± 0.20

Ordering Information

Product Number	Package	Marking Code	BVDSS	FOSC	RDS(on)
KA5H0165R-TU	TO-220F-4L	5H0165R	650V	100kHz	8Ω
KA5H0165R-YDTU	TO-220F-4L(Forming)				
KA5M0165R-TU	TO-220F-4L	5M0165R	650V	67kHz	8Ω
KA5M0165R-YDTU	TO-220F-4L(Forming)				
KA5L0165R-TU	TO-220F-4L	5L0165R	650V	50kHz	8Ω
KA5L0165R-YDTU	TO-220F-4L(Forming)				
KA5H0165RN	8-DIP	5H0165R	650V	100kHz	8Ω
KA5M0165RN	8-DIP	5M0165R	650V	67kHz	8Ω
KA5L0165RN	8-DIP	5L0165R	650V	50kHz	8Ω
KA5M0165RI-TU	I ² -PAK-5L	5M0165RI	650V	67kHz	8Ω
KA5M0165RI-YDTU	I ² -PAK-5L(Forming)				
KA5L0165RI-TU	I ² -PAK-5L	5L0165RI	650V	50kHz	8Ω
KA5L0165RI-YDTU	I ² -PAK-5L(Forming)				

Product Number	Package	Marking Code	BVDSS	FOSC	RDS(on)
KA5H0165RV-TU	TO-220F-4L	5H0165RV	650V	100kHz	8Ω
KA5H0165RV-YDTU	TO-220F-4L(Forming)				
KA5M0165RV-TU	TO-220F-4L	5M0165RV	650V	67kHz	8Ω
KA5M0165RV-YDTU	TO-220F-4L(Forming)				
KA5L0165RV-TU	TO-220F-4L	5L0165RV	650V	50kHz	8Ω
KA5L0165RV-YDTU	TO-220F-4L(Forming)				
KA5H0165RVN	8-DIP	5H0165RV	650V	100kHz	8Ω
KA5M0165RVN	8-DIP	5M0165RV	650V	67kHz	8Ω
KA5L0165RVN	8-DIP	5L0165RV	650V	50kHz	8Ω

TU : Non Forming Type

YDTU : Forming Type

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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.