The RF MOSFET Line

RF Power Field Effect Transistors N-Channel Enhancement-Mode Lateral MOSFETs

Designed for PCN and PCS base station applications with frequencies from 2.0 to 2.2 GHz. Suitable for FM, TDMA, CDMA and multicarrier amplifier applications. To be used in Class AB for PCN-PCS/cellular radio and WLL applications.

- Wideband CDMA Performance: -45 dB ACPR @ 4.096 MHz, 28 Volts
 Output Power 3.5 Watts
 Power Gain 14 dB
 Efficiency 15%
- High Gain, High Efficiency and High Linearity
- Integrated ESD Protection
- Designed for Maximum Gain and Insertion Phase Flatness
- Capable of Handling 10:1 VSWR, @ 28 Vdc, 2.11 GHz, 30 Watts CW Output Power
- Excellent Thermal Stability
- Characterized with Series Equivalent Large-Signal Impedance Parameters
- Low Gold Plating Thickness on Leads, 40μ" Nominal.
- In Tape and Reel. R3 Suffix = 250 Units per 32 mm, 13 Inch Reel.

MRF21030LR3 MRF21030LSR3

2.2 GHz, 30 W, 28 V LATERAL N-CHANNEL RF POWER MOSFETs



CASE 465E-04, STYLE 1 NI-400 MRF21030LR3



CASE 465F-04, STYLE 1 NI-400S MRF21030LSR3

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	65	Vdc
Gate-Source Voltage	V_{GS}	-0.5, +15	Vdc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	83.3 0.48	Watts W/°C
Storage Temperature Range	T _{stg}	- 65 to +150	°C
Operating Junction Temperature	TJ	200	°C

ESD PROTECTION CHARACTERISTICS

Test Conditions	Class
Human Body Model	2 (Minimum)
Machine Model	M3 (Minimum)

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case		2.1	°C/W

NOTE - **CAUTION** - MOS devices are susceptible to damage from electrostatic charge. Reasonable precautions in handling and packaging MOS devices should be observed.

Rev. 10





ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	1		1	•	•
Drain-Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D = 20 μA)	V _{(BR)DSS}	65	_	_	Vdc
Zero Gate Voltage Drain Current (V _{DS} = 28 Vdc, V _{GS} = 0 Vdc)	I _{DSS}		_	1	μAdc
Gate-Source Leakage Current (V _{GS} = 5 Vdc, V _{DS} = 0 Vdc)	I _{GSS}	_	_	1	μAdc
N CHARACTERISTICS	1		u.		
Gate Threshold Voltage ($V_{DS} = 10 \text{ Vdc}, I_D = 100 \mu \text{Adc}$)	V _{GS(th)}	2	3	4	Vdc
Gate Quiescent Voltage (V _{DS} = 28 Vdc, I _D = 250 mA)	V _{GS(Q)}	2	3.3	4.5	Vdc
Drain-Source On-Voltage (V _{GS} = 10 Vdc, I _D = 1 Adc)	V _{DS(on)}	_	0.29	0.4	Vdc
Forward Transconductance (V _{DS} = 10 Vdc, I _D = 1 Adc)	9 _{fs}	_	2	_	S
YNAMIC CHARACTERISTICS	1		u.		l
Input Capacitance (Including Input Matching Capacitor in Package) (1) $(V_{DS}=28\ V_{GS}=0,f=1\ MHz)$	C _{iss}		98.5	_	pF
Output Capacitance (1) (V _{DS} = 28 Vdc, V _{GS} = 0, f = 1 MHz)	C _{oss}	_	37	_	pF
Reverse Transfer Capacitance (V _{DS} = 28 Vdc, V _{GS} = 0, f = 1 MHz)	C _{rss}	_	1.3	_	pF
UNCTIONAL TESTS (In Motorola Test Fixture, 50 ohm system)	1		u.		
Two-Tone Common-Source Amplifier Power Gain $(V_{DD}=28\ Vdc,\ P_{out}=30\ W\ PEP,\ I_{DQ}=250\ mA,\ f1=2140.0\ MHz,\ f2=2140.1\ MHz)$	G _{ps}	_	13	_	dB
Two-Tone Drain Efficiency $(V_{DD}=28\ Vdc,\ P_{out}=30\ W\ PEP,\ I_{DQ}=250\ mA, f1=2140.0\ MHz,\ f2=2140.1\ MHz)$	η	_	33	_	%
3rd Order Intermodulation Distortion $(V_{DD} = 28 \text{ Vdc}, P_{out} = 30 \text{ W PEP}, I_{DQ} = 250 \text{ mA}, f1 = 2140.0 \text{ MHz}, f2 = 2140.1 \text{ MHz})$	IMD	_	-30	_	dBc
Input Return Loss $(V_{DD} = 28 \text{ Vdc}, P_{out} = 30 \text{ W PEP}, I_{DQ} = 250 \text{ mA}, f1 = 2140.0 \text{ MHz}, f2 = 2140.1 \text{ MHz})$	IRL	_	-13	_	dB
Two-Tone Common-Source Amplifier Power Gain $(V_{DD}=28~Vdc,~P_{out}=30~W~PEP,~I_{DQ}=250~mA,~f1=2110.0~MHz,~f2=2110.1~MHz~and~f1=2170.0~MHz,~f2=2170.1~MHz)$	G _{ps}	12	13	_	dB
Two-Tone Drain Efficiency $(V_{DD}=28\ Vdc,\ P_{out}=30\ W\ PEP,\ I_{DQ}=250\ mA,\ f1=2110.0\ MHz,\ f2=2110.1\ MHz\ and\ f1=2170.0\ MHz,\ f2=2170.1\ MHz)$	η	31	33	_	%
3rd Order Intermodulation Distortion $(V_{DD}=28~Vdc,~P_{out}=30~W~PEP,~I_{DQ}=250~mA,~f1=2110.0~MHz,~f2=2110.1~MHz~and~f1=2170.0~MHz,~f2=2170.1~MHz)$	IMD	_	-30	-27.5	dBc
Input Return Loss $(V_{DD}=28~Vdc,~P_{out}=30~W~PEP,~I_{DQ}=250~mA,~f1=2110.0~MHz,~f2=2110.1~MHz~and~f1=2170.0~MHz,~f2=2170.1~MHz)$	IRL		-13	-9	dB
Output Mismatch Stress $(V_{DD}=28\ Vdc,\ P_{out}=30\ W\ CW,\ I_{DQ}=250\ mA,\\f=2110\ MHz,\ VSWR=10:1,\ All\ Phase\ Angles\ at\ Frequency\ of\ Tests)$	Ψ	No Degradation In Output Power Before and After Test			

⁽¹⁾ Part is internally matched both on input and output.

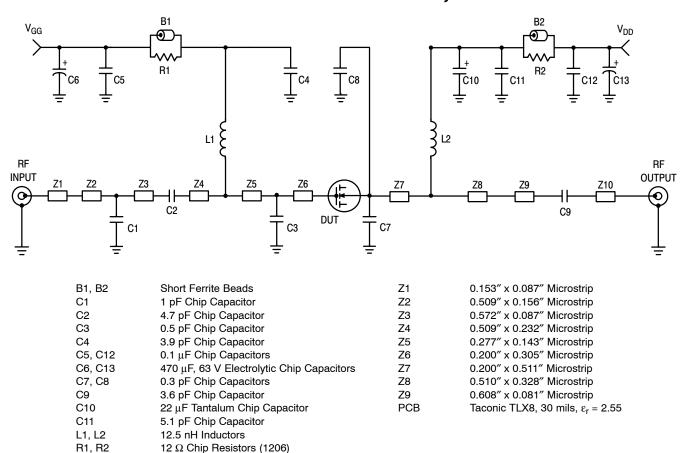


Figure 1. MRF21030LR3(LSR3) Test Circuit Schematic

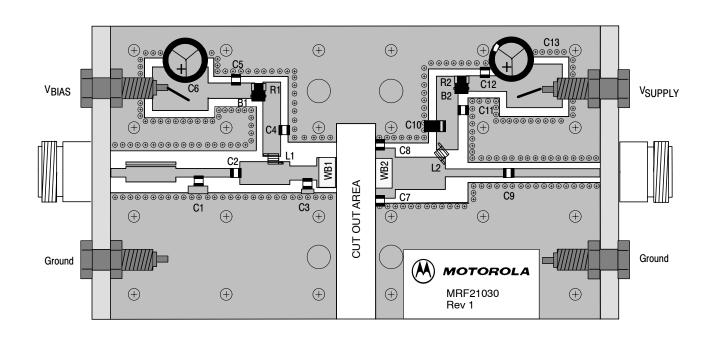
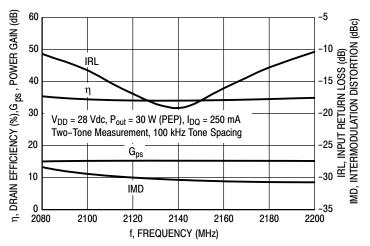


Figure 2. MRF21030LR3(LSR3) Test Circuit Component Layout

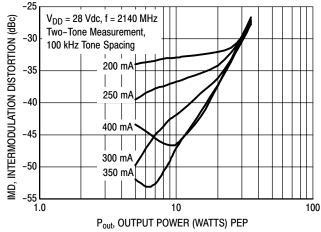
TYPICAL CHARACTERISTICS



8 30 -20 , POWER GAIN (c 원 양 상 상 상 ADJACENT CHANNEL POWER RATIO (dB) $V_{DD} = 28 \text{ Vdc}, I_{DQ} = 250 \text{ mA}, f = 2140 \text{ MHz}$ Channel Spacing (Channel Bandwidth): 4.096 MHz (5 MHz) η, DRAIN EFFICIENCY (%),G_{ps.}, F **ACPR** -70 0 2 3 5 6 Pout, OUTPUT POWER (WATTS Avg.) CDMA

Figure 3. Class AB Broadband Circuit Performance

Figure 4. CDMA ACPR, Power Gain and Drain Efficiency versus Output Power



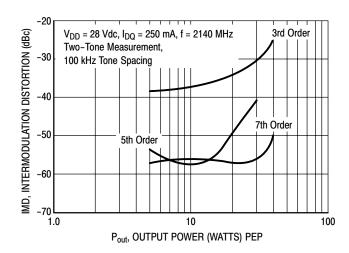
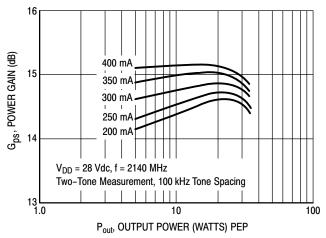


Figure 5. Intermodulation Distortion versus Output Power

Figure 6. Intermodulation Distortion Products versus Output Power



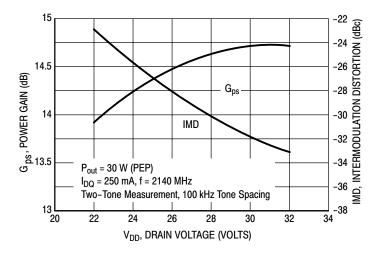
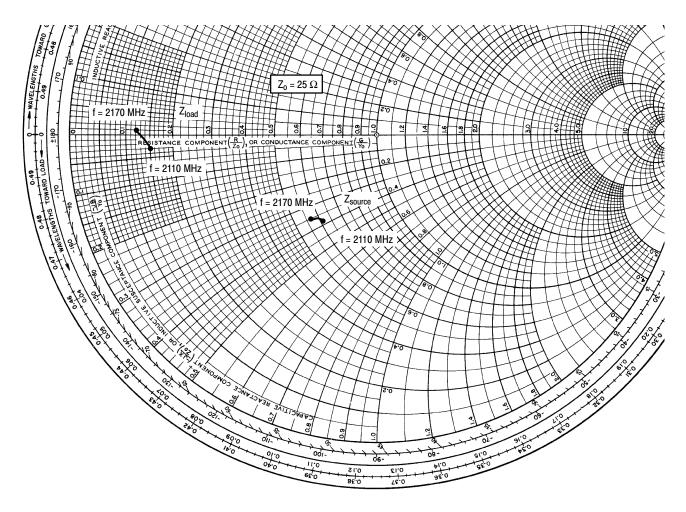


Figure 7. Power Gain versus Output Power

Figure 8. Power Gain and Intermodulation Distortion versus Supply Voltage



 V_{DD} = 28 V, I_{DQ} = 250 mA, P_{out} = 30 W PEP

f MHz	$\mathbf{Z_{source}}_{\Omega}$	$\mathbf{Z_{load}}_{\Omega}$
2110	15.3 - j9.4	3.7 - j0.78
2140	14.6 - j9.4	3.4 - j0.37
2170	14.3 - j8.8	3.0 + j0.13

 Z_{source} = Test circuit impedance as measured from gate to ground.

Z_{load} = Test circuit impedance as measured from drain to ground.

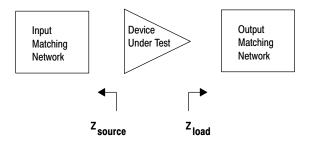
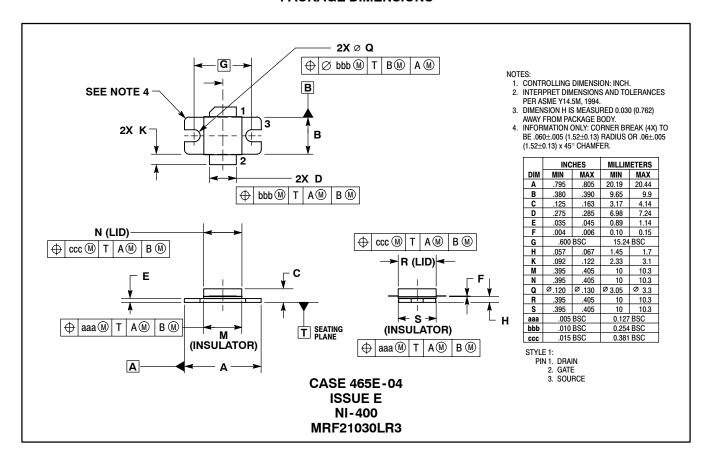
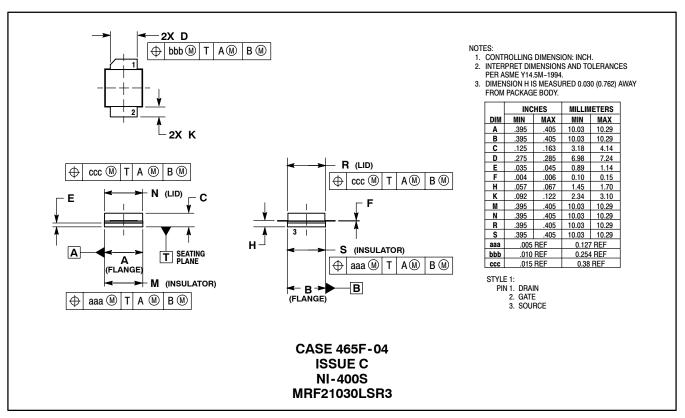


Figure 9. Series Equivalent Source and Load Impedance

PACKAGE DIMENSIONS





Information in this document is provided solely to enable system and software implementers to use Motorola products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Motorola data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part.

MOTOROLA and the Stylized M Logo are registered in the US Patent and Trademark Office. All other product or service names are the property of their respective owners. Motorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

© Motorola Inc. 2004

HOW TO REACH US:

USA/EUROPE/LOCATIONS NOT LISTED: Motorola Literature Distribution P.O. Box 5405, Denver, Colorado 80217 1-800-521-6274 or 480-768-2130 JAPAN: Motorola Japan Ltd.; SPS, Technical Information Center, 3-20-1, Minami-Azabu, Minato-ku, Tokyo 106-8573, Japan 81-3-3440-3569

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; Silicon Harbour Centre, 2 Dai King Street, Tai Po Industrial Estate, Tai Po, N.T., Hong Kong 852-26688334

HOME PAGE: http://motorola.com/semiconductors

