Freescale Semiconductor Technical Data

200 kPa On-Chip Temperature Compensated and Calibrated Silicon Pressure Sensors

The MPMZ2202 device is a silicon piezoresistive pressure sensor providing a highly accurate and linear voltage output directly proportional to the applied pressure. The sensor is a single, monolithic silicon diaphragm with the strain gauge and a thin-film resistor network integrated on-chip. The chip is laser trimmed for precise span and offset calibration and temperature compensation.

Features

- Temperature Compensated Over 0°C to + 85°C
- Available in Easy-to-Use Tape and Reel
- Ratiometric to Supply Voltage
- Gauge Ported and Non Ported Options
- Increased media compatibility fluorocarbon gel

Typical Applications

- Pump/Motor Controllers
- Robotics
- Level Indicators
- Medical Diagnostics
- Pressure Switching
- Barometers
- Altimeters

ORDERING INFORMATION						
Device Type	Options	Case No.	MPX Series Order No.	Packing Options	Device Marking	
Ported	Gauge, Axial Port	1320A	MPMZ2202GS	Rails	MPMZ2202GS	
	Gauge, Axial Port	1320A	MPMZ2202GST1	Tape & Reel	MPMZ2202GS	



COMPENSATED AND CALIBRATED PRESSURE SENSOR 0 TO 200 kPA (0 TO 29 psi) 40 mV FULL SCALE SPAN (TYPICAL)



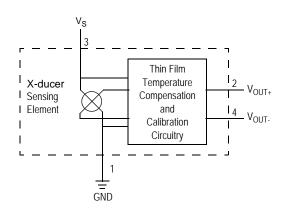
PIN NUMBER					
1	GND	3	VS		
2	+V _{OUT}	4	-V _{OUT}		

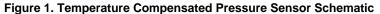


MPMZ2202 Rev 1, 09/2006

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Figure 1 shows a block diagram of the internal circuitry on the stand-alone pressure sensor chip.





VOLTAGE OUTPUT VERSUS APPLIED DIFFERENTIAL PRESSURE

The differential voltage output of the sensor is directly proportional to the differential pressure applied.

The output voltage of the differential or gauge sensor increases with increasing pressure applied to the pressure side relative to the vacuum side. Similarly, output voltage increases as increasing vacuum is applied to the vacuum side relative to the pressure side.

Table 1. Maximum Ratings⁽¹⁾

Rating	Symbol	Value	Unit
Maximum Pressure	P _{max}	400	kPa
Storage Temperature	T _{stg}	-40 to +125	°C
Operating Temperature	T _A	-40 to +125	°C

1. Exposure beyond the specified limits may cause permanent damage or degradation to the device.

C	Characteristic	Symbol	Min	Тур	Max	Unit
Pressure Range ⁽¹⁾		P _{OP}	0	_	200	kPa
Supply Voltage ⁽²⁾		Vs	_	10	16	Vdc
Supply Current		Ι _Ο	_	6.0	—	mAdc
Full Scale Span ⁽³⁾		V _{FSS}	38.5	40	41.5	mV
Offset ⁽⁴⁾	MPMZ2202D/G Series	V _{OFF}	-1.0	—	1.0	mV
Sensitivity		ΔV/ΔΡ	_	0.2	_	mV/kPa
Linearity ⁽⁵⁾	MPMZ2202D/G Series	_	-0.6	_	0.4	%V _{FSS}
Pressure Hysteresis ⁽⁵⁾ (0 to 100 kPa)		_	_	±0.1	_	%V _{FSS}
Temperature Hysteresis ⁽⁵⁾ (-40°C to +125°C)		_	_	±0.5	_	%V _{FSS}
Temperature Effect on Full Sca	ale Span ⁽⁵⁾	TCV _{FSS}	-2.0	_	2.0	%V _{FSS}
Temperature Effect on Offset ⁽⁵	i)	TCV _{OFF}	-1.0	_	1.0	mV
Input Impedance		Z _{IN}	1000	—	2500	Ω
Output Impedance		Z _{OUT}	1400	_	3000	Ω
Response Time ⁽⁶⁾ (10% to 90%	%)	t _R	_	1.0	_	ms
Warm-Up		_	_	20	_	ms
Offset Stability ⁽⁷⁾		_	_	±0.5	_	%V _{FSS}

Table 2. Operating Characteristics ($V_S = 10 \text{ Vdc}, T_A = 25^{\circ}\text{C.}$)

1. 1.0 kPa (kiloPascal) equals 0.145 psi.

2. Device is ratiometric within this specified excitation range. Operating the device above the specified excitation range may induce additional error due to device self-heating.

3. Full Scale Span (V_{FSS}) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.

- 4. Offset (V_{off}) is defined as the output voltage at the minimum rated pressure.
- 5. Accuracy (error budget) consists of the following:
 - Linearity: Output deviation from a straight line relationship with pressure, using end point method, over the specified pressure range.
 - Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the minimum or maximum rated pressure, at 25°C.
 - Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.
 TcSpan: Output deviation at full rated pressure over the temperature range of 0 to 85°C, relative to 25°C.

• TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85°C, relative to 25°C.

6. Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.

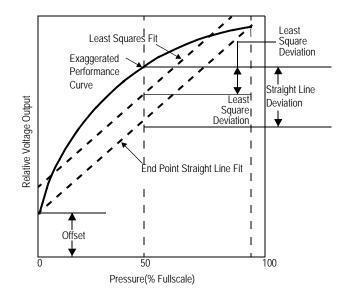
7. Offset stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.

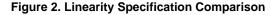
LINEARITY

Linearity refers to how well a transducer's output follows the equation: $V_{OUT} = V_{OFF}$ + sensitivity x P over the operating pressure range. There are two basic methods for calculating nonlinearity: (1) end point straight line fit (see Figure 2) or (2) a least squares best line fit. While a least squares fit gives the "best case" linearity error (lower numerical value), the calculations required are burdensome.

Conversely, an end point fit will give the "worst case" error (often more desirable in error budget calculations) and the calculations are more straightforward for the user. The specified pressure sensor linearities are based on the end point straight line method measured at the midrange pressure.

Operating characteristics, internal reliability and qualification tests are based on use of dry clean air as the pressure media. Media other than dry clean air may have adverse effects on sensor performance and long term reliability. Contact the factory for information regarding media compatibility in your application.





ON-CHIP TEMPERATURE COMPENSATION AND CALIBRATION

Figure 3 shows the minimum, maximum and typical output characteristics of the MPMZ2202 series at 25°C. The output

is directly proportional to the differential pressure and is essentially a straight line.

A gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the silicon diaphragm.

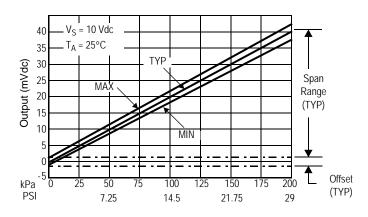
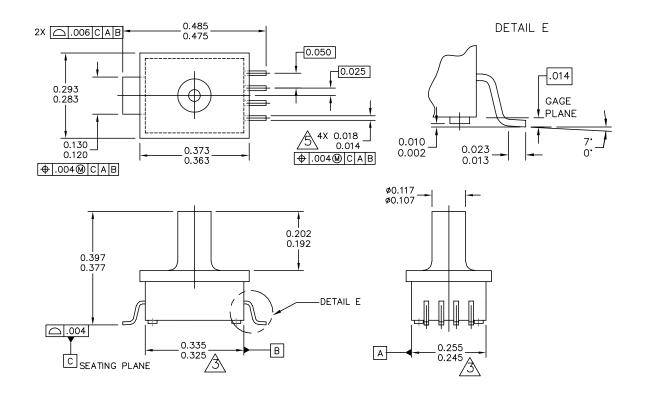


Figure 3. Output versus Pressure Differential



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5 LD M-PAC, PORTED		CASE NUMBER: 1320A-02 22 JUL 20		
	STANDARD: NO	N-JEDEC		

CASE 1320A-02 ISSUE A NOTES:

- 1. DIMENSIONS ARE IN INCHES.
- 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994.
- A. DIMENSIONS DOES NOT INCLUDE MOLD FLASH OR PROTRUSION. MOLD FLASH OR PROTRUSION SHALL NOT EXCEED .006" PER SIDE.
- 4. ALL VERTICAL SURFACES TO BE 5" MAXIMUM.
- 5. DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE .008 MAXIMUM.

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TITLE:	DOCUMENT N	DOCUMENT NO: 98ARH99087A	
5 LD M-PAC, POR	TED CASE NUMBE	CASE NUMBER: 1320A-02	
	STANDARD: N	ON-JEDEC	

CASE 1320A-02 ISSUE A

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